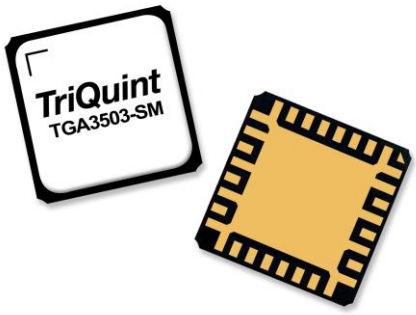
TGA3503-SM

2-30 GHz GaAs Wideband Gain Block

Applications

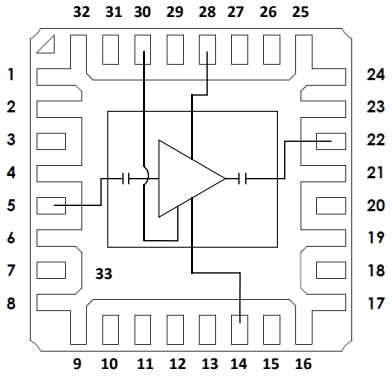
- General Purpose Wideband Gain Block
- Electronic Warfare
- Military & Commercial Radar
- Military Communications
- Commercial Communications
- Instrumentation



Product Features

- Frequency Range: 2 – 30 GHz
- Small Signal Gain: 10 dB typical
- Return Loss: 12 dB typical
- NF: 4.0 dB mid-band
- P1dB: 19 dBm, P_{SAT} = 19 dBm at P_{IN} = 10 dBm
- OTOI: > 25 dBm at Pout/tone = 8 dBm
- Bias: V^+ = 10.4 V, I_{DQ} = 135 mA, V_{CTRL} = 2.2 V, V_G = -1 V Typical
- Package Dimensions: 5 x 5 x 1.5 mm

Functional Block Diagram



General Description

TriQuint's TGA3503-SM is a wideband gain block with adjustable gain control giving the user extra flexibility to fine tune system performance. Operating from 2 to 30GHz, the TGA3503-SM provides 19dBm P1dB and 10dB of small-signal gain with return losses of greater than 12dB.

The TGA3503-SM is fabricated on TriQuint's production TQPHT15 0.15um GaAs pHEMT process and is offered in a robust, 5x5mm ceramic air-cavity QFN. With integrated DC blocking caps and fully match to 50ohms, the TGA3503-SM is easily integrated in both commercial and military system architectures.

Lead-free and RoHS compliant

Evaluation Boards are available upon request.

Pad Configuration

Pad No.	Symbol
1-4, 6-13, 15-21, 23-27, 29, 31-32	Gnd
5	RF_{IN}
14	V_G
22	RF_{OUT}
28	V^+
30	V_{CTRL} or V_{G2}
33	Gnd

Ordering Information

Part	ECCN	Description
TGA3503-SM	EAR99	2 – 30 GHz GaAs Wideband Gain Block

Absolute Maximum Ratings

Parameter	Value (1)
Bias Voltage (V^+)	10.4 V
Drain Voltage (V_D)	6 V (2)
Gate Voltage Range (V_G)	-3 to 0 V
Control Voltage Range (V_{CTRL})	-3 to 3 V
Drain Current (I_D)	150 mA
Gate Current (I_G)	-2 to 10 mA
Power Dissipation, 85 °C (P_{DISS})	1.4 W
Input Power, CW, 50 Ω, (P_{IN})	21 dBm
Channel temperature (T_{CH})	200 °C
Mounting Temperature (30 Seconds)	260 °C
Storage Temperature	-55 to 150 °C

Notes:

1. Operation of this device outside the parameter ranges given above may cause permanent damage. These are stress ratings only, and functional operation of the device at these conditions is not implied.
2. Assure $V_D - V_{CTRL} \leq 8$ V. Compute $V_D = V^+ - I_{DQ}*40$

Recommended Operating Conditions

Parameter	Value
Supply Voltage (V^+)	10.4 V
Drain Voltage (V_D)	5 V
Drain Current (I_{DQ})	135 mA
Gate Voltage (V_G)	-1 V Typical
Gain Control Voltage (V_{CTRL})	2.2 V
Temperature (T_{BASE})	-40 to 85 °C

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all operating conditions.

Electrical Specifications

Test conditions unless otherwise noted: 25 ^{0}C, V^+ = 10.4 V, I_{DQ} = 135 mA, V_{CTRL} = 2.2 V, V_G = -1 V Typical, CW

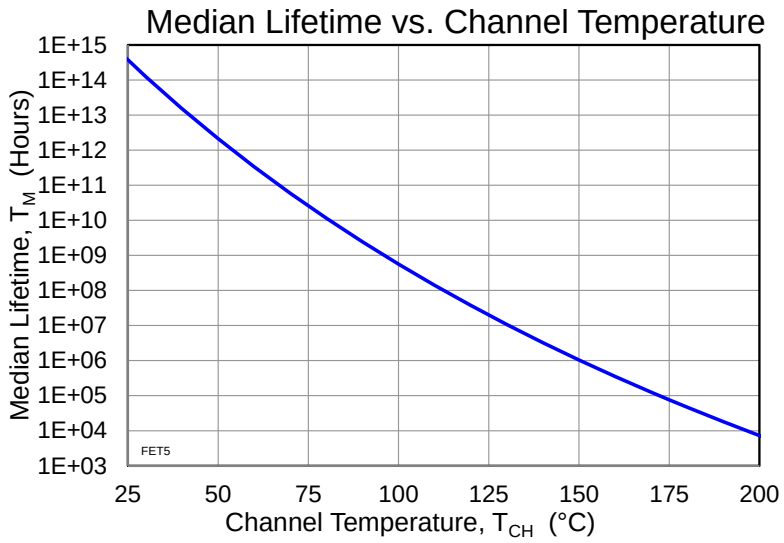
Parameter	Min	Typical	Max	Units
Operational Frequency Range	2		30	GHz
Small Signal Gain		10		dB
Input Return Loss		12		dB
Output Return Loss		12		dB
Noise Figure (at mid-band)		4		dB
Output Power at 1 dB Gain Compression		19		dBm
Output TOI at Pout/tone = 8 dBm		> 25		dBm
Gain Temperature Coefficient		-0.01		dB/°C

Thermal and Reliability Information

Parameter	Test Conditions	Value	Units
Thermal Resistance (θ_{JC}) (1)	T_{base} = 85 °C, V^+ = 10.4 V, V_D = 5 V, V_{CTRL} = 2.2 V, I_{DQ} = 135 mA, P_{DISS} = 0.7 W	30	ºC*mm/W
Channel Temperature (T_{CH}) (no RF drive)		105	°C
Median Lifetime (T_M)		2.8 x 10^8	Hrs
Thermal Resistance (θ_{JC}) (1)	T_{base} = 85 °C, V^+ = 10.4 V (CW), V_{ds} = 5 V, I_{DQ} = 135 mA, I_{D_DRIVE} = 142 mA, P_{IN} = 10 dBm, P_{OUT} = 19 dBm, Freq = 16 GHz, P_{DISS} = 0.62 W	30	ºC*mm/W
Channel Temperature (T_{CH}) (with RF drive)		105	°C
Median Lifetime (T_M)		2.8 x 10^8	Hrs

Notes:
1. Thermal resistance measured at back of the package.

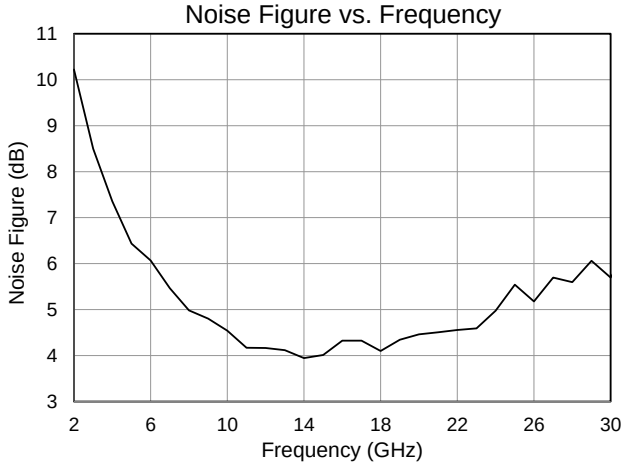
Test Conditions: V_D = 6 V; Failure Criteria is 10% reduction in I_{D_MAX}



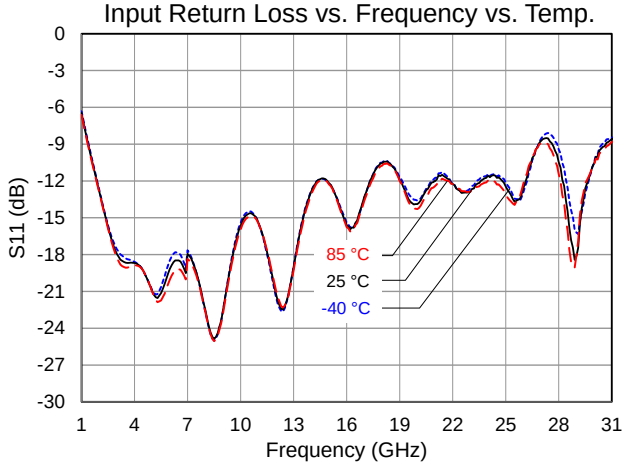
Typical Performance: Small Signal

Conditions unless otherwise specified: V^+ = 10.4 V, I_{DQ} = 135 mA, V_{CTRL} = 2.2 V, V_G = -1 V Typical, CW, 25 °C

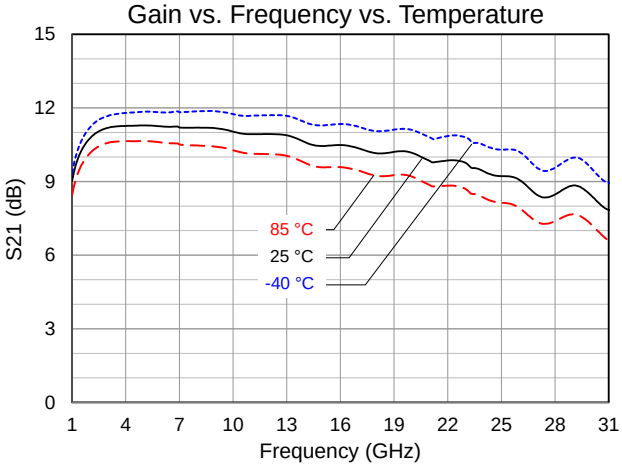
Noise Figure vs. Frequency



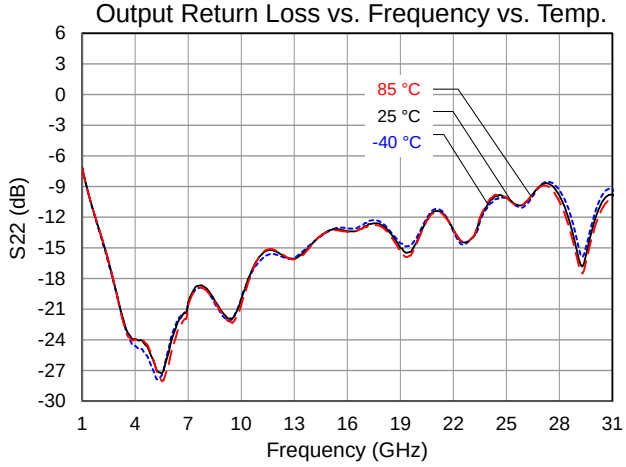
Input Return Loss vs. Frequency vs. Temp.



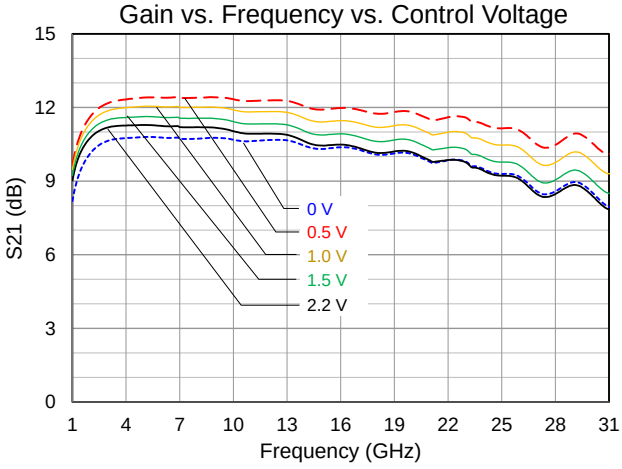
Gain vs. Frequency vs. Temperature



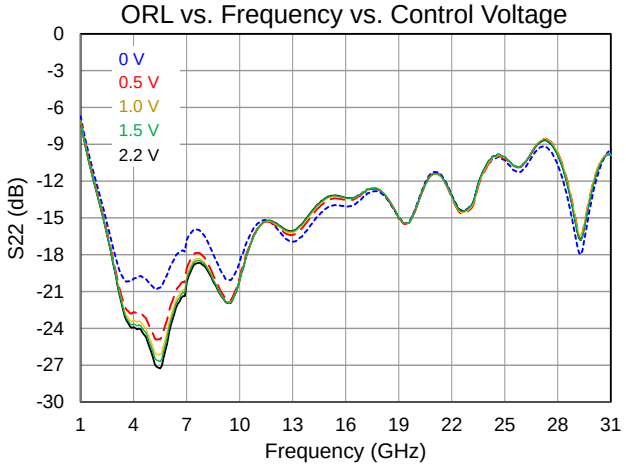
Output Return Loss vs. Frequency vs. Temp.



Gain vs. Frequency vs. Control Voltage



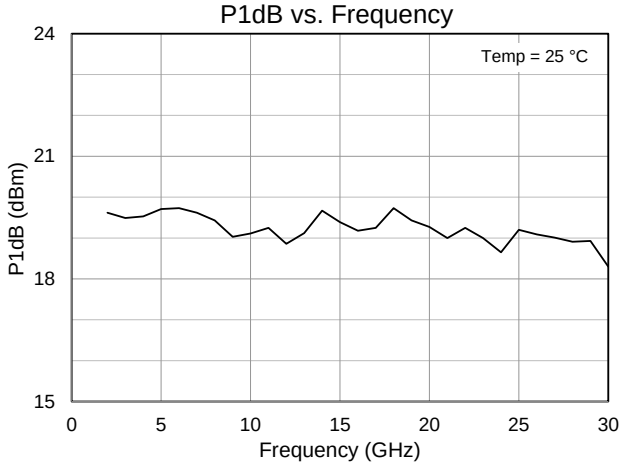
ORL vs. Frequency vs. Control Voltage



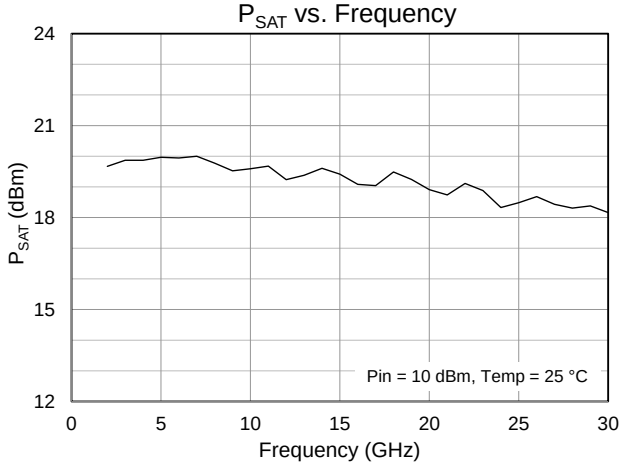
Typical Performance: Large Signal

Conditions unless otherwise specified: V^+ = 10.4 V, I_{DQ} = 135 mA, V_{CTRL} = 2.2 V, V_G = -1 V Typical, CW, 25 °C

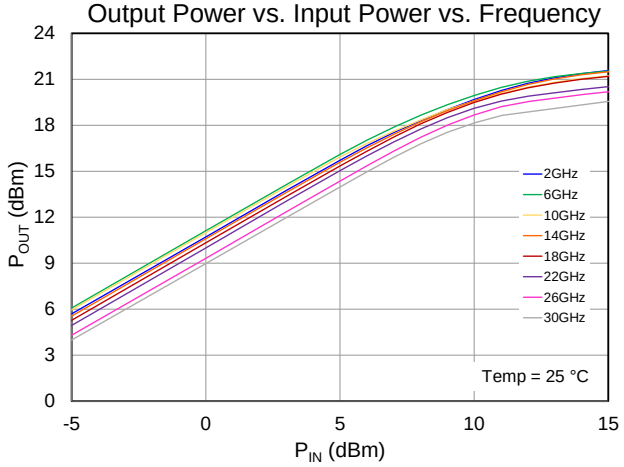
P1dB vs. Frequency



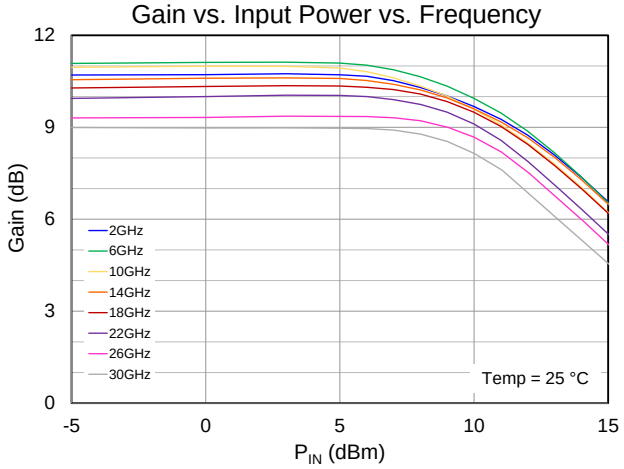
P_{SAT} vs. Frequency



Output Power vs. Input Power vs. Frequency

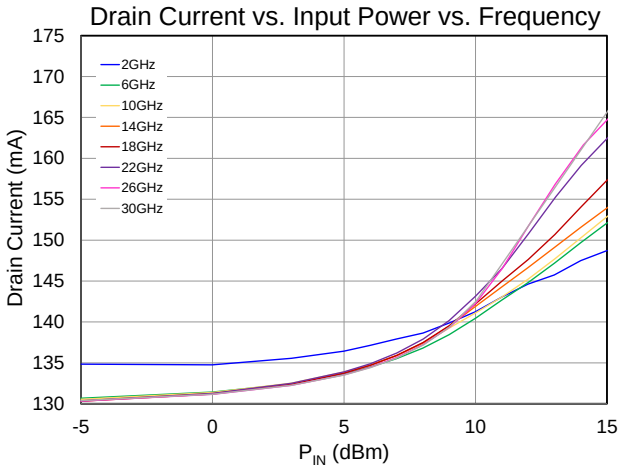


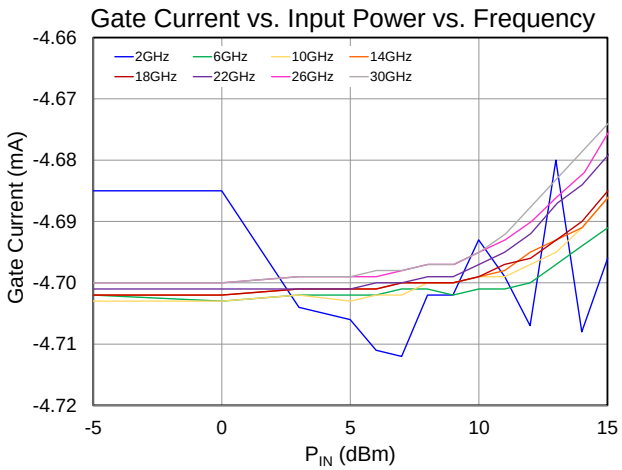
Gain vs. Input Power vs. Frequency

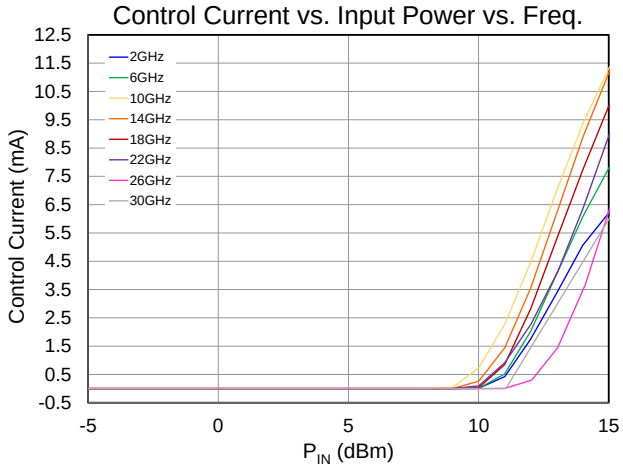


Typical Performance: Large Signal

Conditions unless otherwise specified: V^+ = 10.4 V, I_{DQ} = 135 mA, V_{CTRL} = 2.2 V, V_G = -1 V Typical, CW, 25 °C



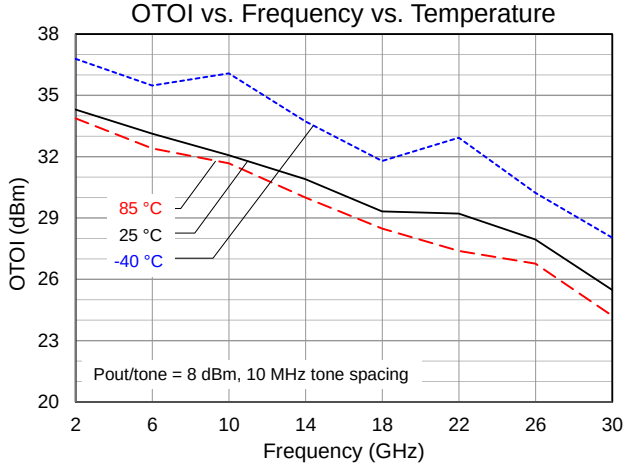




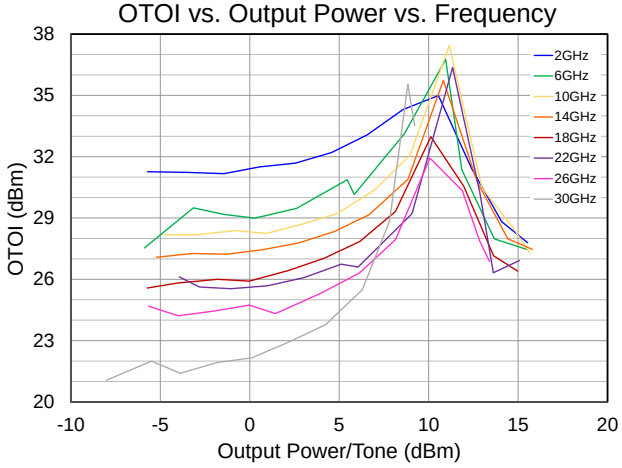
Typical Performance: Linearity

Conditions unless otherwise specified: V^+ = 10.4 V, I_{DQ} = 135 mA, V_{CTRL} = 2.2 V, V_G = -1 V Typical, CW, 25 °C

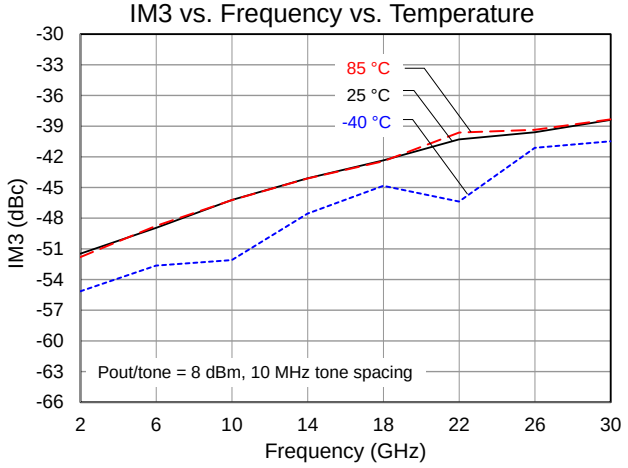
OTOI vs. Frequency vs. Temperature



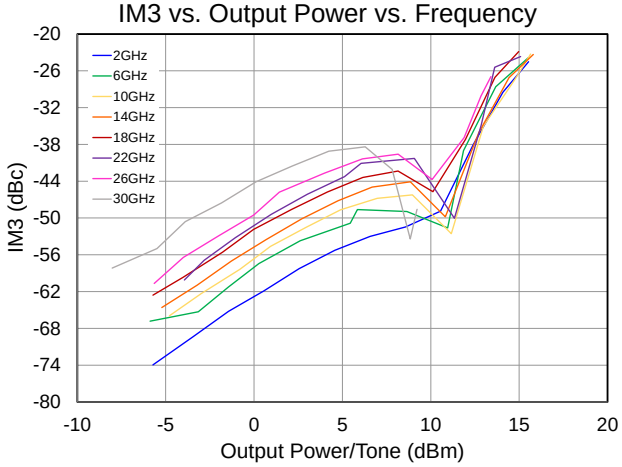
OTOI vs. Output Power vs. Frequency



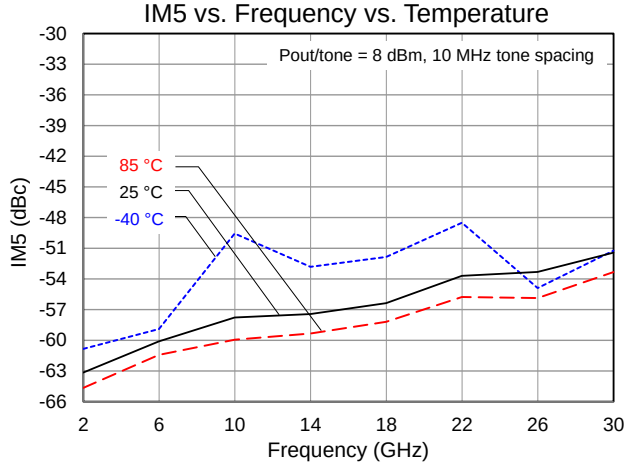
IM3 vs. Frequency vs. Temperature



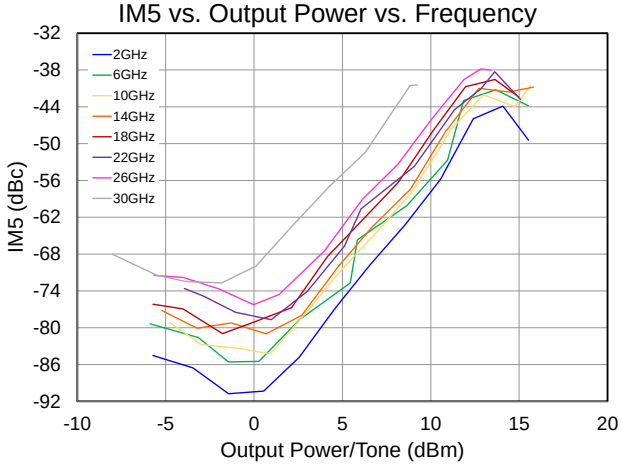
IM3 vs. Output Power vs. Frequency



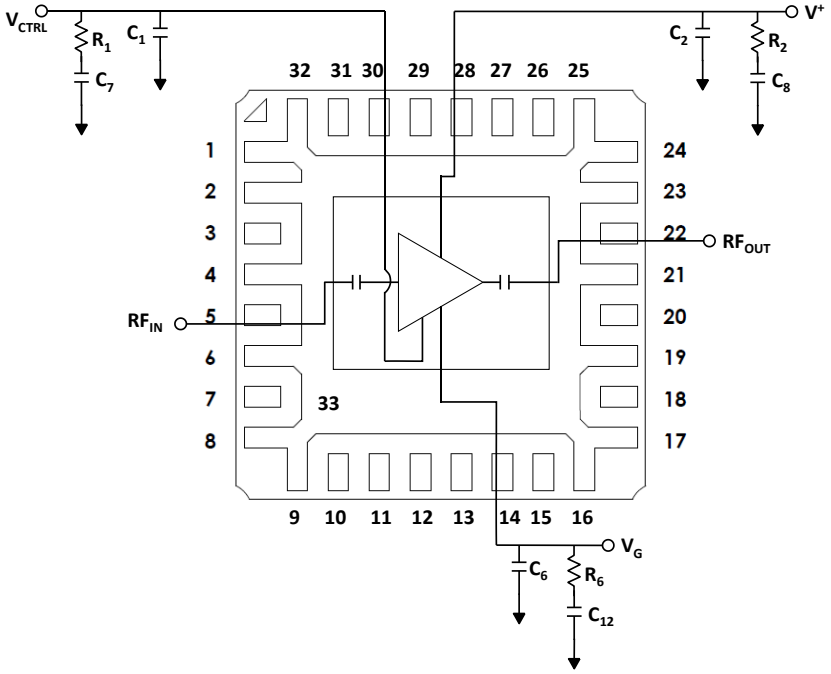
IM5 vs. Frequency vs. Temperature



IM5 vs. Output Power vs. Frequency



Application Information



Bias-up Procedure

1. Set I_D limit to 150 mA, I_G limit to 10 mA
2. Apply -3 V to V_G for pinch off
3. Apply desired value to V_{CTRL}
4. Apply +5 V to V^+
5. Set V_G more positive until I_{DQ} = 100 mA (V_G ~ -1 V Typical)
5. Adjust V^+ to +10.4V, resulting in V_D = 5 V
7. Adjust V_G for I_{DQ} = 135 mA
8. Apply RF signal

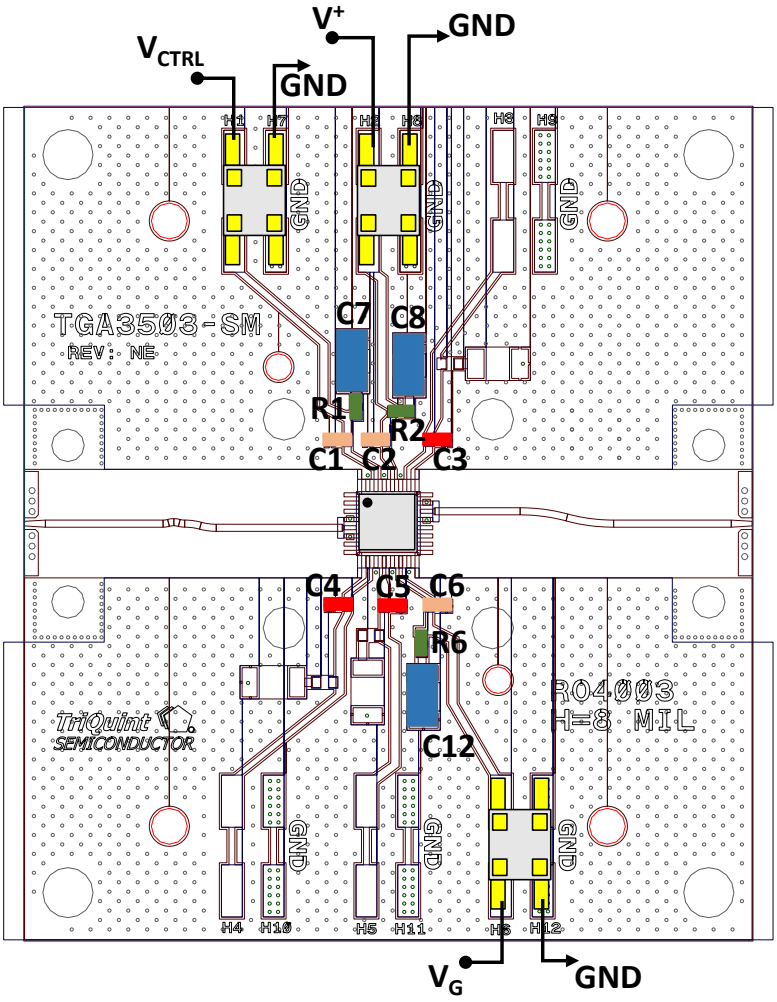
Bias-down Procedure

1. Turn off RF signal
2. Reduce V_G to set I_{DQ} to 100 mA.
3. Set V^+ to 5 V
4. Reduce V_G to -3 V. Ensure I_{DQ} ~ 0 mA
5. Turn off V_{CTRL} supply
6. Set V^+ to 0 V
7. Turn off V_G supply

Pin Description

Pin No.	Symbol	Description
1-4, 6-13, 15-21, 23-27, 29, 31-32	Gnd	Recommend grounding on PCB
5	RF_{IN}	Input; matched to 50 Ω; DC blocked
14	V_G	Gate voltage; bias network is required; see recommended Application Information above.
22	RF_{OUT}	Output; matched to 50 Ω; DC blocked
28	V_+	Drain voltage; bias network is required; see recommended Application Information above.
30	V_{CTRL} or V_{G2}	Gain control voltage; bias network is required; see recommended Application Information above.
33	Gnd	Ground Paddle. Multiple vias should be employed to minimize inductance and thermal resistance.

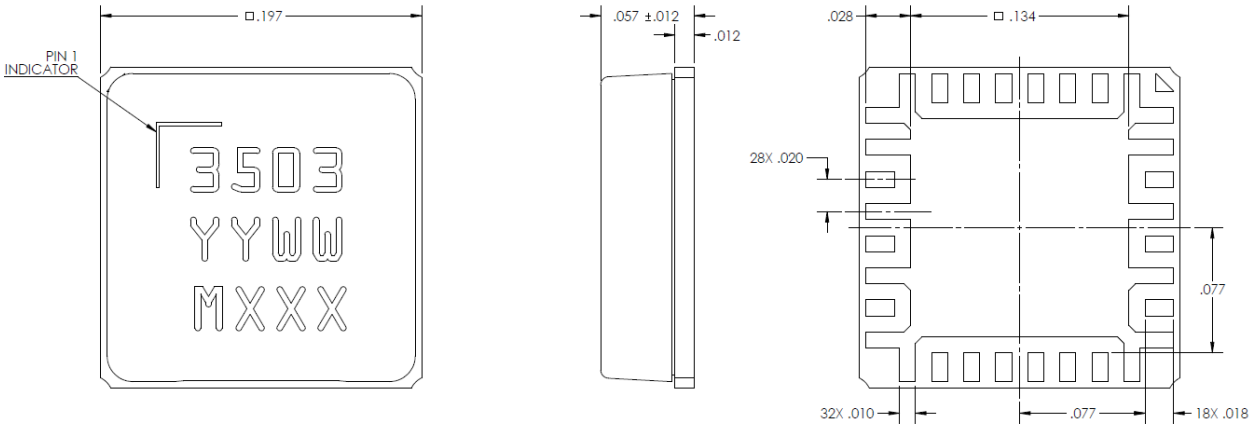
Evaluation Board



Bill of Material

Reference Des.	Value	Description	Manuf.	Part Number
C1, C2, C6	1 µF	Cap, 0402, 50 V, 10%, X7R	Various	
C3, C4, C5	0 Ohms	Res, 0402, 5% (Required for above EVB design)	Various	
C7, C8, C12	2.2 µF	Res, 1206, 50 V, 10%, X7R	Various	
R1, R2, R6	15 Ohms	Res, 0402, 50 V, 5%	Various	

Mechanical Information



Units: inches

Tolerances: unless specified

x.xx = ± 0.01

x.xxx = ± 0.005

Materials:

Base: Aluminum Nitride

All metalized features are Au plated

Part is mold encapsulated

Marking:

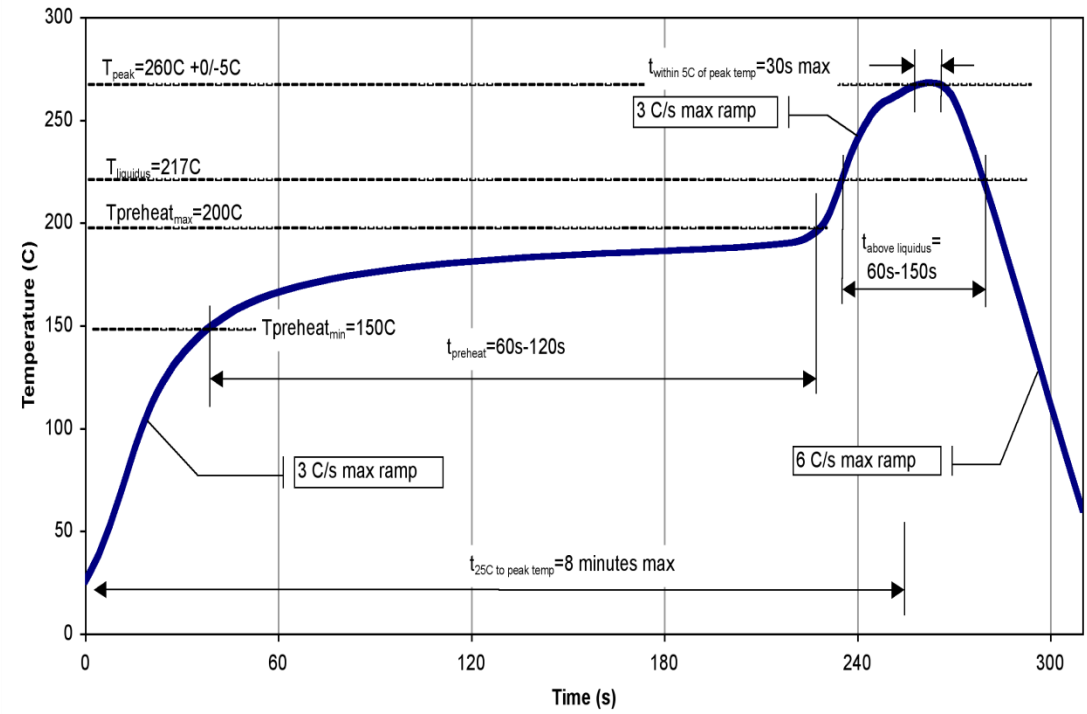
3503: Part number

YY: Part Assembly year

WW: Part Assembly week

MXXX: Batch ID

Recommended Soldering Temperature Profile



Product Compliance Information

ESD Sensitivity Ratings

Caution! ESD-Sensitive Device

ESD Rating: TBD
Value: TBD
Test: Human Body Model (HBM)
Standard: JEDEC Standard JESD22-A114

MSL Rating

Level TBD at TBD°C convection reflow
The part is rated Moisture Sensitivity Level TBD
at TBD°C per JEDEC standard IPC/JEDEC J-STD-020.

ECCN

US Department of Commerce: EAR99

Solderability

Compatible with the latest version of J-STD-020 Lead-free solder, 260 °C.

RoHS Compliance

This part is compliant with EU 2002/95/EC RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment).

This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A ($C_{15}H_{12}Br_4O_2$) Free
- PFOS Free
- SVHC Free

Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations, and information about TriQuint:

Web: www.triquint.com **Tel:** +1.972.994.8465
Email: info-sales@triquint.com **Fax:** +1.972.994.8504

For technical questions and application information: **Email:** info-products@triquint.com

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