

(0.635 mm) .025"

**QTS SERIES** 

**OTHER** 

**OPTION** 

= (7.00 mm)

.275" DIA

Polyimide film

Pick &

Place Pad

\_TR

= Tape & Reel

# **HIGH-SPEED GROUND PLANE HEADER**

## **SPECIFICATIONS**

For complete specifications and recommended PCB layouts see www.samtec.com?QTS

Insulator Material: Liquid Crystal Polymer Contact Material: Phosphor Bronze Plating: Au or Sn over 50 μ" (1.27 μm) Ni

**Current Rating:** Contact: 1.8 A per pin

(2 pins powered) Ground Plane: 23.1 A per ground plane (1 ground plane powered) Operating Temp: -55 °C to +125 °C Voltage Rating: 285 VAC

Max Cycles:

**RoHS Compliant:** 

**Board Mates:** 

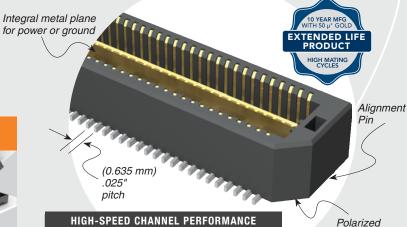
**Cable Mates:** 

Standoffs:

# APPLICATION

POWER/SIGNAL

Compatible with UMPT/UMPS for flexible two-piece power/signal solutions



#### QTS/QSS @ 5 mm Mated Stack Height

Rating based on Samtec reference channel. For full SI performance data visit Samtec.com or contact SIG@samtec.com

### **PROCESSING**

Lead-Free Solderable: SMT Lead Coplanarity:

(0.10 mm) .004" max (025-075) Board Stacking: For applications requiring more

than two connectors per board contact ipg@samtec.com

#### **RECOGNITIONS**

For complete scope of recognitions see www.samtec.com/quality



#### ALSO AVAILABLE (MOQ Required)

- 11 mm & 16 mm stack height
- 30 µ" (0.76 µm) Gold
- Differential Pair and "Partitionable" (combine differential & single-ended banks in same connector) available.
- 100 & 125 positions per row
- Edge Mount

Some lengths, styles and options are non-standard, non-returnable.



-(20.00) .7875

→ (0.635) .025

(5.97)

.235

-025, -050, -075 (50 total positions per bank)

LEAD STYLE from

chart.

(7.11)

.280

**LEAD** 

STYLE

# **OPTION**

**PLATING** 

= Gold Flash on Signal Pins and Ground Plane,

## Matte Tin on tails

= 10 μ" (0.25 μm) Gold on Signal Pins and Ground Plane, Matte Tin on tails

= Electro-Polished Selective 50 μ" (1.27 μm) min Au over 150 μ" (3.81 μm) Ni on Signal Pins in contact area, 10 μ" (0.25 μm) min Au over 50 μ" (1.27 μm) Ni on Ground Plane in contact area, Matte Tin over 50 μ" (1.27 μm) min Ni on all solder tails

\*Note: -C Plating passes 10 year MFG testing



(No. of Positions per Row/25) x (20.00) .7875→

(0.89) .035 →   ←	0.76) .030 *
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LEAD STYLE	Α	MATED HEIGHT
-01	(4.27) .168	(5.00) .197
-02	(7.26) .286	(8.00) .315

Processing conditions will affect mated height. See SO Series for board space tolerances

(0.20) .008 →

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