

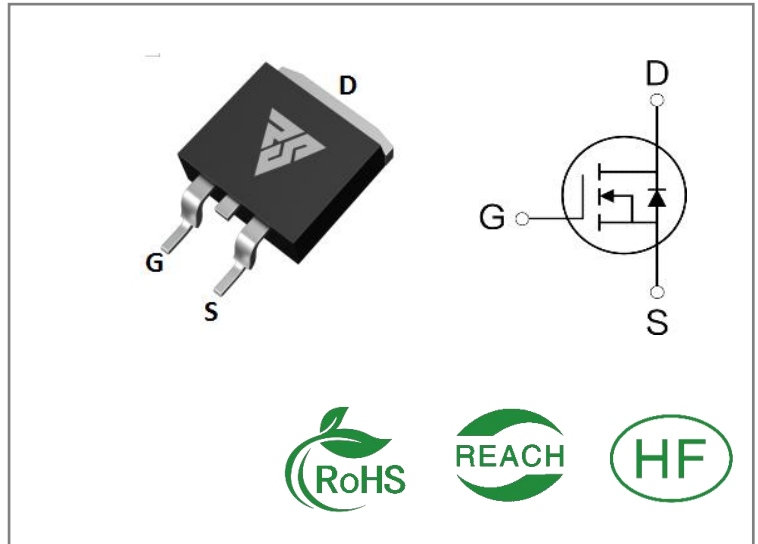
ID	R _{DS(ON)} (Typ)	VDSS
105A	9.8mΩ	150V

Applications:

- Load Switch
- PWM Applications
- Power Managment

Features:

- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability


Ordering Information

Part Number	Package	Marking	Packing	Qty.
RS150N105S	TO-263	RS150N105S	Tape&reel	800 PCS

Absolute Maximun Ratings T_c= 25°C unless otherwise specified

Symbol	Parameter	RS150N105S	Units
VDSS	Drain-to-Source Voltage	150	V
ID	Continuous Drain Current TC=25°C	105	A
ID	Continuous Drain Current TC=100°C	75	
IDM	Pulsed Drain Current	420	
PD	Power Dissipation	380	W
VGS	Gate- to- Source Voltage	±20	V
EAS	Single Pulse Avalanche Engergy L = 0.3mH,VDS = 50V, RG = 25Ω, Tj = 25°C	1000	mJ
TL TPKG	Maximum Temperature for Soldering	300 260	°C
	Leads at 0.063in(1.6mm)from Case for 10 seconds		
	Package Body for 10 seconds		
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

* Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the“ Absolute Maximum Ratings” Table may cause permanent damage to the device.

Thermal Resistance

Symbol	Parameter	RS150N105S	Units	Test Conditions
R θ JC	Junction-to-Case	0.36	°C / W	Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of + 150 °C
R θ JA	Junction-to-Ambient	60		1 cubic foot chamber, free air.

OFF Characteristics T_J= 25°C unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
BVDSS	Drain- to- source Breakdown Voltage	150	--	--	V	V _{GS} =0V, I _D =250μA
IDSS	Drain- to- Source Leakage Current	--	--	1	μA	V _{DS} =150V, V _{GS} =0V
IGSS	Gate- to- Source Forward Leakage	--	--	100	nA	V _{GS} =20V , V _{DS} =0V
	Gate- to- Source Reverse Leakage	--	--	-100		V _{GS} =-20V , V _{DS} =0V

ON Characteristics T_J=25°C unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
R _{DS(on)}	Static Drain- to- Source On-Resistance	--	9.8	11	mΩ	V _{GS} =10V, I _D =60A
V _{GS(TH)}	Gate Threshold Voltage	3.6	--	5.0	V	V _{GS} =V _{DS} , I _D =250μA

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
t _{d(ON)}	Turn- on Delay Time	--	45	--	nS	V _{DS} =50V I _D =40A R _G =2.5Ω V _{GS} =10V
t _{rise}	Rise Time	--	70	--		
t _{d(OFF)}	Turn- OFF Delay Time	--	110	--		
t _{fall}	Fall Time	--	90	--		

Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Ciss	Input Capacitance	--	7000	--	pF	VGS= 0V VDS=50V f=1MHz
Coss	Output Capacitance	--	480	--		
Crss	Reverse Transfer Capacitance	--	210	--		
Qg	Total Gate Charge	--	85	--	nC	VDS= 100V ID=40A VGS=10V
Qgs	Gate- to- Source Charge	--	15	--		
Qgd	Gate-to-Drain(" Miller") Charge	--	25	--		

Source- Drain Diode Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
IS	Continuous Source Current	--	--	105	A	Integral pn- diode in MOSFET
ISM	Maximum Pulsed Current	--	--	420	A	
VSD	Diode Forward Voltage	--	--	1.2	V	IS=60A,VGS=0V
trr	Reverse Recovery Time	--	110	--	nS	VGS=0V IS=30A di/dt=100A/μs
Qrr	Reverse Recovery Charge	--	0.55	--	μC	

Notes:

- * 1. Repetitive rating, pulse width limited by maximum junction temperature.
- * 2. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty Cycle $\leq 1.5\%$

Typical Feature Curve

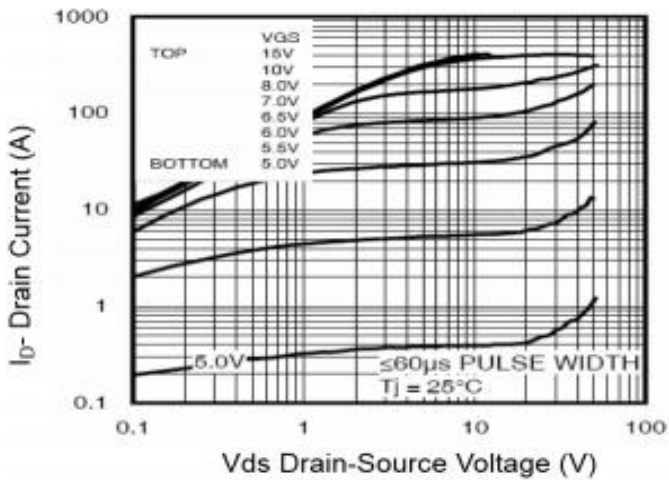


Figure 1 Output Characteristics

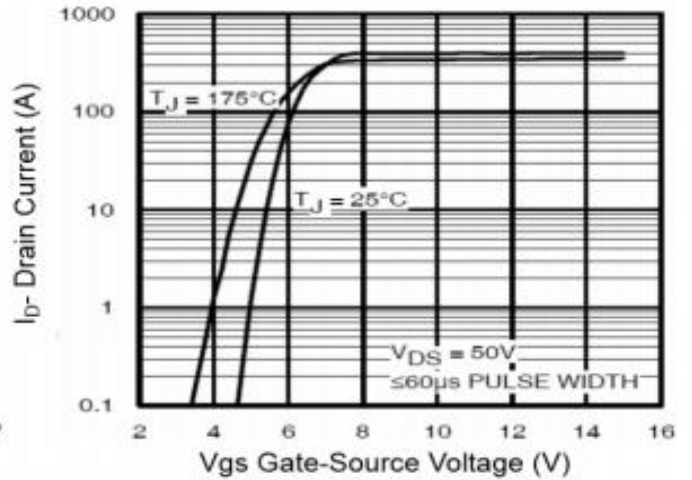


Figure 2 Transfer Characteristics

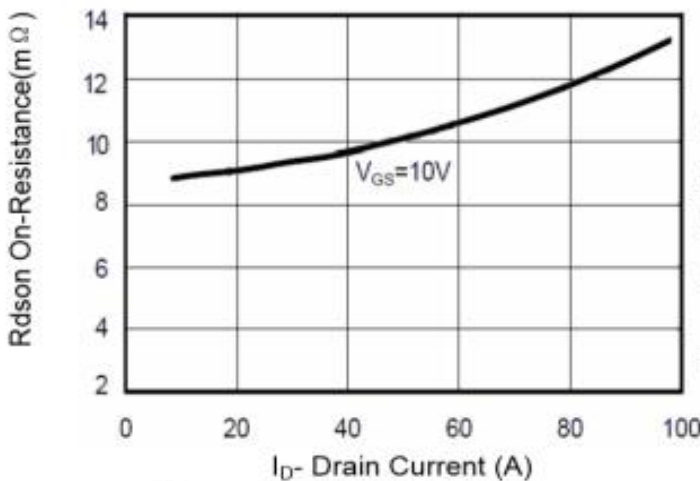


Figure 3 Rdson- Drain Current

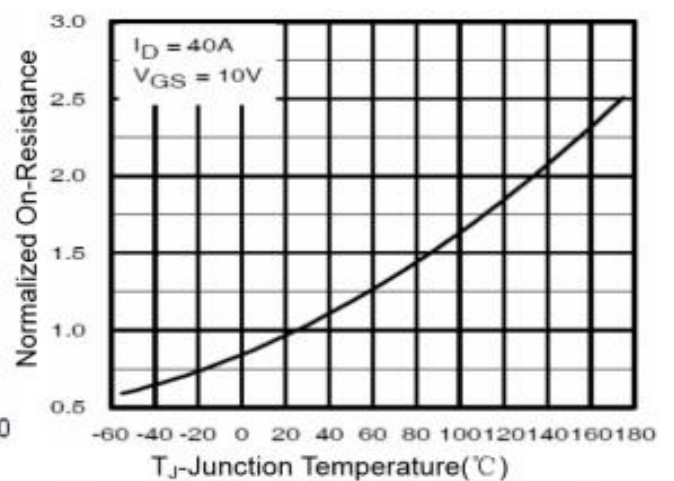


Figure 4 Rdson-Junction Temperature

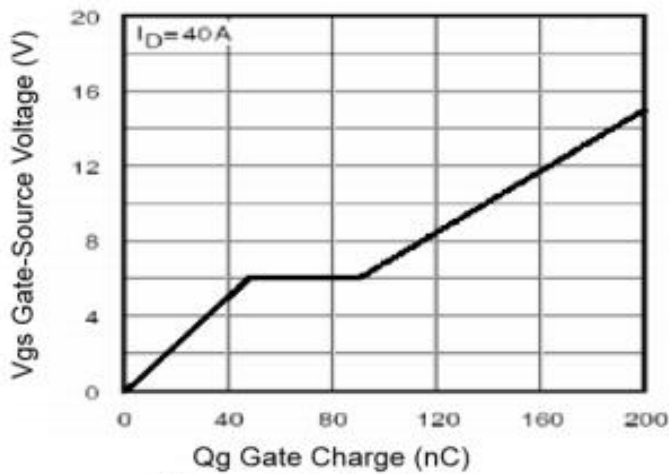


Figure 5 Gate Charge

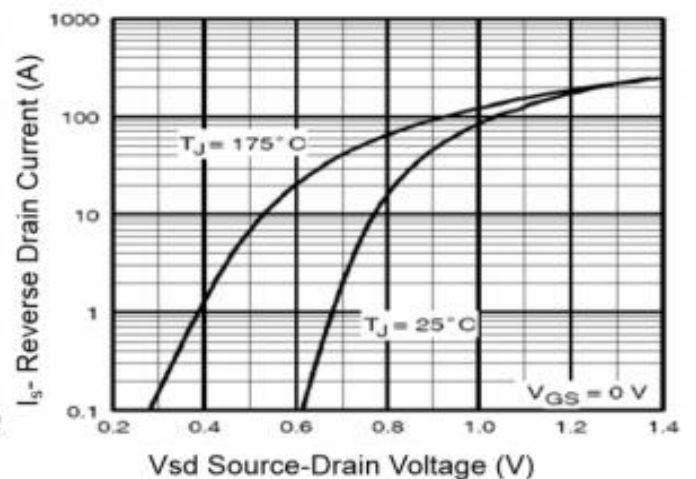


Figure 6 Source- Drain Diode Forward

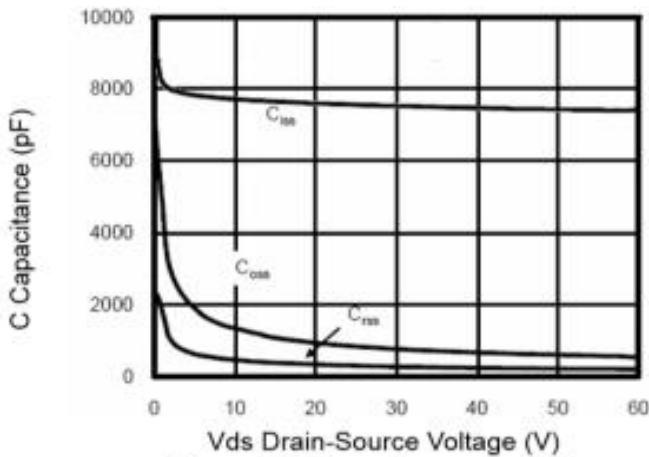


Figure 7 Capacitance vs Vds

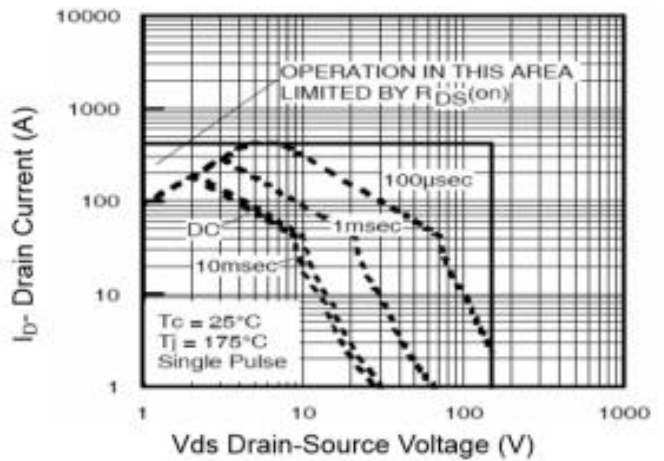


Figure 8 Safe Operation Area

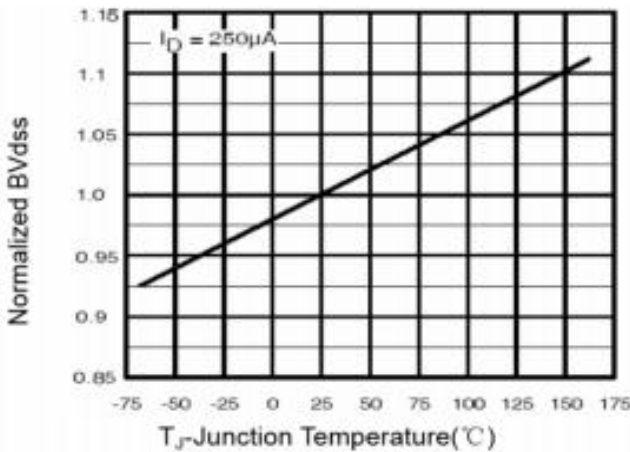


Figure 9 BV_{DSS} vs Junction Temperature

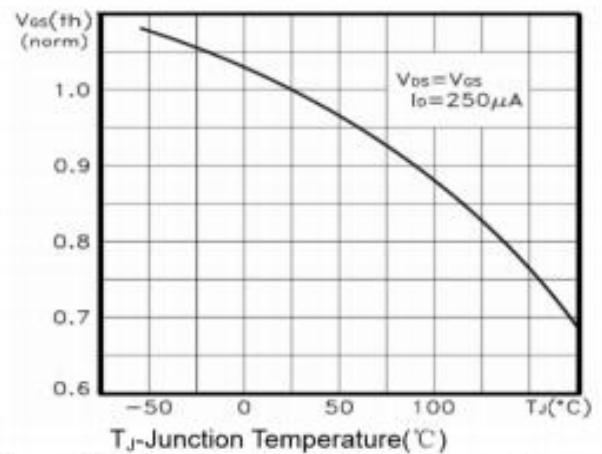


Figure 10 V_{GS(th)} vs Junction Temperature

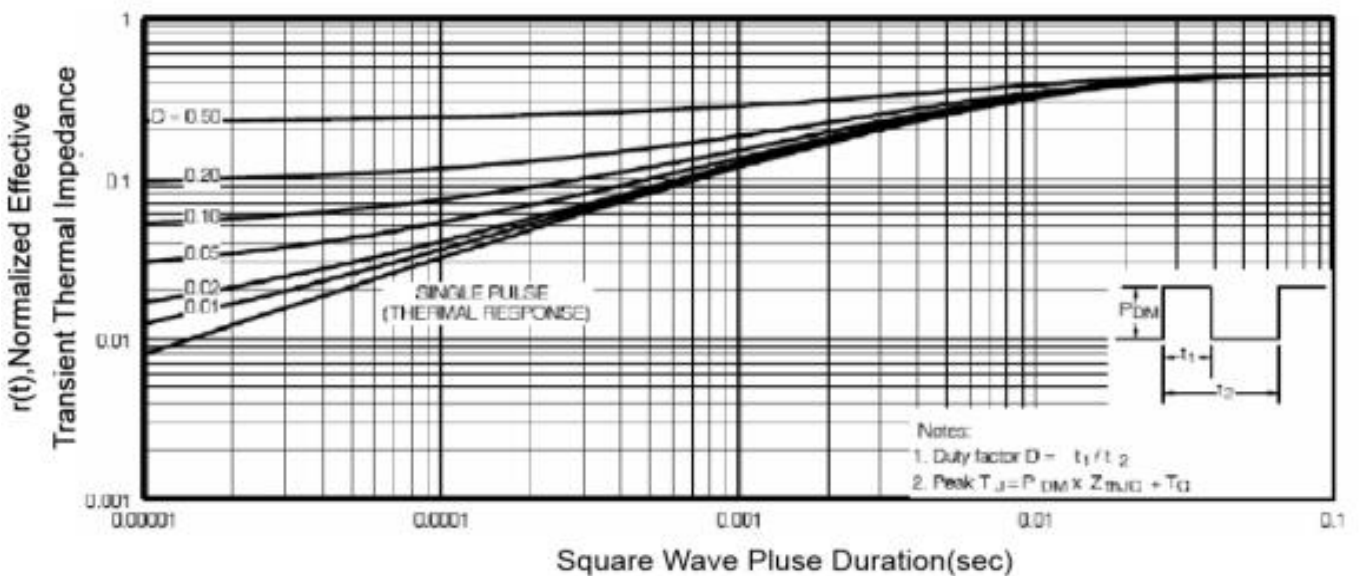


Figure 11 Normalized Maximum Transient Thermal Impedance

Test circuits and Waveforms

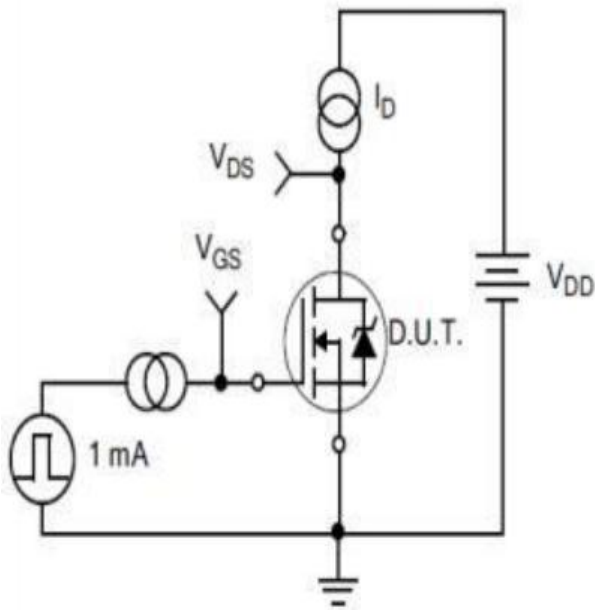


Figure A.
Gate Charge Test Circuit

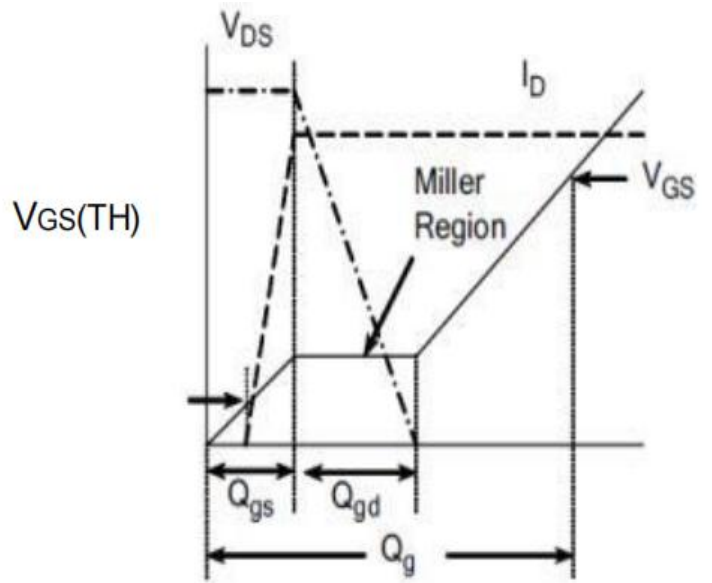


Figure B.
Gate Charge Waveform

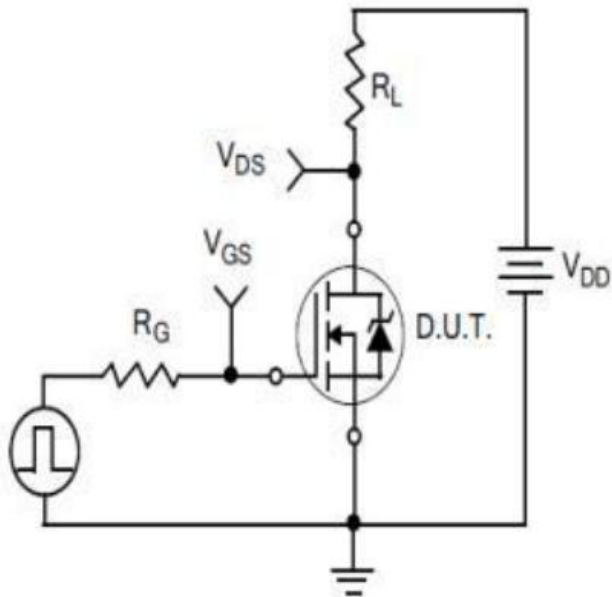


Figure C.
Resistive Switching Test Circuit

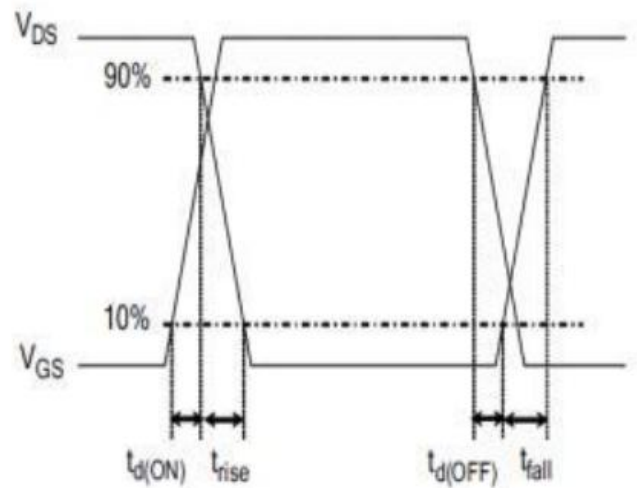


Figure D.
Resistive Switching Waveforms

Test circuits and Waveforms

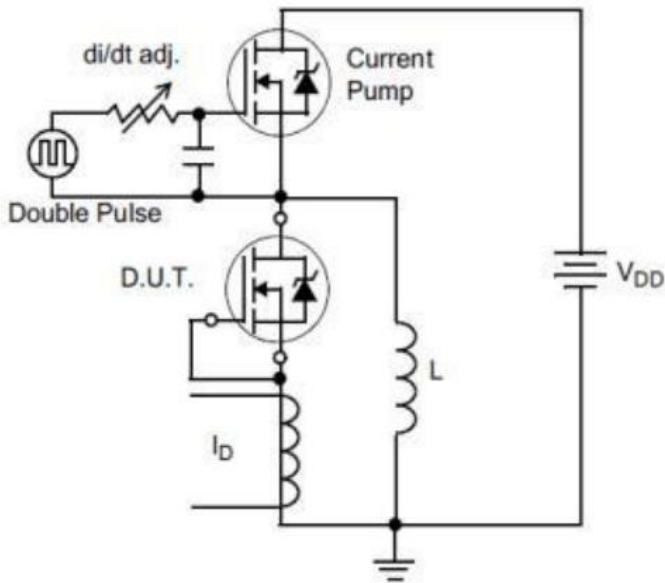


Figure E. Diode Reverse Recovery Test Circuit

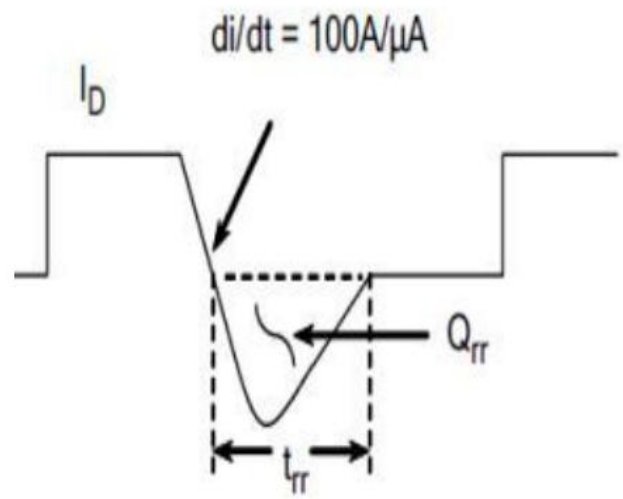


Figure F. Diode Reverse Recovery Waveform

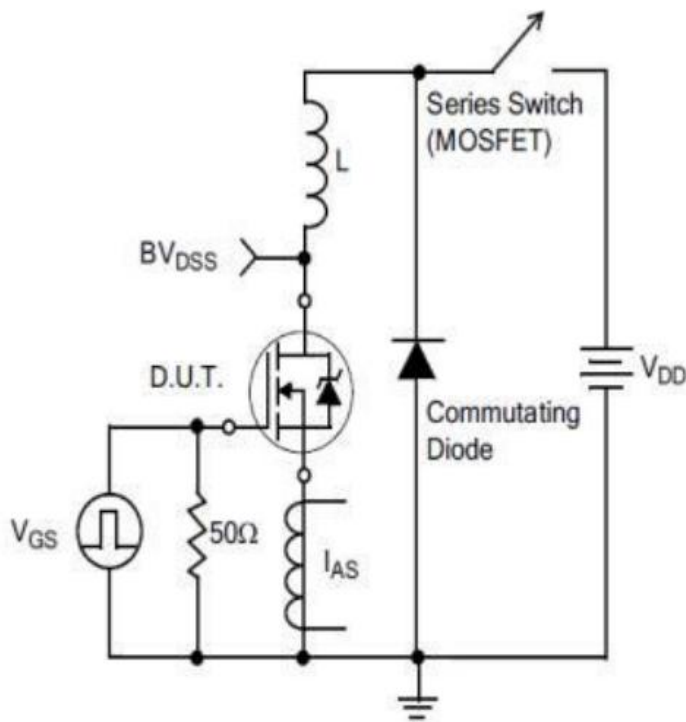
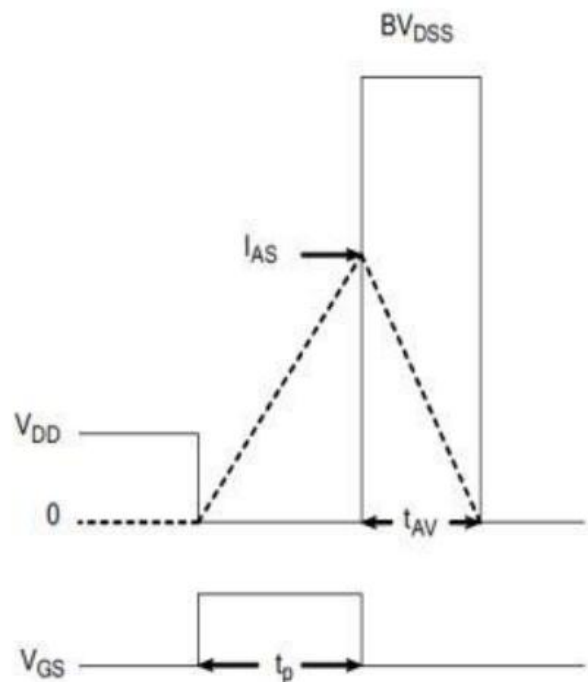


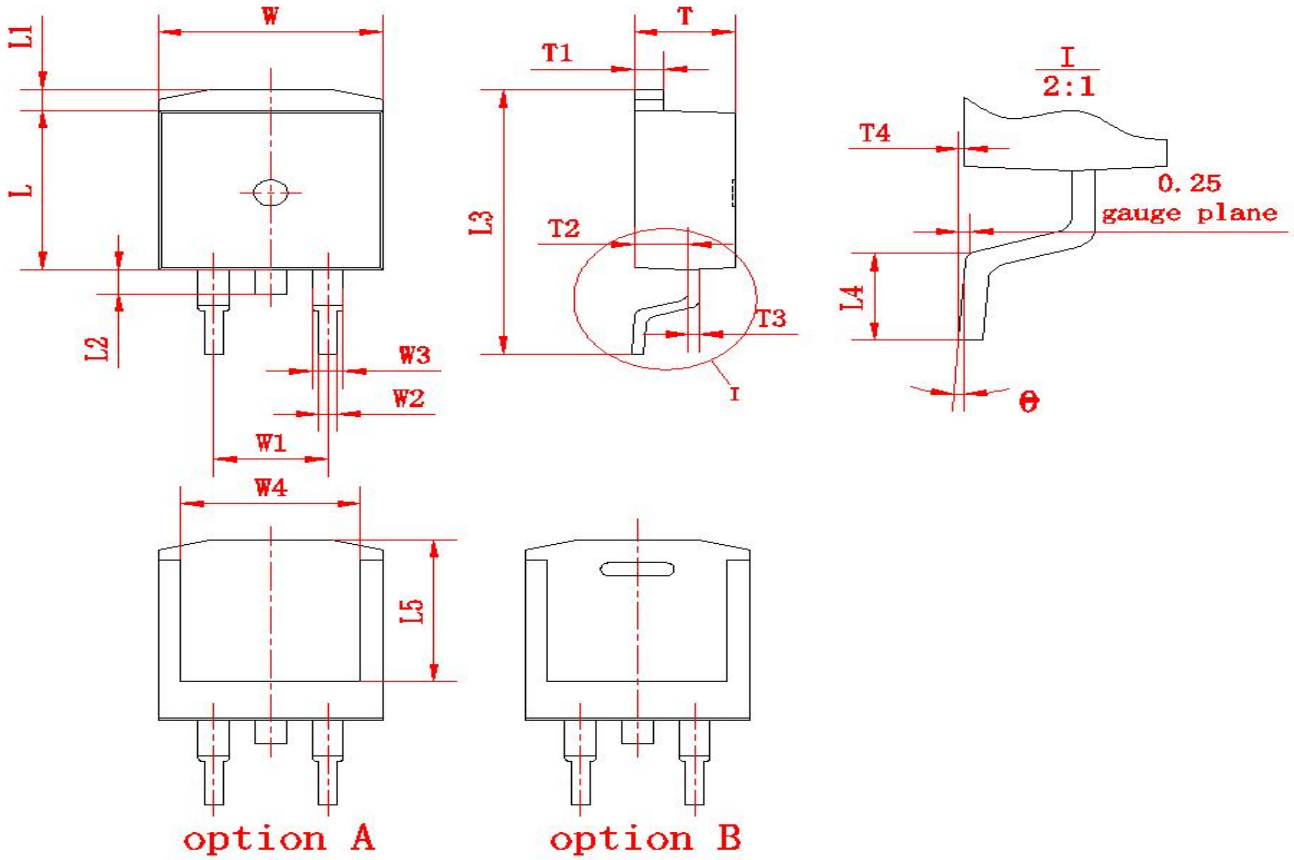
Figure G. Unclamped Inductive Switching Test Circuit



$$EAS = \frac{I_{AS}^2 L}{2}$$

Figure H. Unclamped Inductive Switching Waveforms

Package outline drawing(TO-263 Unit: mm)



(单位: mm)

符号	尺寸		符号	尺寸		符号	尺寸	
	Min	Max		Min	Max		Min	Max
W	9.80	10.20	L1	1.00	1.40	T1	1.20	1.40
W1	(5.08)		L2	1.20	1.60	T2	2.20	2.60
W2	0.70	0.95	L3	15.00	15.60	T3	0.45	0.65
W3	1.17	1.62	L4	2.20	2.80	T4	0	0.25
W4	(8.0)		L5	(8.2)		θ	0°	8°
L	9.00	9.40	T	4.30	4.70			

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