

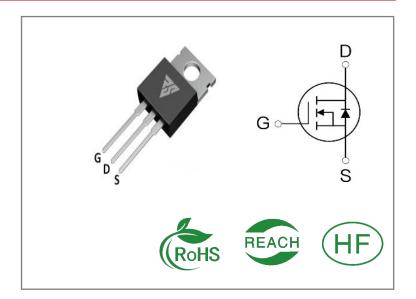
ID	R _{DS} (ON)(Typ)	VDSS
160A	5.2mΩ	150V

Applications:

- Load Switch
- PWM Applications
- Power Managment

Features:

- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability



Ordering Information

Part Number	Package	Marking	Packing	Qty.
RS150N160T	T0-220	RS150N160T	Tube	50 PCS

Absolute Maximun Ratings Tc= 25℃ unless otherwise specified

Symbol	Parameter	RS150N160T	Units
VDSS	Drain-to-Source Voltage	150	V
ID	Continuous Drain Current TC=25℃	160	
ID	Continuous Drain Current TC=100℃	112	А
IDM	Pulsed Drain Current	600	
PD	Power Dissipation	425	W
VGS	Gate- to- Source Voltage	±20	V
EAS	Single Pulse Avalanche Engergy L = 0.3mH,IS =60A, RG = 25Ω , Tj = 25° C	540	mJ
	Maximum Temperature for Soldering		
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	300 260	$^{\circ}$
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

^{*} Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" Table may cause permanent damage to the device.



Thermal Resistance

Symbol	Parameter	RS150N160T	Units	Test Conditions
RθJC	Junction-to-Case	0.42	°C/W	Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of + 1 5 0 $^{\circ}$ C
RθJA	Junction-to- Ambient	46		1 cubic foot chamber,free air.

OFF Characteristics TJ= 25℃ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BVDSS	Drain- to- source Breakdown Voltage	150			V	VGS=0V,ID=250μ A
IDSS	Drain- to- Source Leakage Current			1	μΑ	VDS=120V,VGS= 0V
IGSS	Gate- to- Source Forward Leakage			100	Δ.	VGS=20V ,VDS=0 V
1033	Gate- to- Source Reverse Leakage			-100	nA	VGS=-20V ,VDS= 0V

ON Characteristics TJ=25 °C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain- to- Source On- Resistance		5.2	6.3	mΩ	VGS=10V,ID=20A
VGS(TH)	Gate Threshold Voltage	2.5		4.5	٧	VGS=VDS,ID=25 0μA

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn- on Delay Time		19			VDS=75V RL=3.5Ω RG=6Ω VGS=10V
trise	Rise Time		31		nS	
td(OFF)	Turn- OFF Delay Time		52			
tfall	Fall Time		40			



Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions	
Ciss	Input Capacitance		4300			VGS= 0V	
Coss	Output Capacitance		530		рF	VDS=75V	
Crss	Reverse Transfer Capacitance		7.5			f=1MHz	
Qg	Total Gate Charge		68			VDS= 75V	
Qgs	Gate- to- Source Charge		15		nC	ID=20A	
Qgd	Gate-to-Drain(" Miller") Charge		14			VGS=10V	

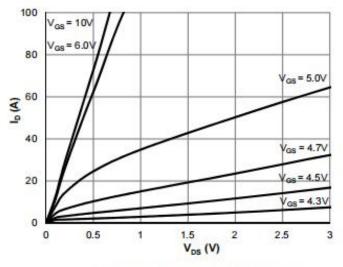
Source-Drain Diode Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
IS	Continuous Source Current			160	Α	Integral pn- diode
ISM	Maximum Pulsed Current			600	Α	in MOSFET
VSD	Diode Forward Voltage			1.0	V	IS=1A,VGS=0V
trr	Reverse Recovery Time		100		nS	VGS=0V
Qrr	Reverse Recovery Charge		150		nC	IS=15A di/dt=100A/μs

Notes:

- * 1. Repetitive rating, pulse width limited by maximum junction temperature.
- * 2. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%

Typical Feature Curve





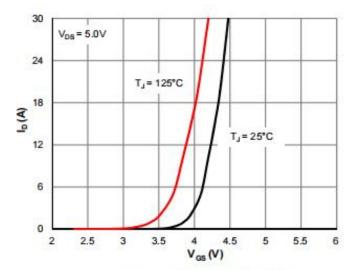
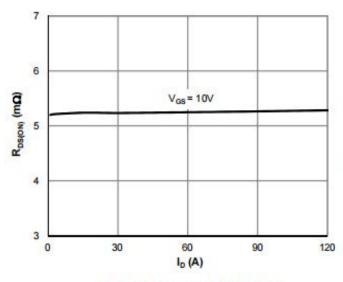


Figure 2: Transfer Characteristics



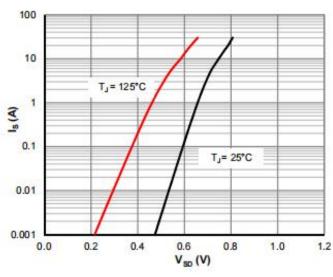


2 V_{GS} = 10V Normalized Roston I_D = 20A 1 0.5 -50 -25 25 50 75 100 125 150 175 Temperature (°C)

2.5

Figure 3: RDS(ON) vs. Drain Current

Figure 4: RDS(ON) vs. Junction Temperature



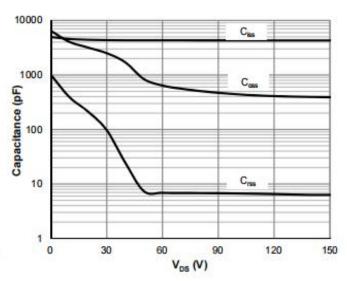
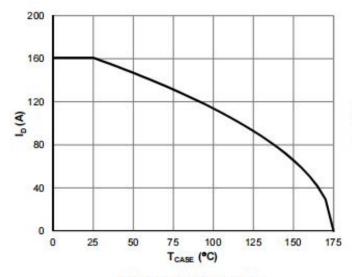


Figure 5: Body-Diode Characteristics

Figure 6: Capacitance Characteristics



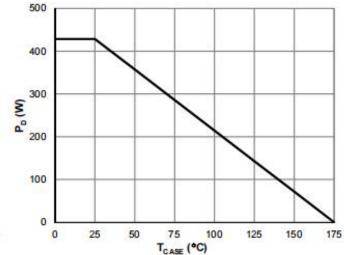


Figure 7: Current De-rating

Figure 8: Power De-rating



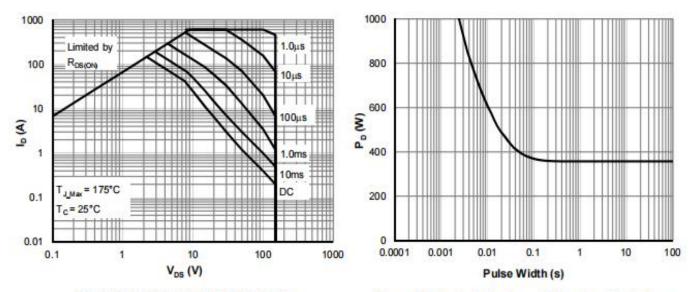


Figure 9: Maximum Safe Operating Area

Figure 10: Single Pulse Power Rating, Junction-to-Case

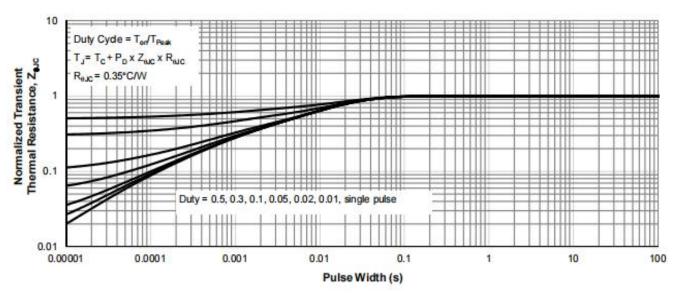


Figure 11: Normalized Maximum Transient Thermal Impedance

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Test ircuits and Waveforms

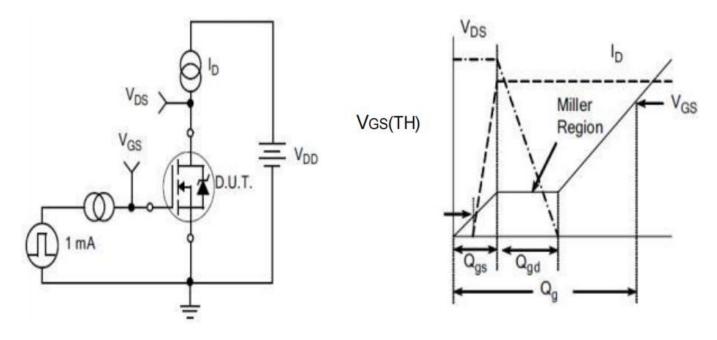


Figure A.
Gate Charge Test Circuit

Figure B.
Gate Charge Waveform

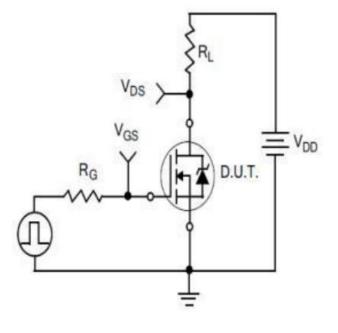


Figure C.
Resistive Switching Test Circuit

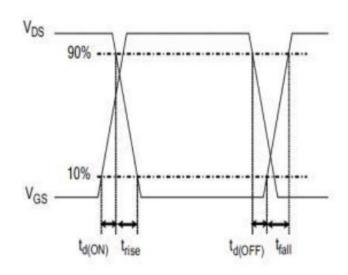
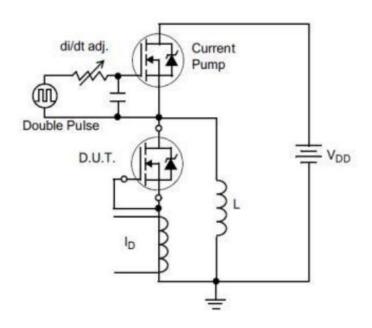


Figure D.
Resistive Switching Waveforms



Test ircuits and Waveforms



 $di/dt = 100A/\mu A$ Q_{rr}

Figure E.Diode Reverse Recovery Test Circuit

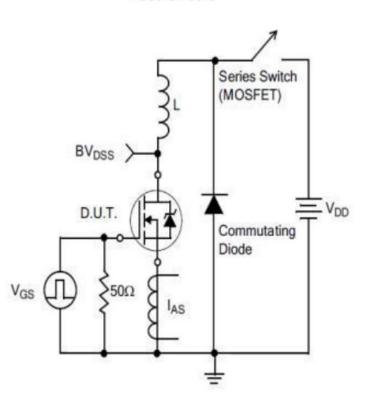


Figure F.Diode Reverse Recovery Waveform

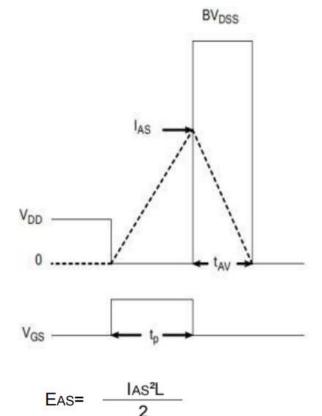


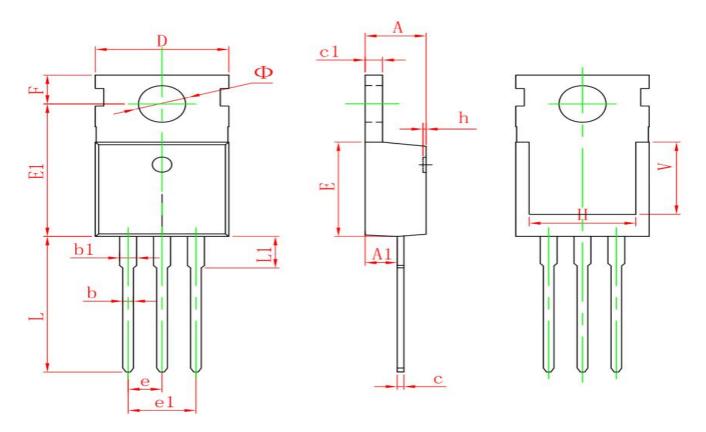
Figure G.Unclamped Inductive Switching Test Circuit

Figure H.Unclamped Inductive Switching Waveforms

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Package outline drawing(TO-220 Unit: mm)



Symbol	Dimensions	In Millimeters	Dimension	s In Inches	
Symbol	Min.	Max.	Min.	Max.	
Α	4.400	4.600	0.173	0.181	
A1	2.250	2.550	0.089	0.100	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.330	0.650	0.013	0.026	
c1	1.200	1.400	0.047	0.055	
D	9.910	10.250	0.390	0.404	
Е	8.950	9.750	0.352	0.384	
E1	12.650	13.050	0.498	0.514	
е	2.540	TYP.	0.100	TYP.	
e1	4.980	5.180	0.196	0.204	
F	2.650	2.950	0.104	0.116	
Н	7.900	8.100	0.311	0.319	
h	0.000	0.300	0.000	0.012	
L	12.900	13.400	0.508	0.528	
L1	2.850	3.250	0.112	0.128	
V	6.900	REF.	0.276 REF.		
Φ	3.400	3.800	0.134	0.150	



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