

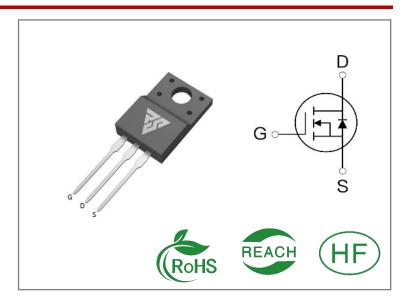
ID	R _{DS} (ON)(Typ)	VDSS
2A	3.8Ω	650V

Applications:

- Switch Mode Power Supply(SMPS)
- Adapter & Charger
- AC-DC Switching Power Supply

Features:

- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability



Ordering Information

Part Number	Package	Marking	Packing	Qty.
RS2N65F	T0-220F	RS2N65F	Tube	50 PCS

Absolute Maximun Ratings Tc= 25°C unless otherwise specified

Symbol	Parameter	RS2N65F	Units
VDSS	Drain-to-Source Voltage	650	V
ID	Continuous Drain Current TC=25℃	2	
ID	Continuous Drain Current TC=100°C	1.45	А
IDM	Pulsed Drain Current (Note*1)	8	
PD	Power Dissipation	33	W
VGS	Gate- to- Source Voltage	±30	V
EAS	Single Pulse Avalanche Engergy L = 30mH, IAS=2.52A,VDD = 145V, RG = 25 Ω	68	mJ
	Maximum Temperature for Soldering	300	
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	260	$^{\circ}$
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

^{*} Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" Table may cause permanent damage to the device.



Thermal Resistance

Symbol	Parameter	RS2N65F	Units	Test Conditions
				Drain lead soldered to water cooled
RθJC	Junction-to-Case	3.78		heatsink, PD adjusted for a peak
			°C/W	junction temperature of + 1 5 0 $^{\circ}{\mathbb{C}}$
RθJA	Junction-to-	42		1 cubic fact chamber free sir
KOJA	Ambient	62		1 cubic foot chamber,free air.

OFF Characteristics TJ= 25[°]C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BVDSS	Drain- to- source Breakdown Voltage	650			V	VGS=0V,ID=250μA
IDSS	Drain- to- Source Leakage Current			1	μΑ	VDS=650V,VGS=0 V
	Gate- to- Source Forward Leakage			100		VGS=30V,VDS=0V
IGSS	Gate- to- Source Reverse Leakage			-100	nA	VGS=-30V ,VDS=0 V

ON Characteristics TJ=25 °C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain- to- Source On- Resistance(Note*2)		3.8	4.5	Ω	VGS=10V,ID=1A
VGS(TH)	Gate Threshold Voltage	2		4	V	VGS=VDS,ID=250μ A

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn- on Delay Time		7.8			
trise	Rise Time		6			VDS=325V ID=2A
td(OFF)	Turn- OFF Delay Time		30		nS	RG=9.1Ω
tfall	Fall Time		11			



Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ciss	Input Capacitance		290			VGS=0V
Coss	Output Capacitance		31		pF	VDS=25V
Crss	Reverse Transfer Capacitance		6			f=1.0MHz
Qg	Total Gate Charge		9			VDS=325V
Qgs	Gate- to- Source Charge		1.5		nC	ID=2A
Qgd	Gate-to-Drain(" Miller") Charge		4			VGS=10V

Source-Drain Diode Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
IS	Continuous Source Current			2	Α	Integral pn- diode
ISM	Maximum Pulsed Current			8	Α	in MOSFET
VSD	Diode Forward Voltage			1.5	٧	IS=2A,VGS=0V
trr	Reverse Recovery Time		425		nS	VGS=0V
Qrr	Reverse Recovery Charge		1.2		μC	IS=2A,di/dt=100A/ μs

Notes:

^{* 1.} Repetitive rating, pulse width limited by maximum junction temperature.

^{* 2.} Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 1%



Typical Feature Curve

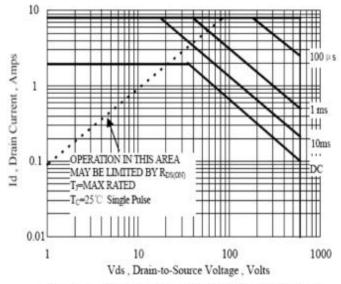


Figure 1 Maximum Forward Bias Safe Operating Area

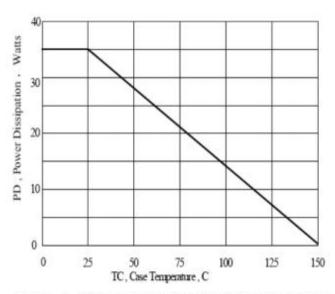


Figure 2 Maximum Power Dissipation vs Case Temperature

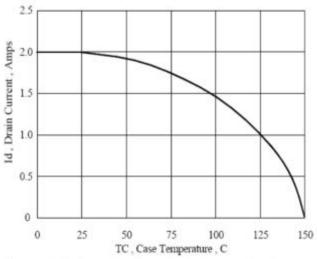
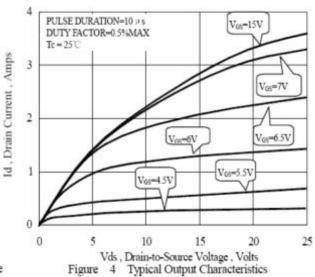


Figure 3 Maximum Continuous Drain Current vs Case Temperature



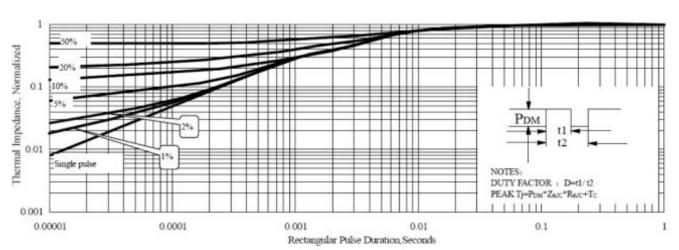


Figure 5 Maximum Effective Thermal Impendance, Junction to Case

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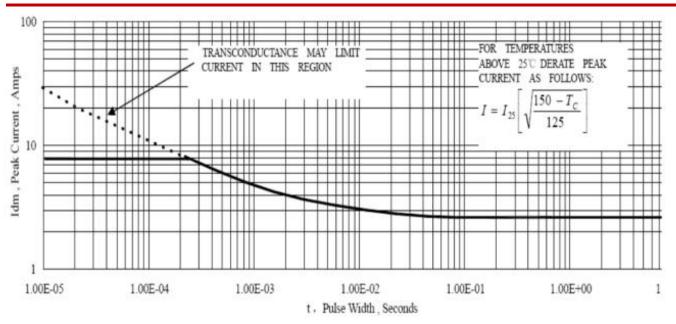


Figure 6 Maximum Peak Current Capability

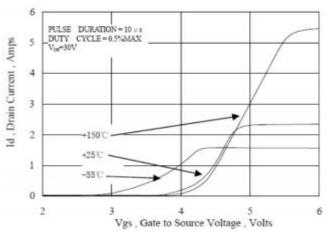


Figure 7 Typical Transfer Characteristics

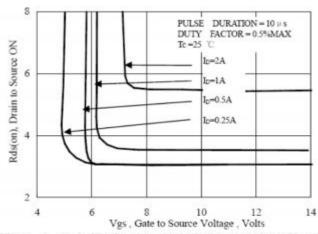


Figure 8 Typical Drain to Source ON Resistance vs Gate Voltage and Drain Current

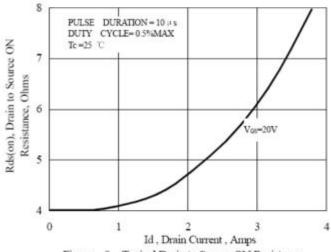


Figure 9 Typical Drain to Source ON Resistance vs Drain Current

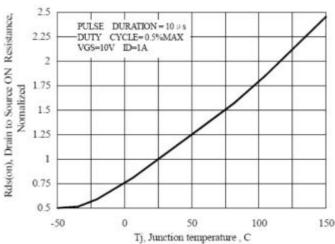
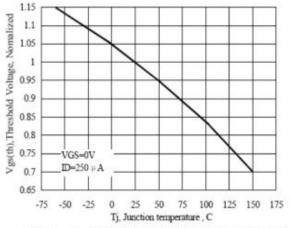


Figure 10 Typical Drian to Source on Resistance vs Junction Temperature

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11 Typical Theshold Voltage vs Junction Temperature

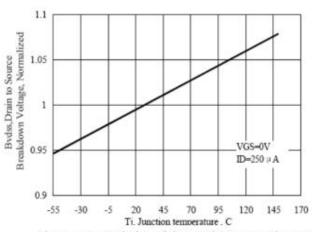


Figure Typical Breakdown Voltage vs Junction Temperature

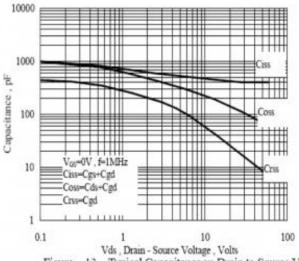


Figure 13 Typical Capacitance vs Drain to Source Voltage

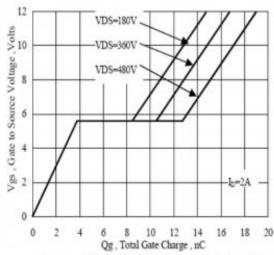


Figure 14 Typical Gate Charge vs Gate to Source Voltage

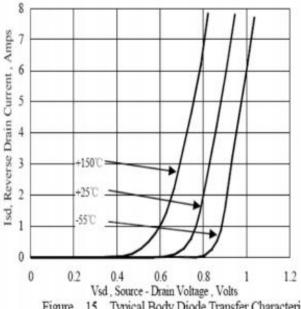
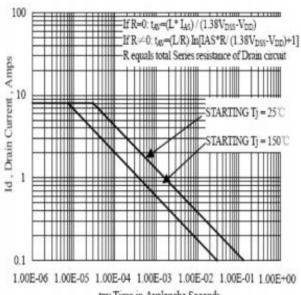


Figure 15 Typical Body Diode Transfer Characteristics



tay, Time in Avalanche, Seconds Unclamped Inductive Switching Capability



Test Circuits and Waveforms

Figure A: Gate Charge Test Circuit and Waveform

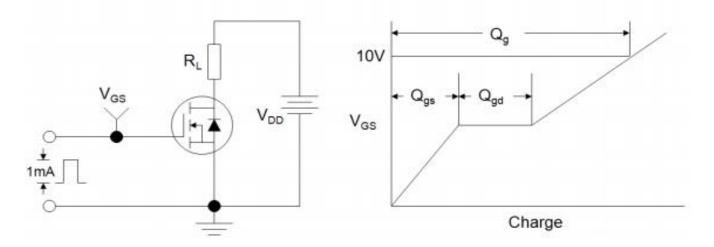


Figure B: Resistive Switching Test Circuit and Waveform

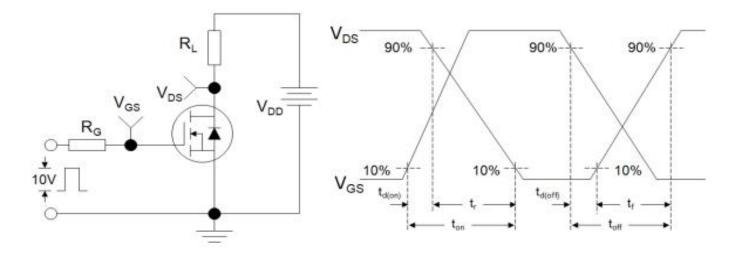
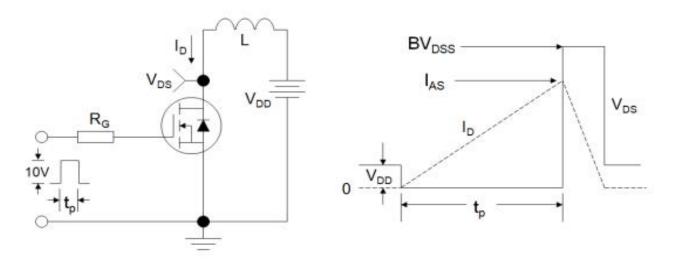
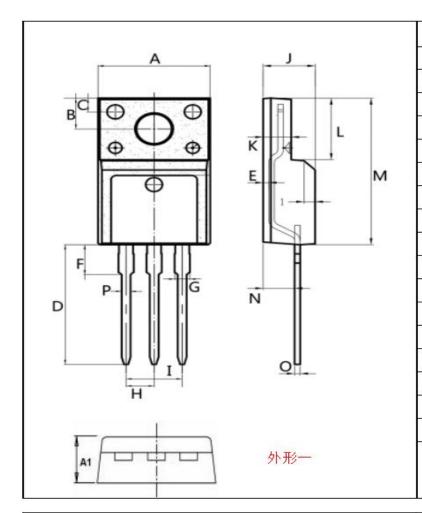


Figure C: Unclamped Inductive Switching Test Circuit and Waveform

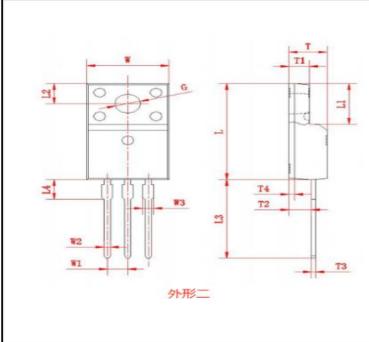




Package outline drawing(TO-220F Unit: mm)



Dim.	Min.	Max.
Α	9.95	10.36
A1	4.5	5.0
В	2.95	3.25
С	1.25	1.45
D	12.60	13.60
E	0.40	0.60
F	2.8	3.5
G	1.30	1.45
Н	(2.54	1)
1	(5.08	3)
J	4.60	4.75
K	2.45	2.65
L	6.5	6.8
М	15.4	16.0
N	2.25	3.05
0	0.45	0.55
Р	0.70	0.90



Dim.	Min.	Max.
W	9.95	10.36
W1	(2.5	4)
W2	0.70	0.90
W3	1.25	1.47
L	15.67	16.07
L1	6.48	6.88
L2	3.2	3.4
L3	12.6	13.6
L4	(3.23	3)
Т	4.50	4.90
T1	2.34	2.74
T2	2.25	2.95
Т3	0.45	0.60
T4	(0.	.70)
G	3.08	3.28

All Dimensions in millimeter



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DMN2080UCB4-7 DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 DMP22D4UFO-7B DMN1006UCA6-7 DMN16M9UCA6-7
STF5N65M6 IRF40H233XTMA1 STU5N65M6 DMN6022SSD-13 DMN13M9UCA6-7 DMTH10H4M6SPS-13 DMN2990UFB-7B
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