

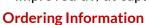
ID	R <sub>DS</sub> (ON)(Typ)	VDSS
3A	2.5Ω	500V

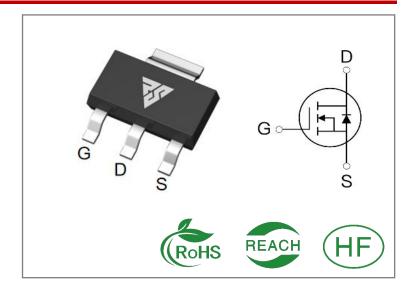
#### **Applications:**

- Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

#### **Features:**

- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability





Part Number	Package	Marking	Packing	Qty.
RS3N50C	SOT-223	RS3N50C	Tape&reel	4000 PCS

### Absolute Maximun Ratings Tc= 25 ℃ unless otherwise specified

Symbol	Parameter	RS3N50C	Units
VDSS	Drain-to-Source Voltage	500	V
ID	Continuous Drain Current	3	Δ.
IDM	Pulsed Drain Current (Note*1)	12	A
PD	Power Dissipation	5.56	W
VGS	Gate- to- Source Voltage	±30	V
EAS	Single Pulse Avalanche Engergy L = 10.0mH, VDD = 50V, RG = 25 $\Omega$	28.8	mJ
IAS	Avalanche Current (Note*1)	2.4	А
E <sub>AR</sub>	Repetitive Avalanche Energy (Note*1)	0.12	mJ
	Maximum Temperature for Soldering	300	
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	260	${\mathbb C}$
TJ and	Operating Junction and Storage	-55 to 150	
TSTG	Temperature Range	33 10 130	

<sup>\*</sup> Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" Table may cause permanent damage to the device.



### **Thermal Resistance**

Symbol	Parameter	RS3N50C	Units	Test Conditions
RθJC	Junction-to-Case	22.5	°C/ <b>W</b>	Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of + 1 5 0 $^{\circ}$ C
RθJA	Junction-to- Ambient	62.5		1 cubic foot chamber,free air.

## **OFF Characteristics** TJ= 25<sup>°</sup>C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BVDSS	Drain- to- source Breakdown Voltage	500			V	VGS=0V,ID=250μA
IDSS	Drain- to- Source Leakage Current			1	μΑ	VDS=500V,VGS=0 V
	Gate- to- Source Forward Leakage			100		VGS=30V,VDS=0V
IGSS	Gate- to- Source Reverse Leakage			-100	nA	VGS=-30V ,VDS=0 V

## ON Characteristics TJ=25 °C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain- to- Source On- Resistance(Note*2)		2.5	3	Ω	VGS=10V,ID=1.5A
VGS(TH)	Gate Threshold Voltage	3		4	V	VGS=VDS,ID=250μ A

## Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn- on Delay Time	-	33			
trise	Rise Time		5.5		C	VDS=250V
td(OFF)	Turn- OFF Delay Time		57		nS	ID=3A RG=25Ω
tfall	Fall Time		34			



## **Dynamic Characteristics** Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ciss	Input Capacitance		267			VGS=0V
Coss	Output Capacitance		35		pF	VDS=25V
Crss	Reverse Transfer Capacitance		7			f=1.0MHz
Qg	Total Gate Charge		9			VDS=400V
Qgs	Gate- to- Source Charge		1.3		nC	ID=3A
Qgd	Gate-to-Drain(" Miller") Charge		5.1			VGS=10V

### **Source-Drain Diode Characteristics**

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
IS	Continuous Source Current			2	А	Integral pn- diode
ISM	Maximum Pulsed Current			8	Α	in MOSFET
VSD	Diode Forward Voltage			1.4	٧	IS=1.5A,VGS=0V
trr	Reverse Recovery Time		325		nS	VGS=0V
Qrr	Reverse Recovery Charge		0.78		μC	IS=3A,di/dt=100A/ μs

### Notes:

<sup>\* 1.</sup> Repetitive rating, pulse width limited by maximum junction temperature.

<sup>\* 2.</sup> Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 1%



#### **Typical Feature Curve**

Figure 1. Output Characteristics (T, = 25°C) 5 10V 4.5 87 4 7V l<sub>p</sub>, Drain Current (A) 6V 3.5 5V 3 4.5V 2.5 2 1.5

10

V<sub>DS</sub> Drain-to-Source Voltage (V)

15

20

5

Figure 2. Body Diode Forward Voltage

100

T<sub>1</sub> = 150°C

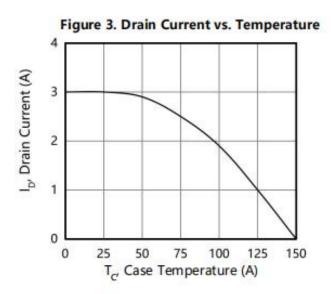
T<sub>2</sub> = 25°C

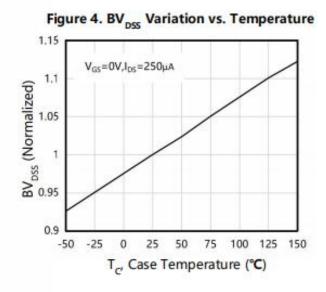
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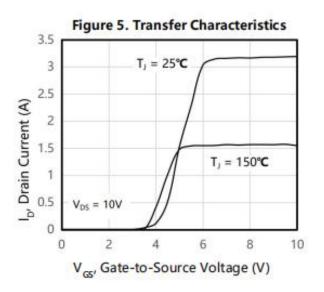
0.1

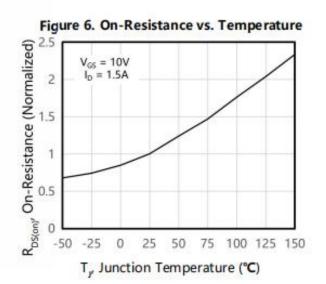
0.2 0.4 0.6 0.8 1 1.2 1.4 1.6

V<sub>SD'</sub> Source-to-Drain Voltage (V)

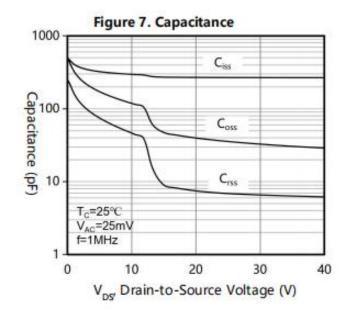












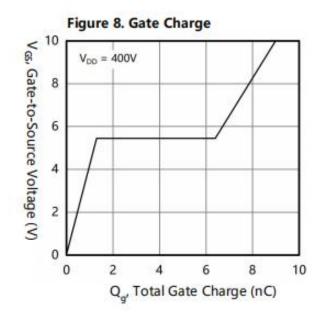
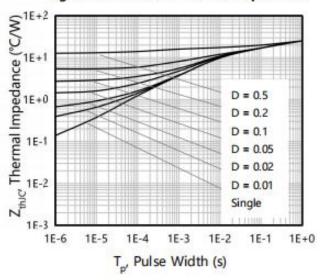


Figure 9. Transient Thermal Impedance





### **Test Circuits and Waveforms**

Figure A: Gate Charge Test Circuit and Waveform

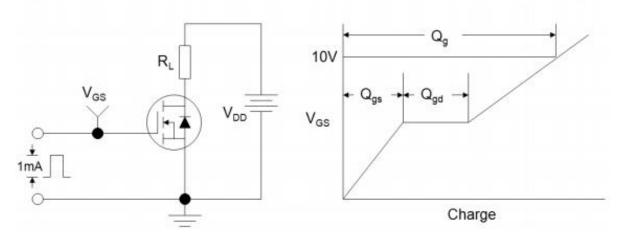


Figure B: Resistive Switching Test Circuit and Waveform

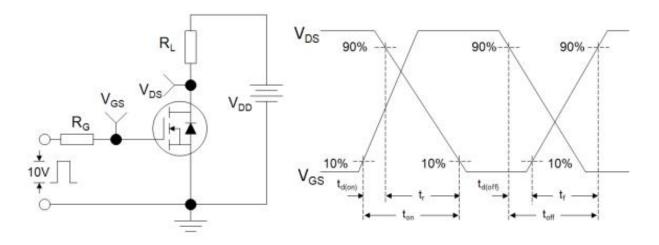
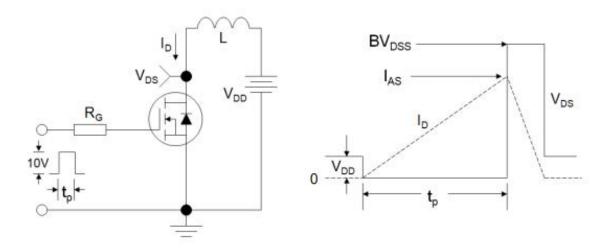


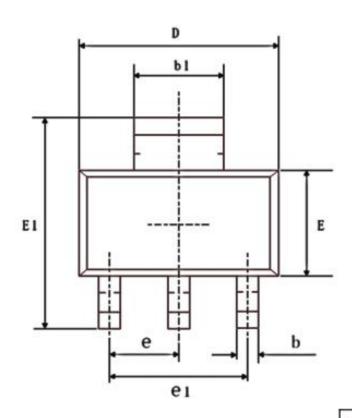
Figure C: Unclamped Inductive Switching Test Circuit and Waveform

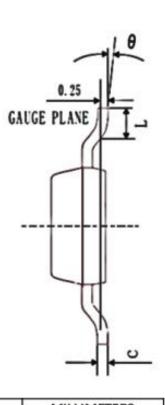


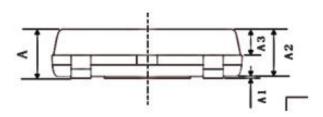


## Package outline drawing

# SOT-223







CVMADOLC	MILLIMETERS				
SYMBOLS	MIN				
Α	1.7.7.V	1.80			
A1	0.00	0.10			
A2	1.50	1.70			
A3	0.85	0.95			
b	0.66	0.80			
b1	2.96	3.10			
С	0.25	0.35			
D	6.30	6.70			
E	3.30	3.70			
E1	6.80	7.20			
e1	4.40	4.80			
L	0.90	1.15			
Θ	0.00	10.00			
е	2.3BSC				



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DMN2080UCB4-7 DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 DMP22D4UFO-7B DMN1006UCA6-7 DMN16M9UCA6-7
STF5N65M6 IRF40H233XTMA1 STU5N65M6 DMN6022SSD-13 DMN13M9UCA6-7 DMTH10H4M6SPS-13 DMN2990UFB-7B
IPB80P04P405ATMA2 2N7002W-G MCAC30N06Y-TP MCQ7328-TP NTMC083NP10M5L BXP7N65D BXP4N65F AOL1454G
WMJ80N60C4 BXP2N20L BXP2N65D BXT1150N10J BXT1700P06M TSM60NB380CP ROG RQ7L055BGTCR DMNH15H110SK3-13
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