

To our customers,

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## Old Company Name in Catalogs and Other Documents

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April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

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# DATA SHEET

# SILICON POWER TRANSISTOR 2SA1741

## PNP SILICON EPITAXIAL TRANSISTOR FOR HIGH-SPEED SWITCHING

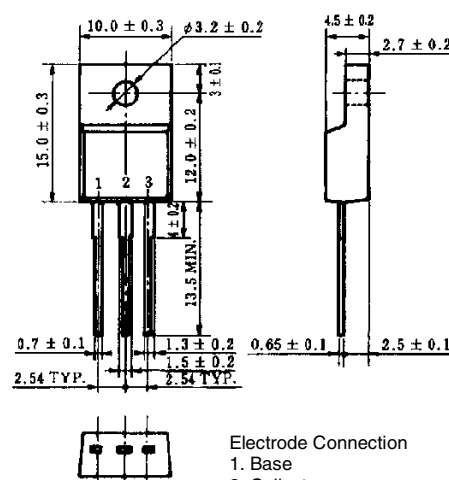
The 2SA1741 is a power transistor developed for high-speed switching and features a high  $h_{FE}$  at low  $V_{CE(sat)}$ . This transistor is ideal for use as a driver in DC/DC converters and actuators.

In addition, a small resin-molded insulation type package contributes to high-density mounting and reduction of mounting cost.

### FEATURES

- High  $h_{FE}$  and low  $V_{CE(sat)}$ :  
 $h_{FE} \geq 100$  ( $V_{CE} = -2\text{ V}$ ,  $I_C = -1\text{ A}$ )  
 $V_{CE(sat)} \leq 0.3\text{ V}$  ( $I_C = -3\text{ A}$ ,  $I_B = -0.15\text{ A}$ )
- Full-mold package that does not require an insulating board or bushing when mounting.

### PACKAGE DRAWING (UNIT: mm)



### ABSOLUTE MAXIMUM RATINGS ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Ratings	Unit
Collector to base voltage	$V_{CBO}$	-100	V
Collector to emitter voltage	$V_{CEO}$	-60	V
Emitter to base voltage	$V_{EBO}$	-7.0	V
Collector current (DC)	$I_{C(DC)}$	-5.0	A
Collector current (pulse)	$I_{C(pulse)^*}$	-10	A
Base current (DC)	$I_{B(DC)}$	-2.5	A
Total power dissipation	$P_T$ ( $T_c = 25^\circ\text{C}$ )	25	W
Total power dissipation	$P_T$ ( $T_a = 25^\circ\text{C}$ )	2.0	W
Junction temperature	$T_j$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

\*  $PW \leq 300\ \mu\text{s}$ , duty cycle  $\leq 50\%$

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**ELECTRICAL CHARACTERISTICS (Ta = 25°C)**

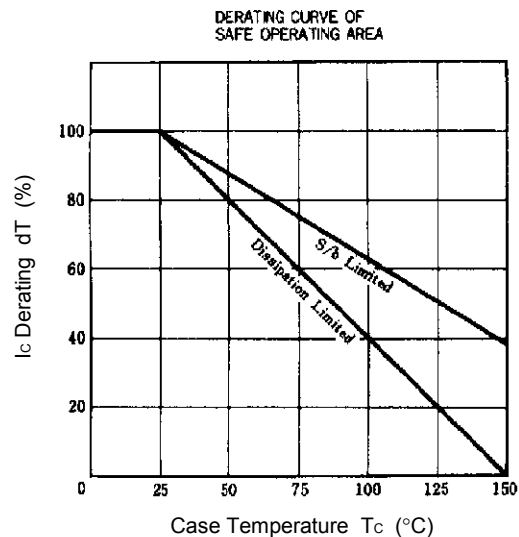
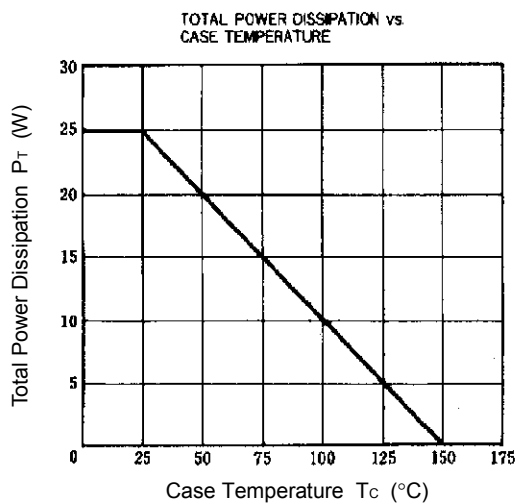
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector to emitter voltage	V <sub>CEO(SUS)</sub>	I <sub>C</sub> = -3.0 A, I <sub>B</sub> = -0.3 A, L = 1 mH	-60			V
Collector to emitter voltage	V <sub>CEX(SUS)</sub>	I <sub>C</sub> = -3.0 A, I <sub>B1</sub> = -I <sub>B2</sub> = -0.3 A, V <sub>BE(OFF)</sub> = 1.5 V, L = 180 μH, clamped	-60			V
Collector cutoff current	I <sub>CBO</sub>	V <sub>CB</sub> = -60 V, I <sub>E</sub> = 0			-10	μA
Collector cutoff current	I <sub>CER</sub>	V <sub>CE</sub> = -60 V, R <sub>BE</sub> = 50 Ω, Ta = 125°C			-1.0	mA
Collector cutoff current	I <sub>CX1</sub>	V <sub>CE</sub> = -60 V, V <sub>BE(OFF)</sub> = 1.5 V			-10	μA
Collector cutoff current	I <sub>CX2</sub>	V <sub>CE</sub> = -60 V, V <sub>BE(OFF)</sub> = 1.5 V, Ta = 125 °C			-1.0	mA
Emitter cutoff current	I <sub>EBO</sub>	V <sub>EB</sub> = -5.0 V, I <sub>C</sub> = 0			-10	μA
DC current gain	h <sub>FE1</sub> *	V <sub>CE</sub> = -2.0 V, I <sub>C</sub> = -0.5 A	100			
DC current gain	h <sub>FE2</sub> *	V <sub>CE</sub> = -2.0 V, I <sub>C</sub> = -1.0 A	100		400	
DC current gain	h <sub>FE3</sub> *	V <sub>CE</sub> = -2.0 V, I <sub>C</sub> = -3.0 A	60			
Collector saturation voltage	V <sub>CE(sat)1</sub> *	I <sub>C</sub> = -3.0 A, I <sub>B</sub> = -0.15 A			-0.3	V
Collector saturation voltage	V <sub>CE(sat)2</sub> *	I <sub>C</sub> = -4.0 A, I <sub>B</sub> = -0.2 A			-0.5	V
Base saturation voltage	V <sub>BE(sat)1</sub> *	I <sub>C</sub> = -3.0 A, I <sub>B</sub> = -0.15 A			-1.2	V
Base saturation voltage	V <sub>BE(sat)2</sub> *	I <sub>C</sub> = -4.0 A, I <sub>B</sub> = -0.2 A			-1.5	V
Collector capacitance	C <sub>ob</sub>	V <sub>CB</sub> = -10 V, I <sub>E</sub> = 0, f = 1.0 MHz		130		pF
Gain bandwidth product	f <sub>T</sub>	V <sub>CE</sub> = -10 V, I <sub>C</sub> = -0.5 A		80		MHz
Turn-on time	t <sub>on</sub>	I <sub>C</sub> = -3.0 A, R <sub>L</sub> = 17 Ω, I <sub>B1</sub> = -I <sub>B2</sub> = -0.15 A, V <sub>CC</sub> ≅ -50 V Refer to the test circuit.			0.3	μs
Storage time	t <sub>stg</sub>				1.5	μs
Fall time	t <sub>f</sub>				0.3	μs

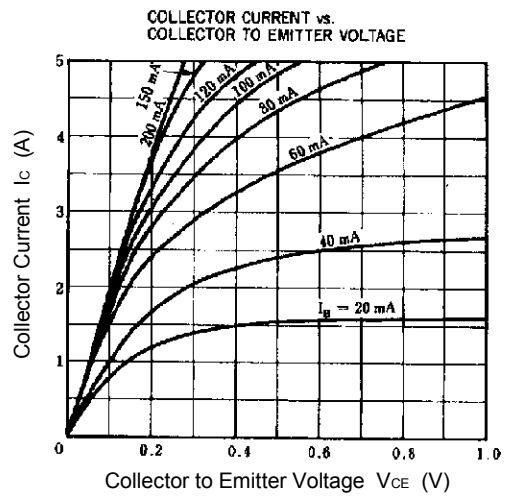
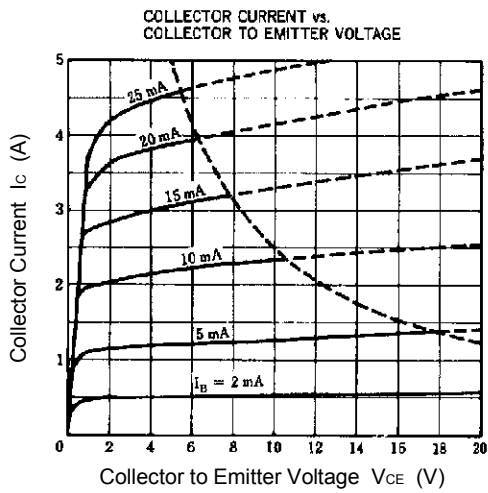
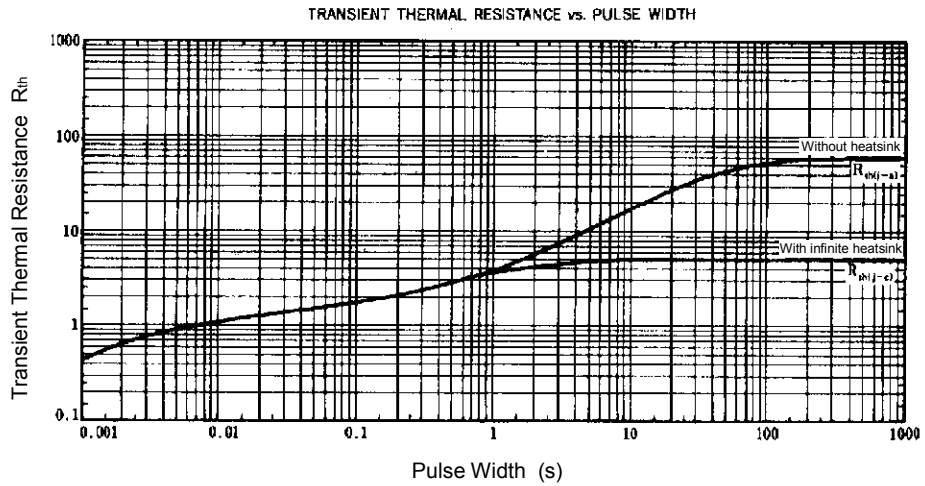
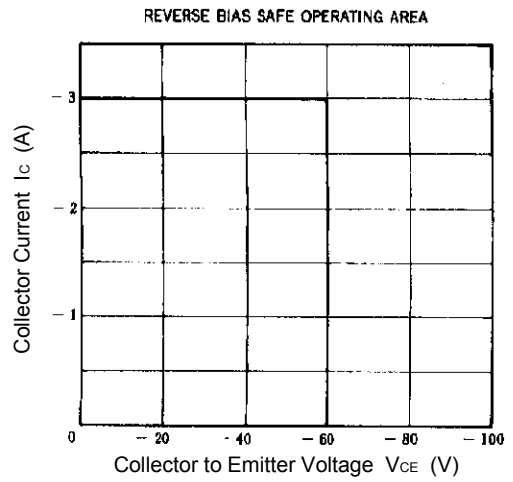
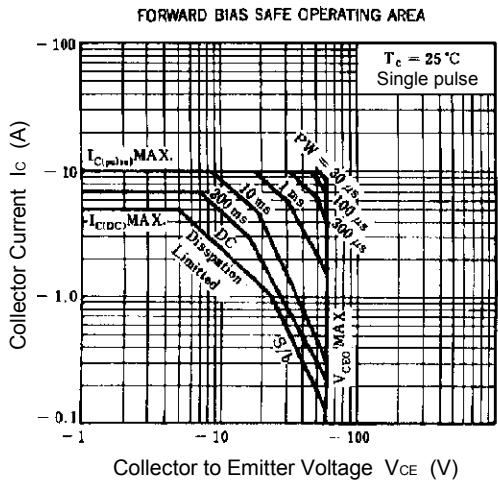
\* Pulse test PW ≤ 350 μs, duty cycle ≤ 2%

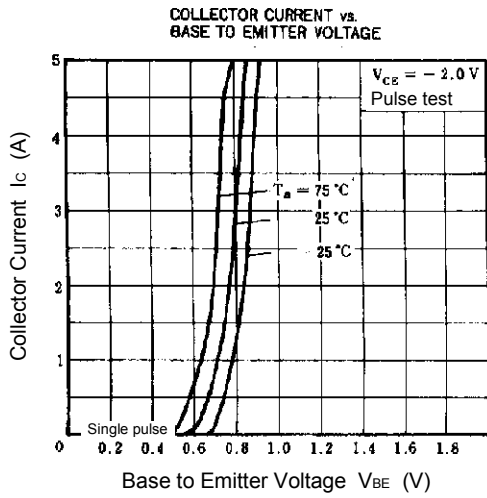
**h<sub>FE</sub> CLASSIFICATION**

Marking	M	L	K
h <sub>FE2</sub>	100 to 200	150 to 300	200 to 400

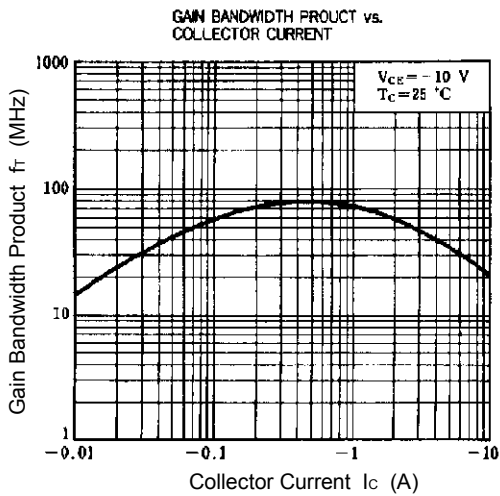
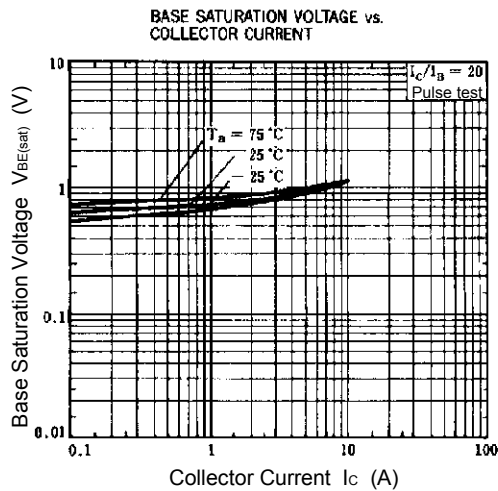
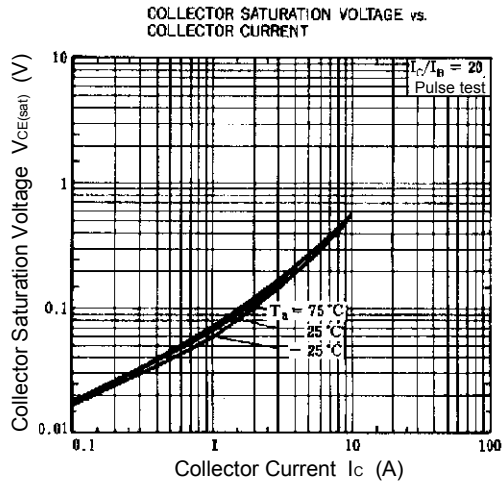
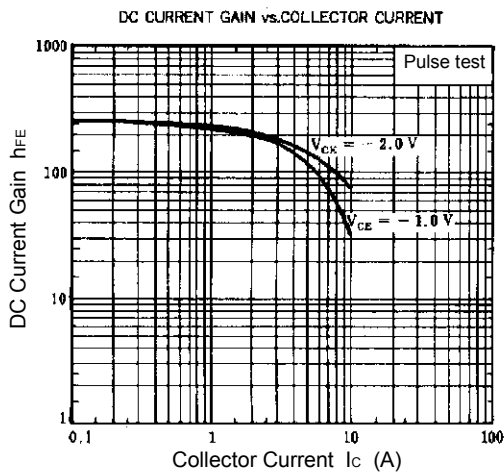
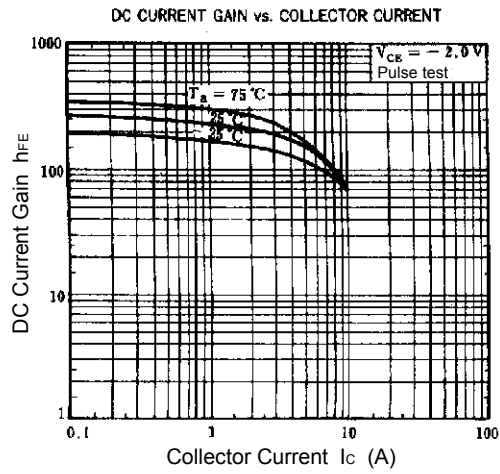
**TYPICAL CHARACTERISTICS (Ta = 25°C)**

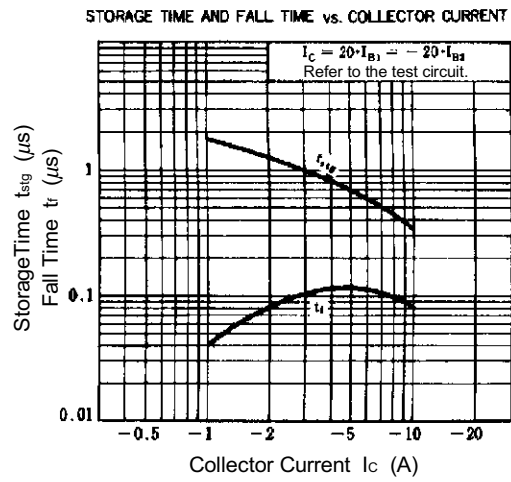
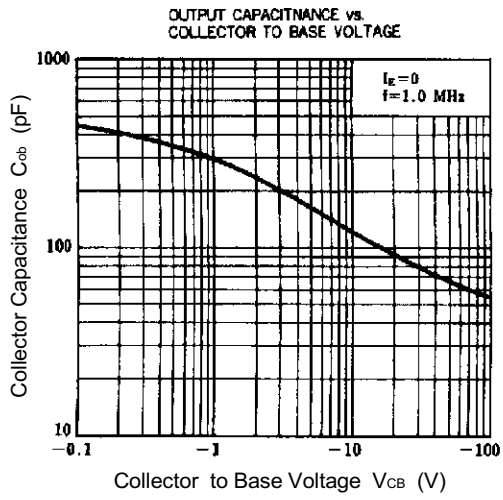




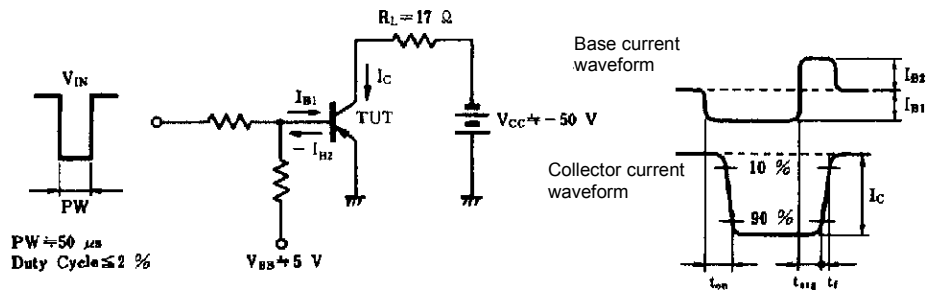


Pulse test





SWITCHING TIME ( $t_{on}$ ,  $t_{stg}$ ,  $t_f$ ) TEST CIRCUIT



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