

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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SILICON POWER TRANSISTOR  
**2SD1899-Z**

**NPN SILICON EPITAXIAL TRANSISTOR**

**DESCRIPTION**

The 2SD1899-Z is designed for Audio Frequency Amplifier and Switching, especially in Hybrid Integrated Circuits.

**FEATURES**

- High  $h_{FE}$ :  $h_{FE} = 100$  to 400
- Low  $V_{CE(sat)}$ :  $V_{CE(sat)} \leq 0.25$  V

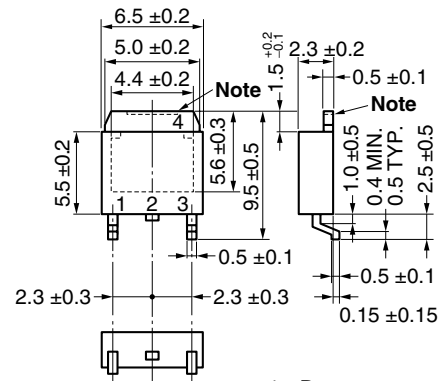
**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ )**

Collector to Base Voltage	$V_{CBO}$	60	V
Collector to Emitter Voltage	$V_{CEO}$	60	V
Base to Emitter Voltage	$V_{EBO}$	7.0	V
Collector Current (DC)	$I_{C(DC)}$	3.0	A
Collector Current (pulse) <sup>Note 1</sup>	$I_{C(pulse)}$	5.0	A
Base Current (DC)	$I_{B(DC)}$	0.5	A
Total Power Dissipation ( $T_A = 25^\circ\text{C}$ ) <sup>Note 2</sup>	$P_{T1}$	2.0	W
Total Power Dissipation ( $T_C = 25^\circ\text{C}$ )	$P_{T2}$	10	W
Junction Temperature	$T_j$	150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

**Notes 1.**  $PW \leq 10$  ms, Duty Cycle  $\leq 50\%$

**2.** When mounted on ceramic substrate of  $7.5\text{ cm}^2 \times 0.7\text{ mm}$

**PACKAGE DRAWING (Unit: mm)**



1. Base
2. Collector
3. Emitter
4. Collector Fin

TO-252 (MP-3Z)

**Note** The depth of notch at the top of the fin is from 0 to 0.2 mm.

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**ELECTRICAL CHARACTERISTICS (T<sub>a</sub> = 25 °C)**

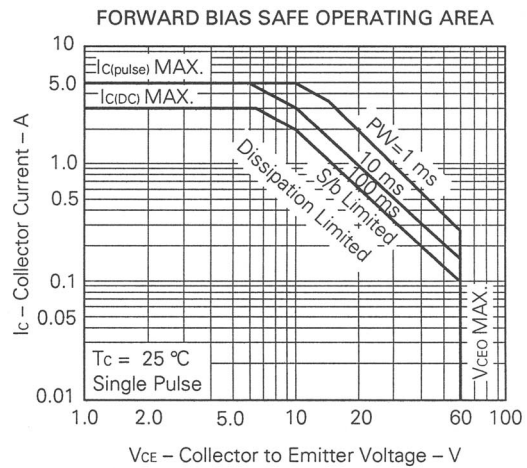
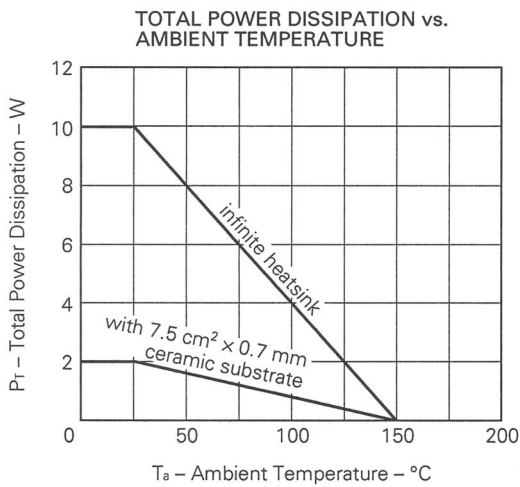
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Collector Cutoff Current	I <sub>CEO</sub>			10	μA	V <sub>CE</sub> = 60 V, I <sub>E</sub> = 0
Emitter Cutoff Current	I <sub>EB0</sub>			10	μA	V <sub>EB</sub> = 7.0 V, I <sub>C</sub> = 0
DC Current Gain	h <sub>FE1</sub> *	60				V <sub>CE</sub> = 2.0 V, I <sub>C</sub> = 0.2 A
DC Current Gain	h <sub>FE2</sub> *	100		400		V <sub>CE</sub> = 2.0 V, I <sub>C</sub> = 0.6 A
DC Current Gain	h <sub>FE3</sub> *	50				V <sub>CE</sub> = 2.0 V, I <sub>C</sub> = 2.0 A
Collector Saturation Voltage	V <sub>CE(sat)</sub> *		0.14	0.25	V	I <sub>C</sub> = 1.5 A, I <sub>B</sub> = 0.15 A
Base Saturation Voltage	V <sub>BE(sat)</sub> *		0.93	1.2	V	I <sub>C</sub> = 1.5 A, I <sub>B</sub> = 0.15 A
Gain Bandwidth Product	f <sub>T</sub>		120		MHz	V <sub>CE</sub> = 5.0 V, I <sub>E</sub> = -1.5 A
Output Capacitance	C <sub>ob</sub>		30		pF	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1.0 MHz
Turn-on Time	t <sub>on</sub>		0.15	0.5	μs	I <sub>C</sub> = 1 A, V <sub>CC</sub> = 10 V, R <sub>L</sub> = 10 Ω I <sub>B1</sub> = -I <sub>B2</sub> = 0.1 A
Storage Time	t <sub>stg</sub>		0.75	2.0	μs	
Fall Time	t <sub>f</sub>		0.2	0.5	μs	

\* Pulsed: PW ≤ 350 μs, Duty Cycle ≤ 2 %

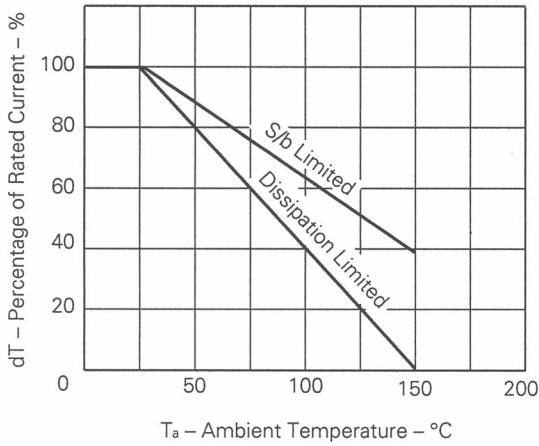
**h<sub>FE</sub> Classification**

MARKING	M	L	K
h <sub>FE2</sub>	100 to 200	160 to 320	200 to 400

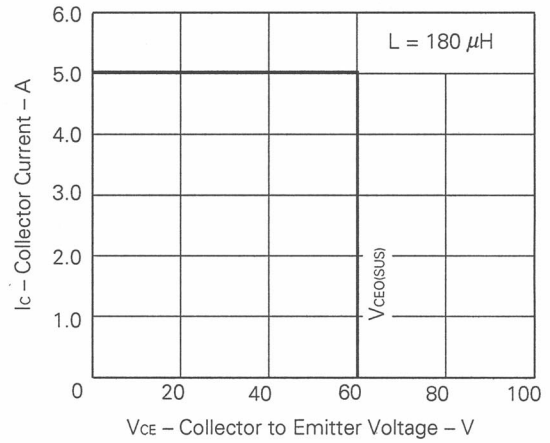
**TYPICAL CHARACTERISTICS (T<sub>a</sub> = 25 °C)**



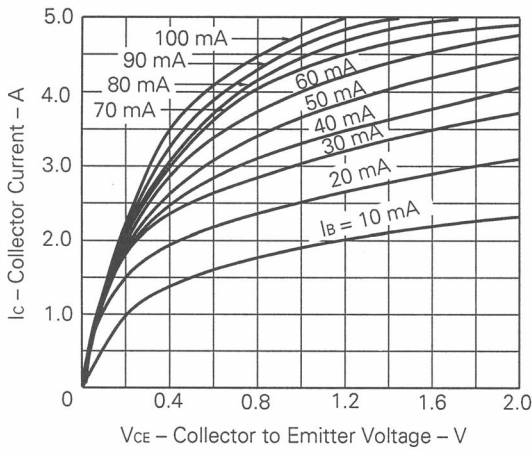
DERATING CURVE OF SAFE OPERATING AREA



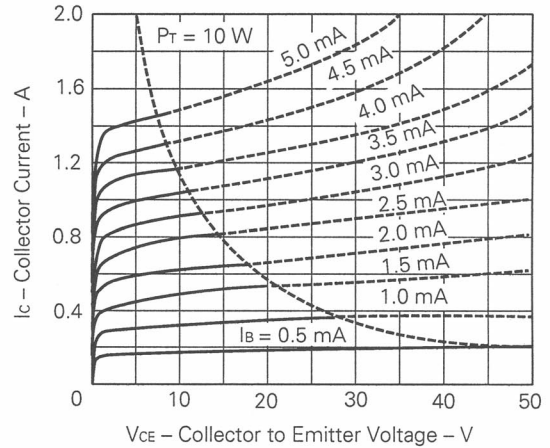
REVERSE BIAS SAFE OPERATING AREA



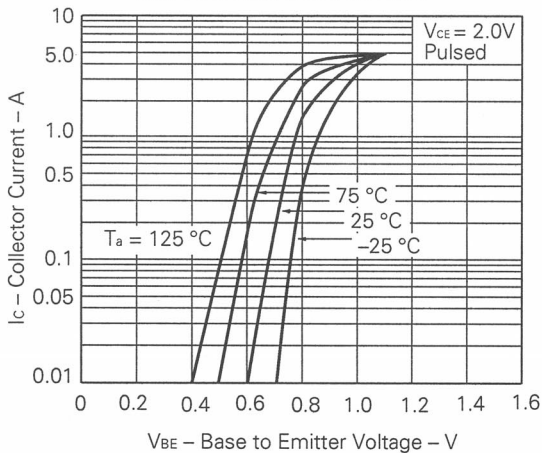
COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



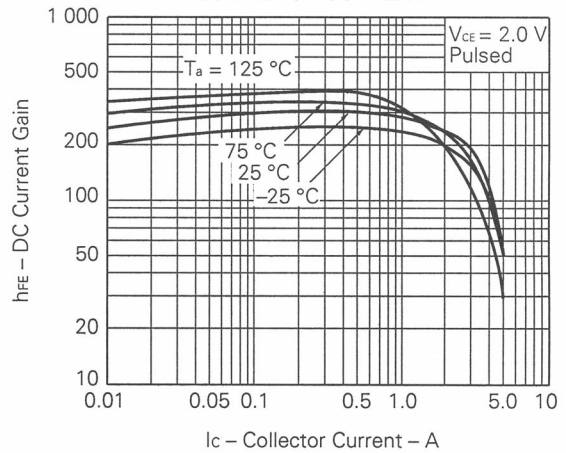
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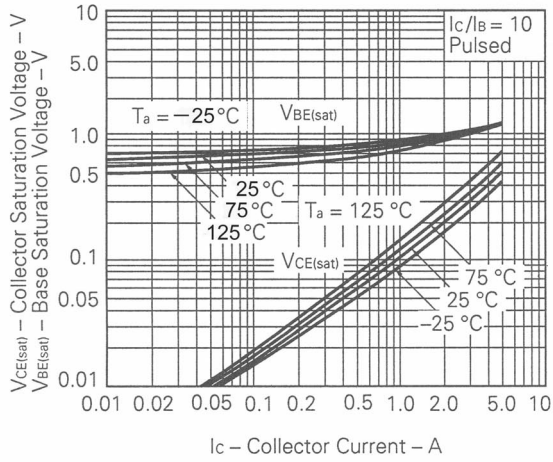
COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE



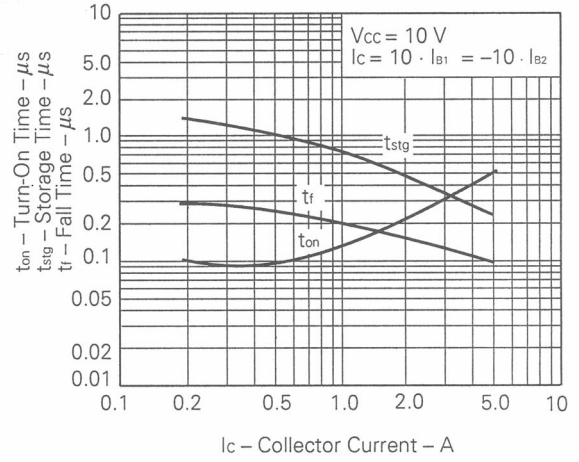
DC CURRENT GAIN vs. COLLECTOR CURRENT



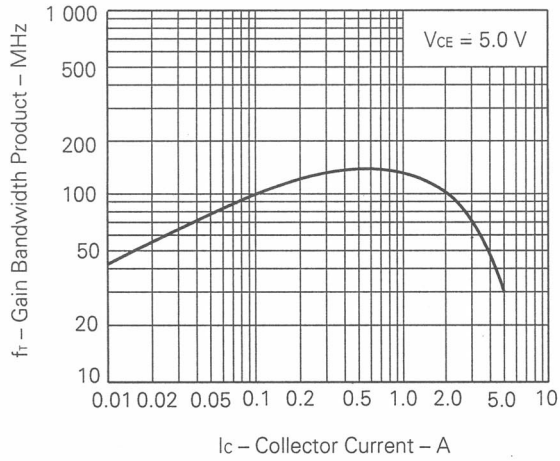
<R> BASE AND COLLECTOR SATURATION VOLTAGE vs. COLLECTOR CURRENT



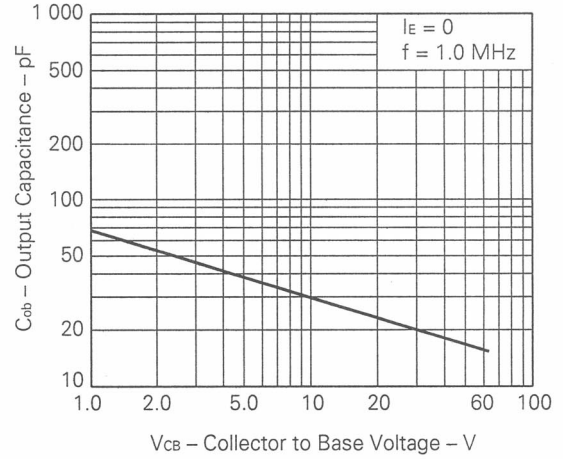
FALL, STORAGE AND TURN ON TIME vs. COLLECTOR CURRENT



GAIN BANDWIDTH PRODUCT vs. COLLECTOR CURRENT



OUTPUT CAPACITANCE vs. COLLECTOR TO BASE VOLTAGE



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