The ISL8205M power module is a single channel synchronous step-down complete power supply, capable of delivering up to 5 A of continuous current. Operating from a single 2.6 V to 5.5 V input power rail and integrating a controller, power inductor and MOSFETs, ISL8205M can achieve up to $95 \%$ conversion efficiency. It also provides fast transient response with excellent loop stability as well as deliver output voltage as low as 0.6 V . Switching frequency is also adjustable from 680 kHz to 3.5 MHz with either external resistor or SYNC clock option. Selectable PFM mode can also be enabled to boost up light load efficiency to extend battery life. Other features include programmable soft-start, soft-stop, input undervoltage lockout, $100 \%$ duty cycle operation, over-temperature, overcurrent/short-circuit with hiccup mode, overvoltage and negative overcurrent protection. It also has a dedicated enable pin and power-good flag that allow for easy system power rails sequencing.

The ISL8205MEVAL1Z evaluation board is designed to demonstrate the performance of the ISL8205M. The board is by default set to be 1.2 V output voltage with 1.6 MHz switching frequency. Other output voltage values can be easily set by changing the jumper position. Switching frequency can be adjusted by changing the FS pin resistor.

## References

- ISL8205M datasheet


## Features

- 2.6 V to 5.5 V input voltage range
- Adjustable output voltage as low as 0.6 V with $\pm 1.6 \%$ accuracy over line/load/temperature
- Default 1.8 MHz current mode control operations
- 680 kHz to 3.5 MHz resistor adjustable
- External synchronization up to 3.5 MHz
- Selectable light-load efficiency mode
- 100\% duty cycle LDO mode
- Programmable soft-start and soft-stop output discharge
- Dedicated enable pin and power-good flag
- UVLO, over-temperature, overcurrent, overvoltage and negative overcurrent protections


## Specifications/Default Set-Up

- $\mathrm{V}_{\mathrm{IN}}=2.6 \mathrm{~V}$ to 5.5 V
- $\mathrm{V}_{\text {OUT }}$ options (via jumper selection): $1 \mathrm{~V}, 1.2 \mathrm{~V}, 2.5 \mathrm{~V}, 3.3 \mathrm{~V}$
- MAX I ${ }_{\text {OUT }}$ up to 5A
- $\mathrm{f}_{\mathrm{SW}}=1.6 \mathrm{MHz}$ by default
- Set to PWM mode by default
- 1 ms soft-start time by default

Ordering Information

| PART NUMBER | DESCRIPTION |
| :---: | :--- |
| ISL8205MEVAL1Z | ISL8205M single 5A power module evaluation <br> board |



FIGURE 1. ISL8205MEVALIZ BLOCK DIAGRAM

TABLE 1. ISL8205M COMPONENT SELECTION GUIDE MATRIX

| $\mathrm{V}_{\mathrm{IN}}(\mathrm{V})$ | $\mathrm{V}_{\text {OUT }}(\mathrm{V})$ | $\mathrm{f}_{\text {SW }}(\mathrm{MHz})$ | $\mathrm{C}_{\text {IN }}(\mu \mathrm{F})$ | $\mathrm{C}_{\text {OUT }}(\mu \mathrm{F})$ | $\mathrm{R}_{7}(\mathrm{k} \Omega)$ | $\mathbf{R}_{\text {SET }}(\mathbf{k} \Omega$ ) | $\mathrm{C}_{\mathrm{FF}}(\mathrm{pF})$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 5 | 0.6 | 0.8 | 2x22 | 1x100 | 261 | OPEN | 390 |
| 5 | 0.9 | 1.2 | 2x22 | $3 \times 22$ | 169 | 200 | 560 |
| 5 | 1 | 1.3 | 2x22 | $3 \times 22$ | 154 | 150 | 560 |
| 5 | 1 | 1.3 | 2x22 | 2x22 | 154 | 150 | 820 |
| 5 | 1.2 | 1.6 | 2×22 | 2x22 | 124 | 100 | 820 |
| 5 | 1.5 | 1.7 | 2x22 | 2x22 | 115 | 66.5 | 560 |
| 5 | 1.8 | 2 | 2×22 | 2x22 | 95.3 | 49.9 | 470 |
| 5 | 2.5 | 2.5 | 2x22 | 2x22 | 75 | 31.6 | 330 |
| 5 | 3.3 | 3 | 2x22 | 2x22 | 59 | 22.1 | 330 |
| 3.3 | 0.6 | 0.8 | 2x22 | 1x100 | 261 | OPEN | 390 |
| 3.3 | 0.9 | 1.2 | 2x22 | $3 \times 22$ | 169 | 200 | 560 |
| 3.3 | 1 | 1.3 | 2x22 | $3 \times 22$ | 154 | 150 | 560 |
| 3.3 | 1 | 1.3 | 2x22 | 2x22 | 154 | 150 | 820 |
| 3.3 | 1.2 | 1.6 | 2x22 | 2x22 | 124 | 100 | 820 |
| 3.3 | 1.5 | 1.7 | 2x22 | 2x22 | 115 | 66.5 | 560 |
| 3.3 | 1.8 | 2 | 2x22 | 2x22 | 95.3 | 49.9 | 470 |
| 3.3 | 2.5 | 2.5 | 2x22 | 2x22 | 75 | 31.6 | 330 |



FIGURE 2. TOP OF BOARD

## Recommended Equipment

- $0 V$ to 5.5 V power supply with at least 7A source current capability
- Electronic load capable of sinking current up to 5A
- Digital Multimeters (DMMs)
- 100MHz quad-trace oscilloscope


## Functional Description

The ISL8205M is a single 5A step-down high efficiency power module optimized for FPGA, DSP and Li-ion battery power devices. The module switches at 1.8 MHz by default when the FS pin is shorted to VIN. The switching frequency is also adjustable from 680 kHz to 3.5 MHz through a resistor from FS to SGND. To


FIGURE 3. BOTTOM OF BOARD
boost light-load efficiency, ISL8205M can also be configured to operate in PFM mode by pulling the SYNC pin to SGND. Peak current mode control scheme is implemented for fast transient response. The module utilizes internal compensation to stabilize the system and optimize transient response, which greatly simplifies the application. Other excellent features include external synchronization, $100 \%$ duty cycle operation and very low quiescent current. For further information, please refer to the ISL8205M datasheet.

## Quick Start

The ISL8205MEVAL1Z is set to be $\mathrm{V}_{\text {OUT }}=1.2 \mathrm{~V}, \mathrm{f}_{\mathrm{SW}}=1.6 \mathrm{MHz}$ by default. For other $\mathrm{V}_{\text {OUT }}$ options, the recommended $\mathrm{f}_{\mathrm{SW}}$ needs to be adjusted by changing resistor $\left(\mathrm{R}_{7}\right)$ connected to the FS pin.

Please refer to Table 1 for more information. The following operation procedure will be based on default setting.

1. Before applying power to input, ensure one shorting jumper on J 1 across pins 5 and 6 is present. This selects $\mathrm{V}_{\text {OUT }}=1.2 \mathrm{~V}$ option.
2. Set the ENABLE switches SW1 to the "OFF" position.
3. Connect the positive of a power supply to the VIN connector and the negative of the power supply to the PGND connector. Make sure the power supply is not enabled when making connections and the input power supply voltage is set to a value between 2.6 V and 5.5 V .
4. Turn the power supply on.
5. Turn ENABLE switch SW1 to the "ON" position to enable module operation.
6. The power-good LED should illuminate in green if the module is operating properly.
7. Measure output voltage, $\mathrm{V}_{\text {OUT }}$, at test points VOUT and PGND. Module output should be 1.2 V .

## Programming the Output Voltage

The ISL8205MEVAL1Z evaluation board has several preset output voltages, $1.0 \mathrm{~V}, 1.2 \mathrm{~V}, 2.5 \mathrm{~V}$ and 3.3 V , which can be selected in J1 jumper. Other $\mathrm{V}_{\text {OUT }}$ values other than on-board options can be set by proper selection of the resistor ( $\mathrm{R}_{\mathrm{SET}}$ ) connecting from FB to SGND. In this case, $\mathrm{R}_{\text {SET }}$ for default options ( $\mathrm{R}_{9}, \mathrm{R}_{10}, \mathrm{R}_{11}$, $R_{12}$ ) may need to be changed. The output voltage is governed by Equation 1.
$V_{\text {OUT }}=V_{\text {REF }} \cdot \frac{R_{\text {SET }}+100 k \Omega}{R_{\text {SET }}}$

For most popular $\mathrm{V}_{\text {OUT }}$ values and corresponding $\mathrm{R}_{\text {SET }}$ values, please refer to Table 1 for more information.

## Feed-Forward Capacitor Selection

In typical applications where the output capacitors are all ceramic, a feed-forward capacitor, $\mathrm{C}_{\mathrm{FF}}$ (as annotated as $\mathrm{C}_{12}, \mathrm{C}_{13}, \mathrm{C}_{14}, \mathrm{C}_{15}$ in schematic) is needed to be put in parallel with each $R_{\text {SET }}$ to insure loop stability in extreme operating conditions. With internal compensation mode enabled, the $\mathrm{C}_{\mathrm{FF}}$ values for typical operating conditions are optimized and listed in Table 1. And that's how $\mathrm{C}_{12}$, $\mathrm{C}_{13}, \mathrm{C}_{14}, \mathrm{C}_{15}$ default values are selected. Please note that, for system parameters that are different from Table 1 or external instead of internal compensation is used, the optimized value of $\mathrm{C}_{\mathrm{FF}}$ needs to be adjusted.

## Frequency Adjust

The switching frequency of ISL8205M is adjustable ranging from 680 kHz to 3.5 MHz via a simple resistor $\mathrm{R}_{\mathrm{FS}}$ (as shown in $\mathrm{R}_{7}$ on ISL8205MEVAL1Z board) across FS to SGND. The switching frequency setting is based on Equation 2:
$\mathrm{R}_{\mathrm{FS}}[\mathrm{k} \Omega]=\frac{220 \cdot 10^{3}}{\mathrm{f}_{\mathrm{OSC}}[\mathrm{kHz}]}-14$
When the FS pin is directly tied to VIN, the frequency of operation is fixed at 1.8 MHz . For a recommended switching frequency with typical operation conditions, refer to Table 1. More detailed
information on recommended switching frequency and $\mathrm{f}_{\mathrm{Sw}}$ selection range is provided in "Recommended Switching Frequency" and "Operation Range".

## Recommended Switching Frequency

Selection of switching frequency for each $\mathrm{V}_{\text {IN }}$ and $\mathrm{V}_{\text {OUT }}$ combination needs to take into account a few trade-offs. Generally, lower switching frequency will lead to higher efficiency. However, switching frequency should not be decreased too low due to negative current protection limit. Moreover, when output voltage is relatively high, low switching frequency will result in more sub-harmonic oscillation. Therefore, operating frequency needs to be kept relatively high under high $\mathrm{V}_{\text {OUT }}$ conditions. However, again, switching frequency cannot be increased too much. Otherwise, the minimum on-time limit could be violated. Based on these considerations, Figure 4 provides the recommended switching frequency under various typical $\mathrm{V}_{\mathrm{IN}}$ and across $\mathrm{V}_{\text {OUT }}$ ranges.


FIGURE 4. SWITCHING FREQUENCY RECOMMENDATION

## Operation Range

By default, the ISL8205MEVAL1Z is configured to be operating at $\mathrm{V}_{\text {OUT }}=1.2 \mathrm{~V}$ ( J 1 jumper position is at pins 5 and 6 ), $\mathrm{f}_{\mathrm{SW}}=1.6 \mathrm{MHz}$ conditions. $\mathrm{V}_{\text {IN }}$ ranges from 2.6 V to 5.5 V . The board can also support a wider operating range to meet the requirement of specific applications. The $\mathrm{V}_{\text {OUT }}$ can be adjusted from 0.6 V to 5 V . Load current range is from $0 A$ to 5 A . Note that, for continuous operation at 5A, airflow across the board may be needed. The $\mathrm{f}_{\mathrm{SW}}$ can also be tuned. However, to ensure sufficient stability margins, switching frequency can only be adjusted within the safe operatiing regions represented with Figures 5 through 9. Operating outside of these areas may lead to system instability.


FIGURE 5. OPERATION REGION $V_{I N}=5.5 \mathrm{~V}$


FIGURE 7. OPERATION REGION $V_{I N}=4.0 \mathrm{~V}$


FIGURE 6. OPERATION REGION $\mathrm{V}_{\mathrm{IN}}=5.0 \mathrm{~V}$


FIGURE 8. OPERATION REGION $\mathrm{V}_{\mathrm{IN}}=3.3 \mathrm{~V}$


FIGURE 9. OPERATION REGION $\mathrm{V}_{\mathrm{IN}}=\mathbf{2 . 6 V}$

## PWM/PFM Selection

By default, ISL8205MEVAL1Z is set to operate in PWM mode. This is achieved by connecting SYNC to VIN through $\mathrm{R}_{1}$. Pulling the SYNC pin LOW, forces the module into PFM mode, which enables pulse-skipping at light load to minimize the switching loss by reducing the switching frequency. Refer to ISL8205M datasheet for more detailed information. For ISL8205MEVAL1Z, PFM mode can be enabled by removing $R_{1}$ and shorting SYNC to SGND by putting a $0 \Omega$ resistor on $\mathrm{R}_{2}$.

## Soft Start-Up

The soft start-up reduces the inrush current during the start-up. For ISL8205MEVAL1Z, the soft start-up time is set to 1 ms by default through $\mathrm{C}_{6}$. However, start-up time can be adjusted by tuning the $\mathrm{C}_{6}$ value. Equation 3 can be used to determine the $\mathrm{C}_{6}$ value for the target soft-start time $\mathrm{t}_{\mathrm{ss}}$.

$$
\begin{equation*}
\mathrm{C}_{6}[\mu \mathrm{~F}]=3.1 \times \mathrm{t}_{\mathrm{SS}}[\mathrm{~S}] \tag{EQ.3}
\end{equation*}
$$

## Evaluation Board Information

The ISL8205MEVAL1Z evaluation board is a $2 \times 2 \mathrm{in}^{2}$ four-layer FR-4 board with 2oz. copper on all the layers. The board can be used as a single 5A reference design. Refer to Figures 11 through 14 for board layout information. The board is designed with mechanical switches for ENABLE, power-good LED indicators, several connectors, test points and jumpers, which make testing the board easy.

## Thermal Considerations and Current Derating

Board layout is very critical in order to make the module operate safely and deliver maximum allowable power. In order for the board to operate properly in the high ambient temperature environments and carry full load currents, the board layout needs to be carefully designed to maximize thermal performance. To achieve this, select enough trace width, copper weight and proper connectors.

The ISL8205MEVAL1Z evaluation board is capable of operating at 5A full load current at room temperature with plenty of safety margin for junction temperature. However, if the board is to operate at elevated ambient temperatures, then the available output current may need to be derated. Refer to the derated current curves in the ISL8205M datasheet to determine the maximum output current the evaluation board can supply.


FIGURE 10. SCHEMATIC

## Bill of Materials

| REFERENCE DESIGNATOR | QTY | MANUFACTURER | PART NUMBER | DESCRIPTION |
| :---: | :---: | :---: | :---: | :---: |
| C16 | 1 | MURATA | GRM21BR71A475KA73L | CAP, SMD, 0805, 4.7 $\mathrm{F}, 10 \mathrm{~V}, 10 \%$, X7R, ROHS |
| C12 | 1 | TDK | CGA2B2C0G1H331J050BA | CAP, SMD, 0402, 330pF, 50V, $5 \%$, NPO, ROHS |
| c6 | 1 | PANASONIC | ECU-E1E332KBQ | CAP, SMD, 0402, 3300pF, 25V, 10\%, X7R, ROHS |
| C13 | 1 | YAGEO | CCO402JRNPO9BN391 | CAP, SMD, 0402, 390pF, 50V, 5\%, NPO, ROHS |
| C14, C15 | 2 | SAMSUNG | CL05C821JB5NNNC | CAP, SMD, 0402, 820pF, 50V, 5\%, NPO, ROHS |
| C4, C5 | 0 |  |  | CAP, SMD, 0402, DNP-PLACE HOLDER, ROHS |
| c2, c3, c7, c8 | 4 | TAIYO YUDEN | LMK316AB7226KL-TR | CAP, SMD, 1206, 22 $\mu \mathrm{F}, 10 \mathrm{~V}, 10 \%$, X7R, ROHS |
| C1, C 9 | 0 |  |  | CAP, SMD, 1206, DNP-PLACE HOLDER, ROHS |
| TP1-TP8 | 8 | KEYSTONE | 5002 | CONN-MINI TEST POINT, VERTICAL, WHITE, ROHS |
| BAN1-BAN4 | 4 | KEYSTONE | 575-4 | CONN-JACK, MINI BANANA, 0.175 PLUG, NICKEL/BRASS, ROHS |
| J1 | 1 | BERG/FCI | 68000-236HLF | CONN-HEADER, 1x8, BRKAWY 1x36, 2.54mm, ROHS |
| J1-Pins 5-6 | 1 | SULLINS | SPCO2SYAN | CONN-JUMPER, SHORTING, 2PIN, BLACK, GOLD, ROHS |
| LED1 | 1 | LITEON/VISHAY | LTST-C190KGKT | LED, SMD, 0603, GREEN CLEAR, 2V, 20mA, $571 \mathrm{~nm}, 35 \mathrm{mcd}$, ROHS |
| U1 | 1 | INTERSIL | ISL8205MIRZ | IC-5A PWR MODULE, ANALOG DC/DC, 22P, QFN, 4.5x7.5, ROHS |
| Q1 | 1 | DIODES, INC. | 2N7002-7-F | TRANSISTOR, N-CHANNEL, 3LD, SOT-23, 6OV, 115 mA , ROHS |
| R12 | 1 | TE CONNECTIVITY | 3-1879216-2 | RES, SMD, 0402, 150k, 1/16W, 0.1\%, TF, ROHS |
| R9 | 1 | TE CONNECTIVITY | 6-1879215-6 | RES, SMD, 0402, 22.1k, 1/16W, 0.1\%, TF, ROHS |
| R11 | 1 | TE CONNECTIVITY | 9-1879208-7 | RES, SMD, 0402, 100k, 1/16W, 0.1\%, TF, ROHS |
| R4 | 0 |  |  | RES, SMD, 0603, 0.1\%, MF, DNP-PLACE HOLDER |
| R5, R15 | 2 | VENKEL | CR0402-16W-00T | RES, SMD, 0402, 0 , 1/16W, 5\%, TF, ROHS |
| R1 | 1 | ROHM | MCR01MZPF2003 | RES, SMD, 0402, 200k, 1/16W, 1\%, TF, ROHS |
| R2, R6, R16 | 0 |  |  | RES, SMD, 0402, DNP, DNP, DNP, TF, ROHS |
| R13 | 1 | VENKEL | CR0603-10W-1003FT | RES, SMD, 0603, 100k, 1/10W, 1\%, TF, ROHS |
| R14 | 1 | KOA | RK73H1JTTD2001F | RES, SMD, 0603, $2 \mathrm{k}, 1 / 10 \mathrm{~W}, 1 \%$, TF, ROHS |
| R3 | 1 | VENKEL | CR0603-10W-2003FT | RES, SMD, 0603, 200k, 1/10W, 1\%, TF, ROHS |
| R7 | 1 | STACKPOLE | RNCFO402DTE124K | RES, SMD, 0402, 124k, 1/16W, 0.5\%, TF, ROHS |
| R10 | 1 | YAGEO | RT0402BRD0731K6L | RES, SMD, 0402, 31.6k, 1/16W, 0.1\%, TF, ROHS |
| sw1 | 1 | ITT INDUSTRIES/C\&K DIVISION | GT11MSCBE | SWITCH-TOGGLE, SMD, 6PIN, SPDT, 2POS, ON-NONE-ON, ROHS |
| C10 | 0 |  |  | CAP, TANT, SMD, 2917, 10V, DNP-PLACE HOLDER |
| C11 | 0 |  |  | CAP, TANT POLY, SMD, 2917, 10V, DNP-PLACE HOLDER |

## Board Layout



FIGURE 11. TOP LAYER (TOP VIEW)


FIGURE 13. INNER LAYER 2 (TOP VIEW)


FIGURE 12. INNER LAYER 1 (TOP VIEW)


FIGURE 14. BOTTOM LAYER (BOTTOM VIEW)

ISL8205MEVAL1Z Performance Data board at $+25^{\circ} \mathrm{C}$ ambient and free air OLFM.


FIGURE 15. EFFICIENCY $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{IN}}=3.3 \mathrm{~V}$ PFM MODE


FIGURE 17. EFFICIENCY $T_{A}=+25^{\circ} \mathrm{C}, \mathrm{V}_{\text {IN }}=3.3 \mathrm{~V}$ PWM MODE


FIGURE 19. LOAD TRANSIENT $V_{I N}=5 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=1.2 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=0$ TO 5A, $f_{\text {SW }}=1.6 \mathrm{MHz}, \mathrm{C}_{\text {OUT }}=2 \times 22 \mu \mathrm{~F}$ CERAMIC CAPACITORS LOAD CURRENT SLEW RATE: 1A/ $\mu \mathrm{s}$


FIGURE 16. EFFICIENCY $T_{A}=+25^{\circ} \mathrm{C}, \mathrm{V}_{\text {IN }}=5 \mathrm{~V}$ PFM MODE


FIGURE 18. EFFICIENCY $T_{A}=+25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{IN}}=5 \mathrm{~V}$ PWM MODE


FIGURE 20. LOAD TRANSIENT $\mathrm{V}_{\text {IN }}=5 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=3.3 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=0$ TO 5A, $\mathrm{f}_{\text {SW }}=3 \mathrm{MHz}, \mathrm{C}_{\text {OUT }}=2 \times 22 \mu \mathrm{~F}$ CERAMIC CAPACITORS, LOAD CURRENT SLEW RATE: 1A/ $\mu \mathrm{s}$

ISL8205MEVAL1Z Performance Data The following data was acquired usinga ILL8205mEVALIZ evaluation
board at $+25^{\circ} \mathrm{C}$ ambient and free air OLFM. (Continued)


FIGURE 21. THERMAL IMAGE AT $\mathrm{V}_{\text {IN }}=5 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=1.2 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=5 \mathrm{AA}, \mathrm{f}_{\text {SW }}=1.6 \mathrm{MHz}, \mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, FREE AIR 0 LFM

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