

# PHOTOCOUPLER **PS2806-1,PS2806-4**

# HIGH ISOLATION VOLTAGE AC INPUT, DARLINGTON TRANSISTOR TYPE \_NEPOC Series\_ SOP PHOTOCOUPLER

### **DESCRIPTION**

The PS2806-1 and PS2806-4 are optically coupled isolators containing GaAs light emitting diodes and an NPN silicon darlington connected phototransistor in a plastic SOP for high density applications.

This package has shield effect to cut off ambient light.

### **FEATURES**

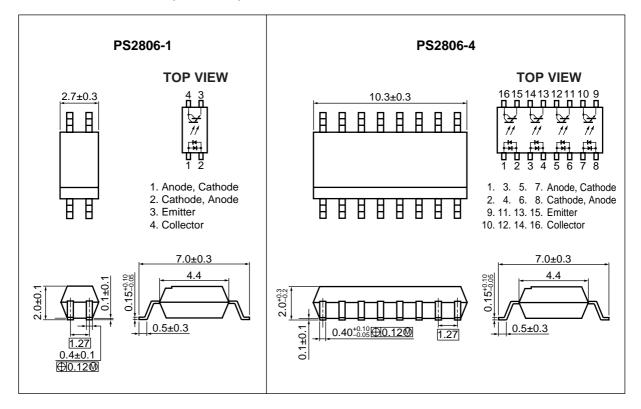
- High isolation voltage (BV = 2 500 Vr.m.s.)
- Small and thin package (4,16-pin SOP, Pin pitch 1.27 mm)
- · AC input response
- High current transfer ratio (CTR = 2 000 % TYP. @ IF =  $\pm 1$  mA, VcE = 2 V)
- Ordering number of tape product: PS2806-1-F3, F4, PS2806-4-F3, F4
- · Safety standards: PS2806-1, -4
  - UL approved: File No. E72422 (S)
  - BSI approved: No. 8188, 8189
  - VDE0884 approved (Option): PS2806-4 only

### **APPLICATIONS**

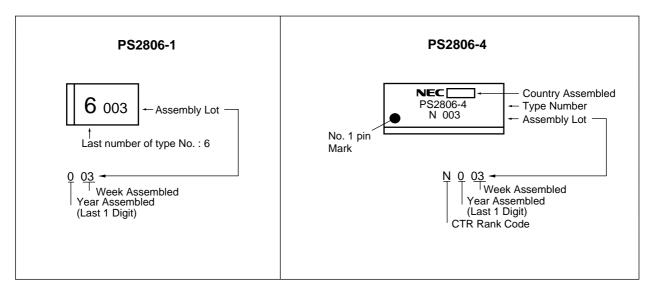
- Programmable logic controllers
- · Measuring instruments
- Hybrid IC

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# **★ PACKAGE DIMENSIONS (UNIT: mm)**



## **MARKING**



# **ORDERING INFORMATION (Solder Contains Lead)**

Part Number	Package	Packing Style	Application Part Number*1
PS2806-1	4-pin SOP	50 pcs (Tape 50 pcs cut)	PS2806-1
PS2806-1-F3		Embossed Tape 3 500 pcs/reel	
PS2806-1-F4			
PS2806-4	16-pin SOP	Magazine Case 45 pcs	PS2806-4
PS2806-4-F3		Embossed Tape 2 500 pcs/reel	
PS2806-4-F4			

<sup>\*1</sup> For the application of the Safety Standard, following part number should be used.

# **ORDERING INFORMATION (Pb-Free)**

Part Number	Package	Packing Style	Application Part  *1 Number
PS2806-1-A	4-pin SOP	50 pcs (Tape 50 pcs cut)	PS2806-1
PS2806-1-F3-A		Embossed Tape 3 500 pcs/reel	
PS2806-1-F4-A			
PS2806-4-A	16-pin SOP	Magazine Case 45 pcs	PS2806-4
PS2806-4-F3-A		Embossed Tape 2 500 pcs/reel	
PS2806-4-F4-A			

**<sup>\*1</sup>** For the application of the Safety Standard, following part number should be used.

# ABSOLUTE MAXIMUM RATINGS (TA = 25 °C, unless otherwise specified)

Parameter		Symbol	Ratings		Unit
			PS2806-1	PS2806-4	
Diode	Forward Current (DC)	lF	±50		mA
	Power Dissipation Derating	⊿P <sub>D</sub> /°C	0.6	0.8	mW/°C
	Power Dissipation	P□	60	80	mW/ch
	Peak Forward Current *1	IFP	±1		Α
Transistor	Collector to Emitter Voltage	VCEO	40		V
	Emitter to Collector Voltage	VECO	6		V
	Collector Current	lc	90	100	mA/ch
	Power Dissipation Derating	⊿Pc/°C	1.2		mW/°C
	Power Dissipation	Pc	12	20	mW/ch
Isolation Voltage *2		BV	2 500		Vr.m.s.
Operating Ambient Temperature		TA	-55 to +100		°C
Storage Temperature		T <sub>stg</sub>	-55 to +150		°C

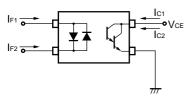
<sup>\*1</sup> PW = 100  $\mu$ s, Duty Cycle = 1 %

<sup>\*2</sup> AC voltage for 1 minute at T<sub>A</sub> = 25 °C, RH = 60 % between input and output

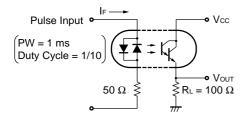
# **ELECTRICAL CHARACTERISTICS (TA = 25 °C)**

	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	VF	$I_F = \pm 5 \text{ mA}$		1.1	1.4	V
	Terminal Capacitance	Ct	V = 0 V, f = 1.0 MHz		30		pF
Transistor	Collector to Emitter Dark Current	ICEO	Vce = 40 V, I <sub>F</sub> = 0 mA			400	nA
Coupled	Current Transfer Ratio (Ic/I <sub>F</sub> )	CTR	$I_F = \pm 1$ mA, $V_{CE} = 2$ V	200	2 000		%
	CTR Ratio*1	CTR1/ CTR2	IF = 1 mA, VcE = 2 V	0.3	1.0	3.0	
	Collector Saturation Voltage	VCE(sat)	$I_F = \pm 1 \text{ mA}, I_C = 2 \text{ mA}$			1.0	V
	Isolation Resistance	R <sub>I-O</sub>	Vi-o = 1.0 kVpc	10 <sup>11</sup>			Ω
	Isolation Capacitance	C <sub>I-O</sub>	V = 0 V, f = 1.0 MHz		0.4		pF
	Rise Time *2	<b>t</b> r	$Vcc = 5 \text{ V}, \text{ Ic} = 2 \text{ mA}, \text{ RL} = 100 \Omega$		200		μS
	Fall Time *2	<b>t</b> f			200		

\*1 CTR1 = Ic1/IF1, CTR2 = Ic2/IF2

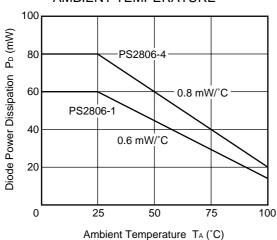


# \*2 Test circuit for switching time

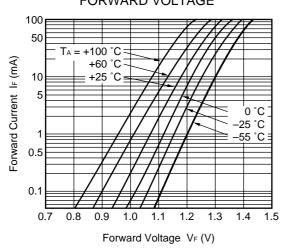


# TYPICAL CHARACTERISTICS (TA = 25 °C, unless otherwise specified)

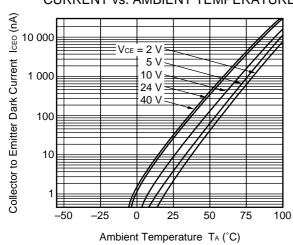
# DIODE POWER DISSIPATION vs. AMBIENT TEMPERATURE



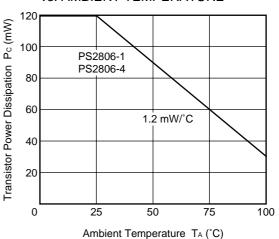
# FORWARD CURRENT vs. FORWARD VOLTAGE



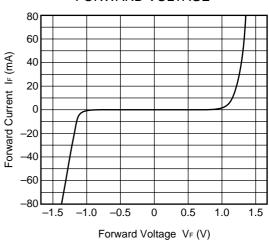
# COLLECTOR TO EMITTER DARK CURRENT vs. AMBIENT TEMPERATURE



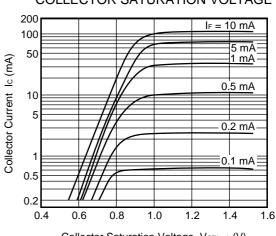
# TRANSISTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



# FORWARD CURRENT vs. FORWARD VOLTAGE

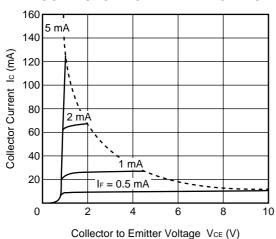


# COLLECTOR CURRENT vs. COLLECTOR SATURATION VOLTAGE

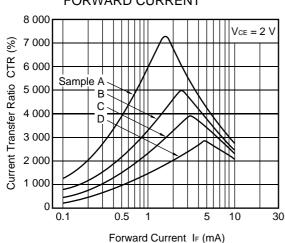


Collector Saturation Voltage VCE(sat) (V)

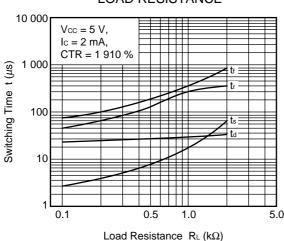
# COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



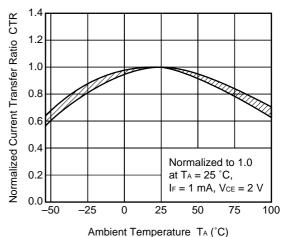
# CURRENT TRANSFER RATIO vs. FORWARD CURRENT



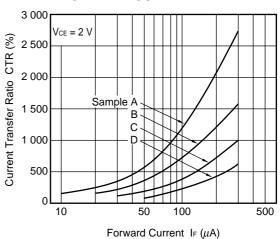
SWITCHING TIME vs. LOAD RESISTANCE



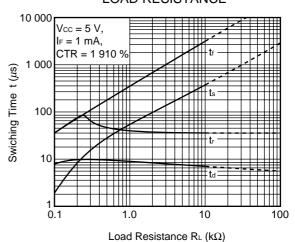
# NORMALIZED CURRENT TRANSFER RATIO vs. AMBIENT TEMPERATURE



CURRENT TRANSFER RATIO vs. FORWARD CURRENT



SWITCHING TIME vs. LOAD RESISTANCE



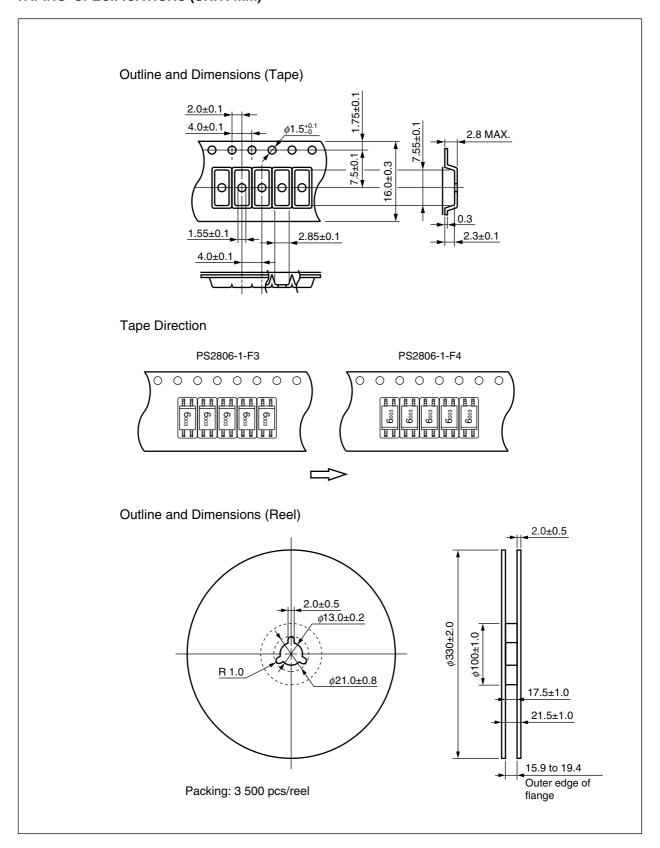
# FREQUENCY RESPONSE | IF = 1 mA, | VcE = 2 V | VcE =

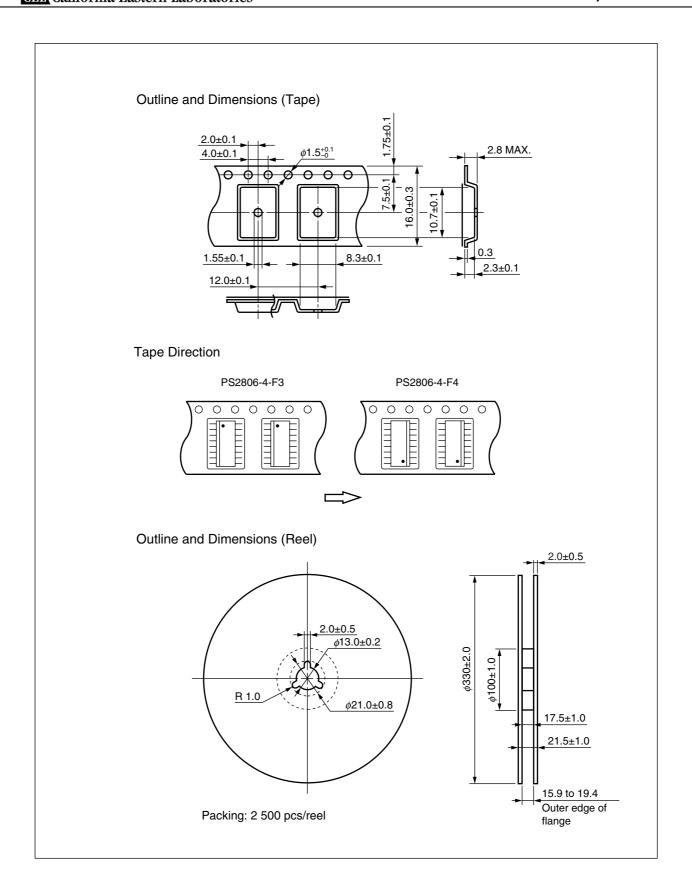
**Remark** The graphs indicate nominal characteristics.

# 

Time (Hr)

# TAPING SPECIFICATIONS (UNIT: mm)





### **NOTES ON HANDLING**

### 1. Recommended soldering conditions

# (1) Infrared reflow soldering

• Peak reflow temperature 260°C or below (package surface temperature)

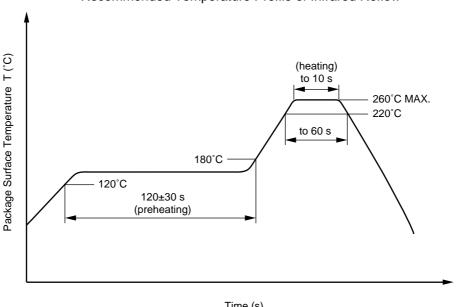
• Time of peak reflow temperature 10 seconds or less • Time of temperature higher than 220°C 60 seconds or less

• Time to preheat temperature from 120 to 180°C 120±30 s · Number of reflows Three

• Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

### Recommended Temperature Profile of Infrared Reflow



### Time (s)

# (2) Wave soldering

• Temperature 260°C or below (molten solder temperature)

• Time 10 seconds or less

· Preheating conditions 120°C or below (package surface temperature)

· Number of times One (Allowed to be dipped in solder including plastic mold portion.)

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

# (3) Cautions

Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

# 2. Cautions regarding noise

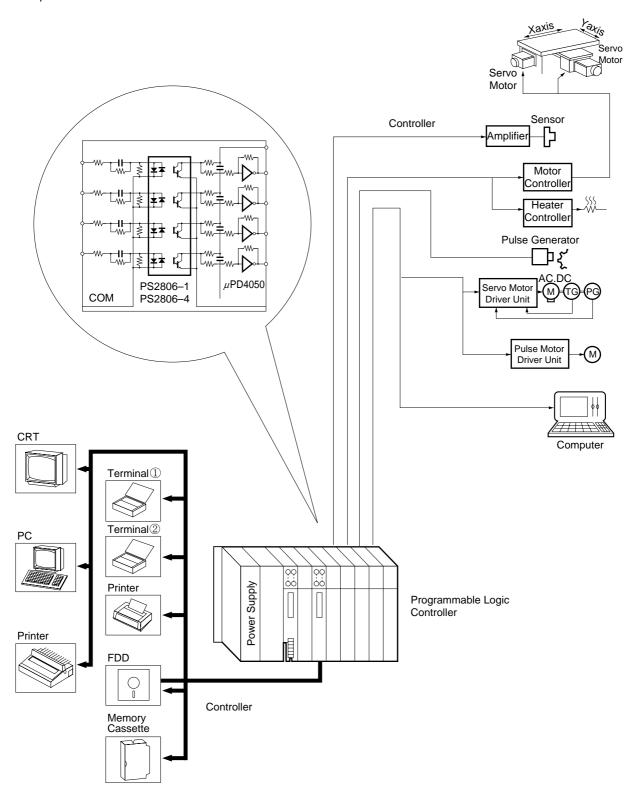
Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output side may enter the on state, even if the voltage is within the absolute maximum ratings.

# **USAGE CAUTIONS**

- 1. Protect against static electricity when handling.
- 2. Avoid storage at a high temperature and high humidity.

# PROGRAMMABLE LOGIC CONTROLLERS EXAMPLE

Purpose: In-out interface





4590 Patrick Henry Drive Santa Clara, CA 95054-1817 Telephone: (408) 919-2500 Facsimile: (408) 988-0279

Subject: Compliance with EU Directives

CEL certifies, to its knowledge, that semiconductor and laser products detailed below are compliant with the requirements of European Union (EU) Directive 2002/95/EC Restriction on Use of Hazardous Substances in electrical and electronic equipment (RoHS) and the requirements of EU Directive 2003/11/EC Restriction on Penta and Octa BDE.

CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (\*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentration contained in CEL devices		
Lead (Pb)	< 1000 PPM	-A Not Detected	-AZ (*)	
Mercury	< 1000 PPM	Not Detected		
Cadmium	< 100 PPM	Not Detected		
Hexavalent Chromium	< 1000 PPM	Not Detected		
PBB	< 1000 PPM	Not Detected		
PBDE	< 1000 PPM	Not Detected		

If you should have any additional questions regarding our devices and compliance to environmental standards, please do not hesitate to contact your local representative.

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