

# RJK60S4DPP-E0

600V - 16A - SJ MOS FET High Speed Power Switching R07DS0638EJ0300 Rev.3.00 Oct 12, 2012

#### **Features**

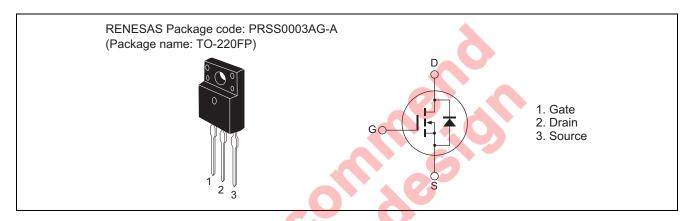
- Superjunction MOSFET
- Low on-resistance

 $R_{DS(on)} = 0.23~\Omega$  typ. (at  $I_D = 8~A,~V_{GS} = 10~V,~Ta = 25^{\circ}C)$ 

• High speed switching

 $t_f$  = 17 ns typ. (at  $I_D$  = 8 A,  $V_{GS}$  = 10 V,  $R_L$  = 37.5  $\Omega$ , Rg = 10  $\Omega$ , Ta = 25°C)

#### **Outline**



### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol Ratings		Unit
Drain to source voltage	V <sub>DSS</sub>	600	V
Gate to source voltage	V <sub>GSS</sub>	+30, -20	V
Drain current Tc = 25°C	I <sub>D</sub> Note1	16	Α
Tc = 100°C	I <sub>D</sub> Note1	10	Α
Drain peak current	I <sub>D(pulse)</sub> Note1 32		А
Body-drain diode reverse drain current	I <sub>DR</sub> Note1	16	Α
Body-drain diode reverse drain peak current	I <sub>DR(pulse)</sub> Note1	32	Α
Avalanche current	I <sub>AP</sub> Note2	4	Α
Avalanche energy	E <sub>AR</sub> Note2	0.87	mJ
Channel dissipation	Pch Note3	29.9	W
Channel to case thermal impedance	θch-c	4.17	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. Limited by Tch max.

- 2. STch = 25°C, Tch  $\leq$  150°C
- 3. Value at Tc = 25°C

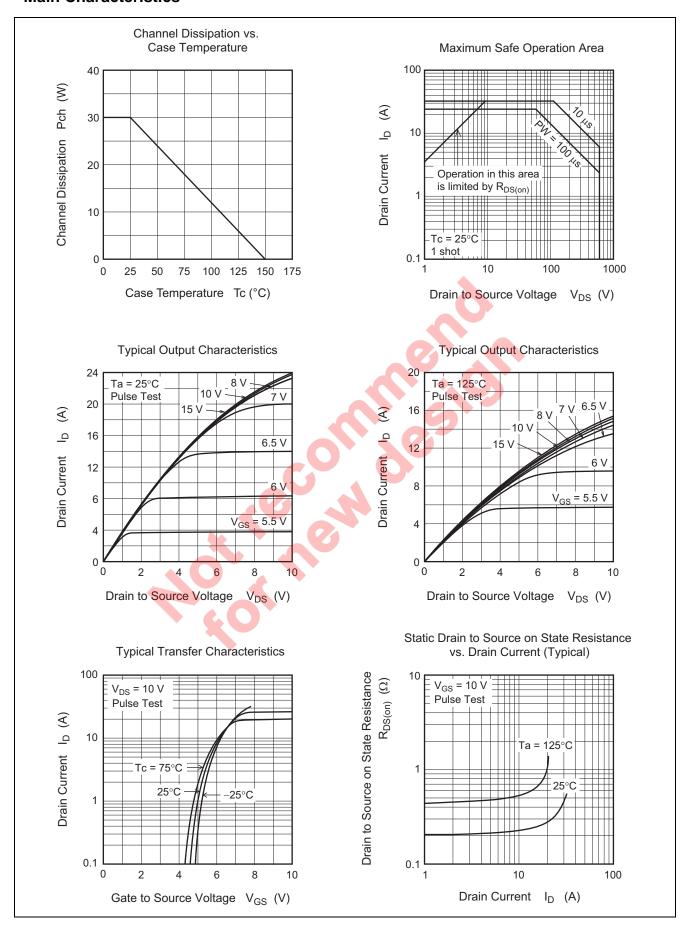
### **Electrical Characteristics**

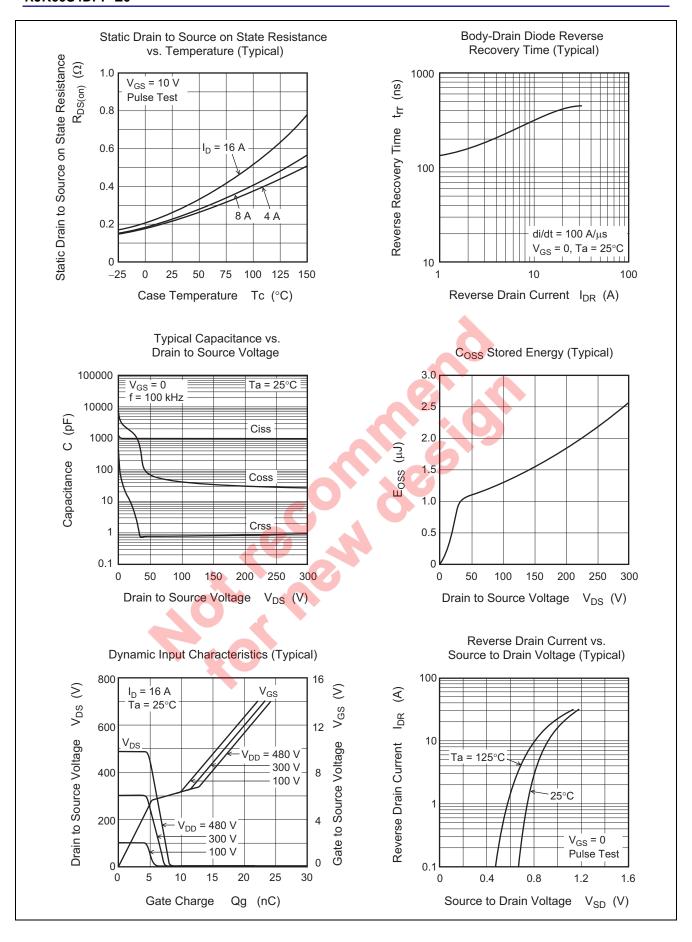
 $(Ta = 25^{\circ}C)$ 

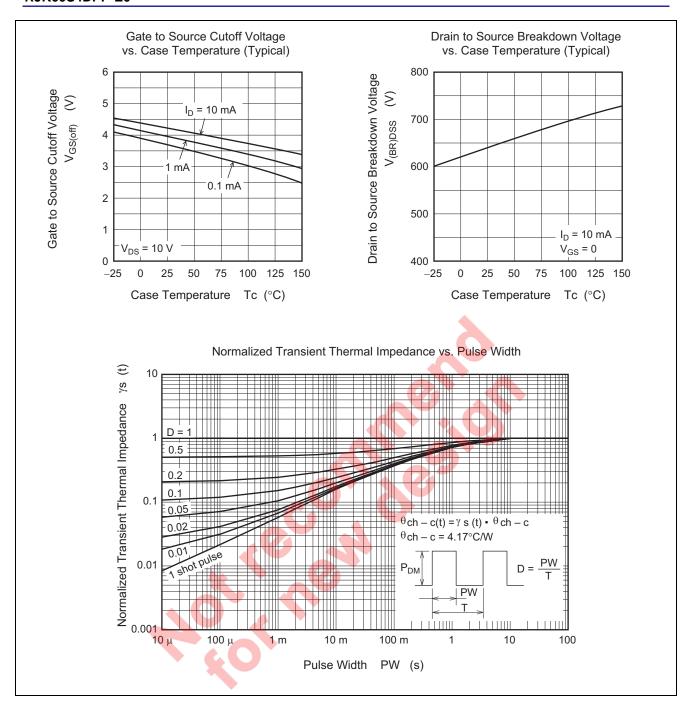
Item	Symbol	Min	Тур	Max	Unit	Test conditions											
Drain to source breakdown voltage	$V_{(BR)DSS}$	600	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$											
Zero gate voltage drain current	I <sub>DSS</sub>			1	mA	$V_{DS} = 600 \text{ V}, V_{GS} = 0$											
Gate to source leak current	I <sub>GSS</sub>	_	_	±0.1	μΑ	$V_{GS} = +30V, -20 V, V_{DS} = 0$											
Gate to source cutoff voltage	$V_{GS(off)}$	3	_	5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$											
Static drain to source on state resistance	R <sub>DS(on)</sub>		0.23	0.29	Ω	$I_D = 8 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$											
	R <sub>DS(on)</sub>	_	0.57	_	Ω	Ta = 150°C											
						$I_D = 8 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$											
Gate resistance	Rg	_	2	_	Ω	f = 1 MHz											
						$V_{DS} = 25 \text{ V}, V_{GS} = 0$											
Input capacitance	Ciss	_	988	—	pF	$V_{DS} = 25 \text{ V}$											
Output capacitance	Coss	_	1415	_	pF	$V_{GS} = 0$											
Reverse transfer capacitance	Crss	_	5.1	_	pF	f = 100kHz											
Turn-on delay time	t <sub>d(on)</sub>		15	_	ns	I <sub>D</sub> = 8 A											
Rise time	t <sub>r</sub>		19	_	ns	$V_{GS} = 10 \text{ V}$											
Turn-off delay time	$t_{d(off)}$		30	_	ns	$R_L = 37.5 \Omega$											
Fall time	t <sub>f</sub>		17	4	ns	$Rg = 10 \Omega^{Note4}$											
Total gate charge	Qg	_	18	-	nC	V <sub>DD</sub> = 480 V											
Gate to source charge	Qgs	_	7		nC	V <sub>GS</sub> = 10 V I <sub>D</sub> = 16 A <sup>Note4</sup>											
Gate to drain charge	Qgd	_	6	-	nC												
Body-drain diode forward voltage	$V_{DF}$	_	1.0	1.6	V	$I_F = 16 \text{ A}, V_{GS} = 0^{\text{Note4}}$											
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	363		ns	I <sub>F</sub> = 16 A											
Body-drain diode reverse recovery current	Irr		23		Α	$V_{GS} = 0$											
Body-drain diode reverse recovery charge	Qrr	<b>6</b> -7	4.9	9-	μС	$di_F/dt = 100 A/\mu s^{Note4}$											
Notes: 4. Pulse test				·	•												
Body-drain diode reverse recovery charge Q <sub>rr</sub> — 4.9 — μC di <sub>F</sub> /dt = 100 A/μs <sup>Note4</sup> Notes: 4. Pulse test																	

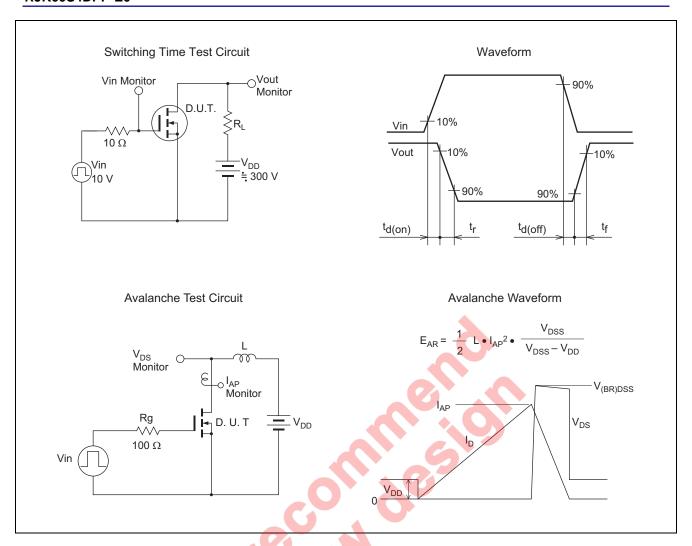
Notes: 4. Pulse test

### **Main Characteristics**

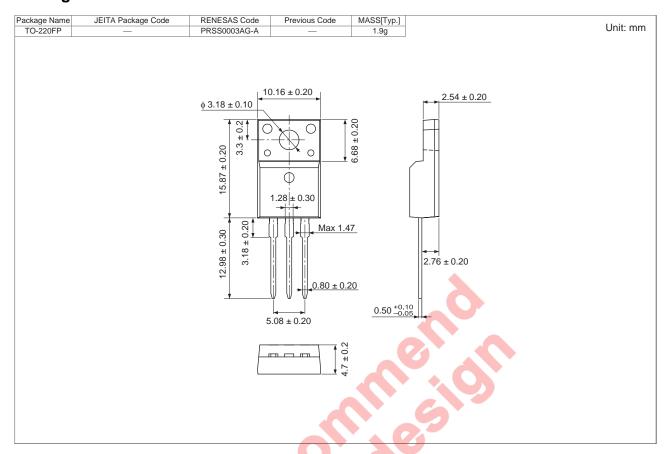








### **Package Dimension**



### **Ordering Information**

Orderable Part Number	Q	uantity	Shipping Container
RJK60S4DPP-E0#T2	1000 pcs		Box (Tube)

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