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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# BIPOLAR ANALOG INTEGRATED CIRCUIT $\mu PC319$

# HIGH SPEED DUAL COMPARATOR

### DESCRIPTION

The  $\mu$ PC319 is a precision high speed dual comparator designed to operate over a wide range of supply voltage down to a 5 V logic supply and ground. Further advantage, they have excellent input characteristics and direct drive capability to all the popular logic families.

# FEATURES

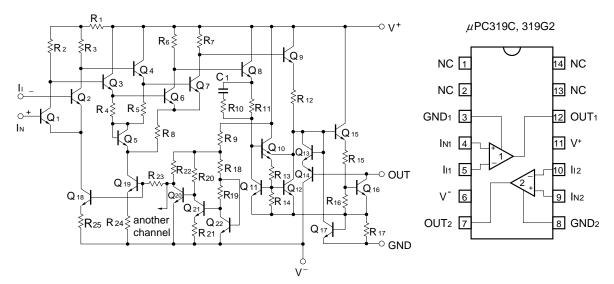
- Operate from single 5 V supply
- Typically 80 ns response time at  $\pm 15 \text{ V}$
- · Open collector output
- Minimum fan-out of 2 each side (TTL)
- High common mode slew rate

#### **ORDERING INFORMATION**

Part Number	ber Package	
μPC319C	14-pin plastic DIP (7.62 mm (300))	
μPC319G2	14-pin plastic SOP (5.72 mm (225))	

## **EQUIVALENT CIRCUIT (1/2 Circuit)**

# **PIN CONFIGURATION (Top View)**



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The mark **\*** shows major revised points.

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Par	ameter	Symbol	Ratings	Unit
Voltage between V $^{\scriptscriptstyle +}$ and V $^{\scriptscriptstyle -Note  1}$		$V^{\scriptscriptstyle +}-V^{\scriptscriptstyle -}$	-0.3 to +36	V
Differential Input Volta	ige	Vid	±5	V
Input Voltage <sup>Note 2</sup>		Vi	V <sup>-</sup> –0.3 to V <sup>+</sup> +0.3	V
Output to Negative S	upply Voltage <sup>Note 3</sup>	Vo − V <sup>-</sup>	-0.3 to +36	V
Ground to Negative S	Supply Voltage <sup>Note 3</sup>	$V_{\text{GND}} - V^{-}$	-0.3 to +25	V
Ground to Positive Su	upply Voltage <sup>Note 3</sup>	$V^{\scriptscriptstyle +}-V_{\rm GND}$	-0.3 to +18	V
Power Dissipation	C Package <sup>Note 4</sup>	P⊤	570	mW
	G2 Package <sup>Note 5</sup>		550	mW
Output Short Circuit D	uration <sup>Note 6</sup>		10	sec
Operating Ambient Temperature		TA	-20 to +80	°C
Storage Temperature		Tstg	-55 to +125	°C

# ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^{\circ}C$ )

Notes 1. Reverse connection of supply voltage can cause destruction.

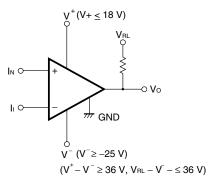
- 2. The input voltage should be allowed to input without damage or destruction. Even during the transition period of supply voltage, power on/off etc., this specification should be kept. The normal operation will establish when the both inputs are within the Common Mode Input Voltage Range of electrical characteristics.
- **3.** This specification is the voltage which should be allowed to supply to the output and GND terminal from external without damage or destructive. Even during the transition period of supply voltage, power on/off etc., this specification should be kept.
- 4. Thermal derating factor is -7.6 mW/°C when operating ambient temperature is higher than 50°C.
- 5. Thermal derating factor is -5.5 mW/°C when operating ambient temperature is higher than 25°C.
- **6.** Pay careful attention to the total power dissipation not to exceed the absolute maximum ratings, Note 4 and Note 5.

# **RECOMMENDED OPERATING CONDITIONS**

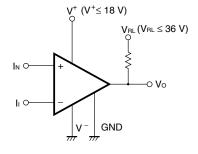
Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage (split)	$V^{\pm}$	±5		±16	V
Supply Voltage (V <sup>−</sup> = GND)	V*	+5		+32	V

# TYPICAL CONNECTIONS





#### SINGLE SUPPLY

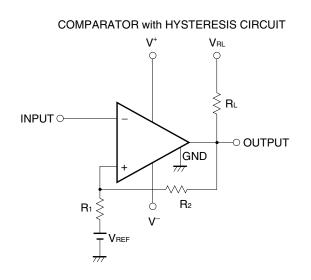


Data Sheet G15950EJ5V0DS

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input Offset Voltage	Vio	$V^{\scriptscriptstyle +}-V^{\scriptscriptstyle -}$ = 5 to 30 V, $R_{\rm S}$ $\leq$ 5 k $\Omega$		±2.0	±8.0	mV
Input Offset Current	lio	$V^{+} - V^{-} = 5 \text{ to } 30 \text{ V}$		±80	±200	nA
Input Bias Current	Ів	$V^{+} - V^{-} = 5 \text{ to } 30 \text{ V}$		400	1,000	nA
Voltage Gain	Av		8,000	40,000		
Response Time		Input 100 mV, Overdrive 5 mV		80		ns
Output Saturation Voltage	Vol	$V_{I} \leq -10 \text{ mV}, I_{O} = 25 \text{ mA}$		0.75	1.5	V
Output Leakage Current	Ioleak	$V_{\rm I}\geq 10~mV,V_{\rm O}=35~V$		0.2	10	μA
Positive Supply Current	l*	$V^{+} = 5 V, V^{-} = 0 V, I_{0} = 0 A,$		4.3		mA
		Both Comparators				
Positive Supply Current	l+	Io = 0 A, Both Comparators		8.0	12.5	mA
Negative Supply Current	l⁻	Io = 0 A, Both Comparators		3.0	5.0	mA
Input Offset Voltage	Vio	$V^{\scriptscriptstyle +}-V^{\scriptscriptstyle -}=5 \text{ to } 30 \text{ V}, \text{Rs} \leq 5 \text{ k}\Omega,$			±10	mV
		$T_A = 0$ to $70^{\circ}C$				
Input Offset Current	lio	$V^{+} - V^{-} = 5$ to 30 V, $T_A = 0$ to 70°C			±300	nA
Input Bias Current	Ів	$V^{\scriptscriptstyle +}-V^{\scriptscriptstyle -}=5$ to 30 V, $T_A=0$ to $70^{\circ}C$			1,200	nA
Common Mode Input Voltage	VICM			±13		v
Range						
Output Saturation Voltage	Vol	$V^{*} = 4.5 V, V^{-} = 0 V, V_{I} \leq -10 mV,$		0.23	0.4	V
		lo ≤ 3.2 mA				

# ELECTRICAL CHARACTERISTICS (TA = $25^{\circ}$ C, V<sup>±</sup> = $\pm 15$ V)

# **TYPICAL APPLICATION CIRCUIT**



# Threshold Voltage

$$V_{\text{TH (High)}} \stackrel{:}{=} V_{\text{REF}} + \frac{R_1}{R_L + R_2 + R_1} (V_{\text{RL}} - V_{\text{REF}})$$
$$V_{\text{TH (Low)}} \stackrel{:}{=} V_{\text{REF}} - \frac{R_1}{R_1 + R_2} (V_{\text{REF}} - V_{\text{OL}})$$

 $V^{\pm} = \pm 15 V$ 

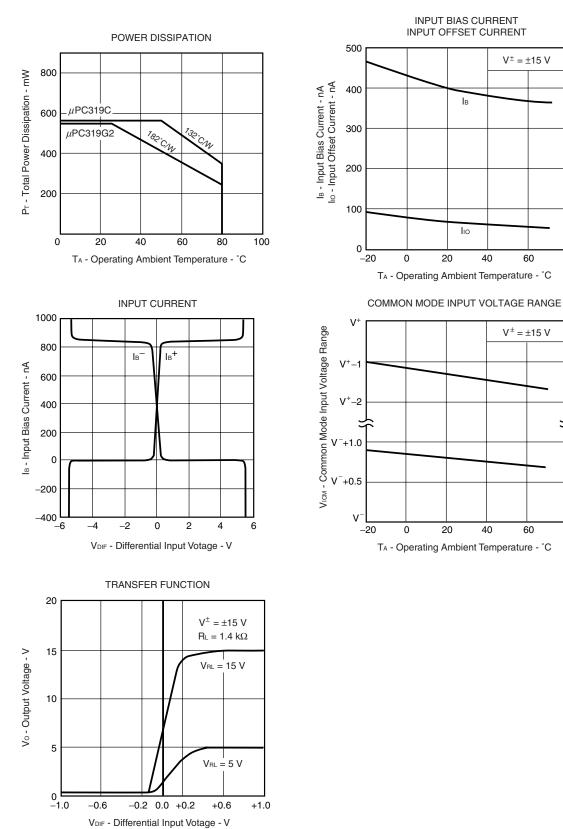
60

 $V^{\pm} = \pm 15 V$ 

60

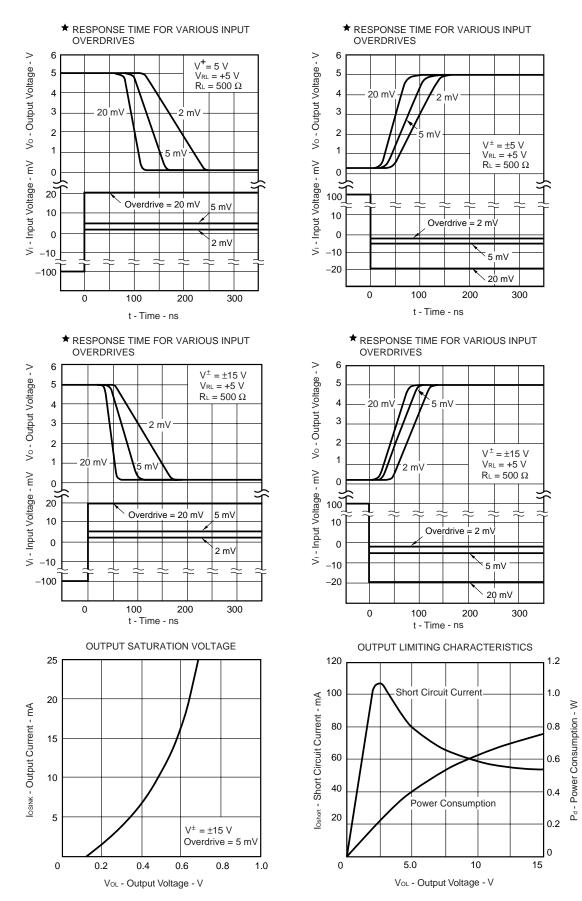
80

80

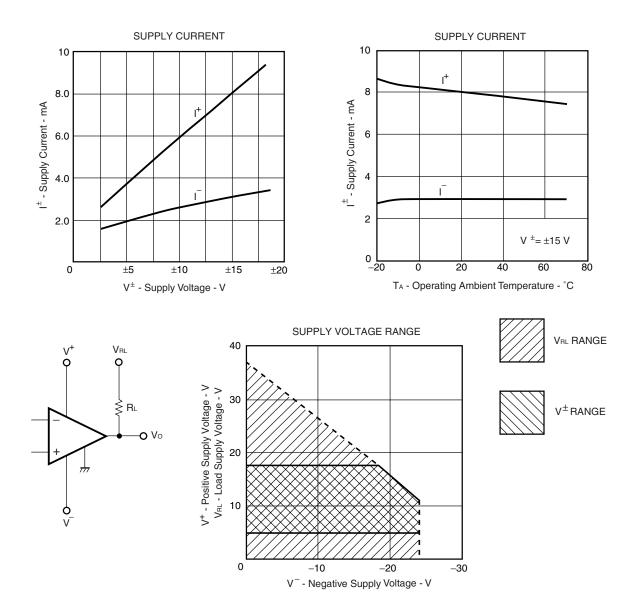


# TYPICAL PERFORMANCE CHARACTERISTICS (TA = 25°C, TYP.)

Data Sheet G15950EJ5V0DS

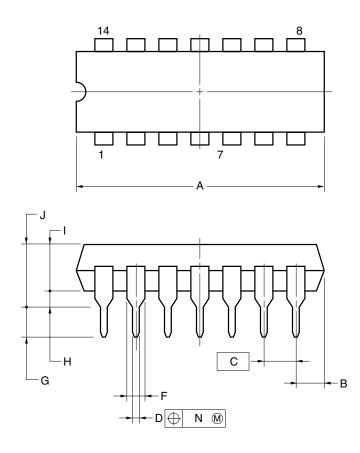


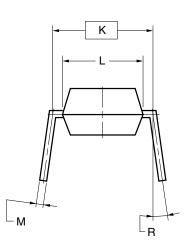
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PACKAGE DRAWINGS (Unit : mm)

# 14-PIN PLASTIC DIP (7.62 mm (300))



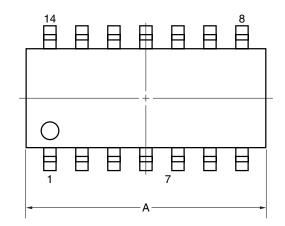


## NOTES

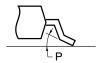
- 1. Each lead centerline is located within 0.25 mm of its true position (T.P.) at maximum material condition.
- 2. Item "K" to center of leads when formed parallel.

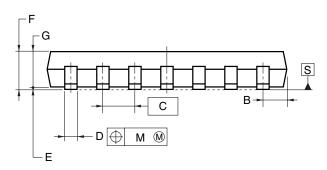
ITEM	MILLIMETERS
A	19.22±0.2
В	2.14 MAX.
С	2.54 (T.P.)
D	0.50±0.10
F	1.32±0.12
G	3.6±0.3
Н	0.51 MIN.
I	3.55
J	4.3±0.2
К	7.62 (T.P.)
L	6.4±0.2
М	$0.25\substack{+0.10 \\ -0.05}$
N	0.25
R	0~15°
F	P14C-100-300B1-3

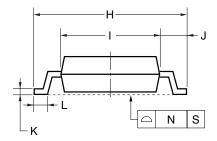
# 14-PIN PLASTIC SOP (5.72 mm (225))



detail of lead end







#### NOTE

Each lead centerline is located within 0.1 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
A	10.2±0.26
В	1.42 MAX.
С	1.27 (T.P.)
D	$0.42\substack{+0.08\\-0.07}$
Е	0.1±0.1
F	$1.59^{+0.21}_{-0.2}$
G	1.49
Н	6.5±0.2
I	4.4±0.1
J	1.1±0.16
к	$0.17\substack{+0.08\\-0.07}$
L	0.6±0.2
М	0.1
Ν	0.10
Ρ	$3^{\circ}^{+7^{\circ}}_{-3^{\circ}}$

S14GM-50-225B, C-6

# ★ RECOMMENDED SOLDERING CONDITIONS

The  $\mu$ PC319 should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, contact an NEC Electronics sales representative.

For technical information, see the following website.

# Semiconductor Device Mount Manual (http://www.necel.com/pkg/en/mount/index.html)

### Type of Surface Mount Device

Process	Conditions	Symbol
Infrared Ray Reflow	Peak temperature: 230°C or below (Package surface temperature),	IR30-00-1
	Reflow time: 30 seconds or less (at 210°C or higher),	
	Maximum number of reflow processes: 1 time.	
Vapor Phase Soldering	Peak temperature: 215°C or below (Package surface temperature),	VP15-00-1
	Reflow time: 40 seconds or less (at 200°C or higher),	
	Maximum number of reflow processes: 1 time.	
Wave Soldering	Solder temperature: 260°C or below, Flow time: 10 seconds or less,	WS60-00-1
	Maximum number of flow processes: 1 time,	
	Pre-heating temperature: 120°C or below (Package surface temperature).	
Partial Heating Method	Pin temperature: 300°C or below,	-
	Heat time: 3 seconds or less (Per each side of the device).	

## µPC319G2: 14-pin plastic SOP (5.72 mm (225))

# Caution Apply only one kind of soldering condition to a device, except for "partial heating method", or the device will be damaged by heat stress.

## Type of Through-hole Device

#### µPC319C: 14-pin plastic DIP (7.62 mm (300))

Process	Conditions	
Wave Soldering	Solder temperature: 260°C or below,	
(only to leads)	Flow time: 10 seconds or less.	
Partial Heating Method	Pin temperature: 300°C or below,	
	Heat time: 3 seconds or less (per each lead).	

# Caution For through-hole device, the wave soldering process must be applied only to leads, and make sure that the package body does not get jet soldered.

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 74HC785DB,118

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