

μPC844, μPC4744

Single Power Supply, High Speed, Wide Band,
Quad Operational Amplifier

R03DS0137EJ0100
Rev.1.00
2019.1.17

DESCRIPTION

μPC844, μPC4744 are high-speed version of the single-power general-purpose operational amplifier μPC451, μPC324 realizing high-speed response and high stability. By adopting a high speed PNP transistor circuit, various characteristics such as slew rate and gain bandwidth are improved, as compared to μPC451 and μPC324, the load capacity stability is also improved, with no crossover distortion.

It can be used widely for various application circuits such as single power supply AC amplifier, active filter, line driver, amplifier for light receiving element, etc.

Depending on the usage and operating ambient temperature range, the μPC844 is designed for extended temperature and suited for wide operating ambient temperature application, and μPC4744 is design for general purposes. Along with this series of lineup, the dual type operational amplifier, μPC842, μPC4742 with the same circuit configuration are also available.

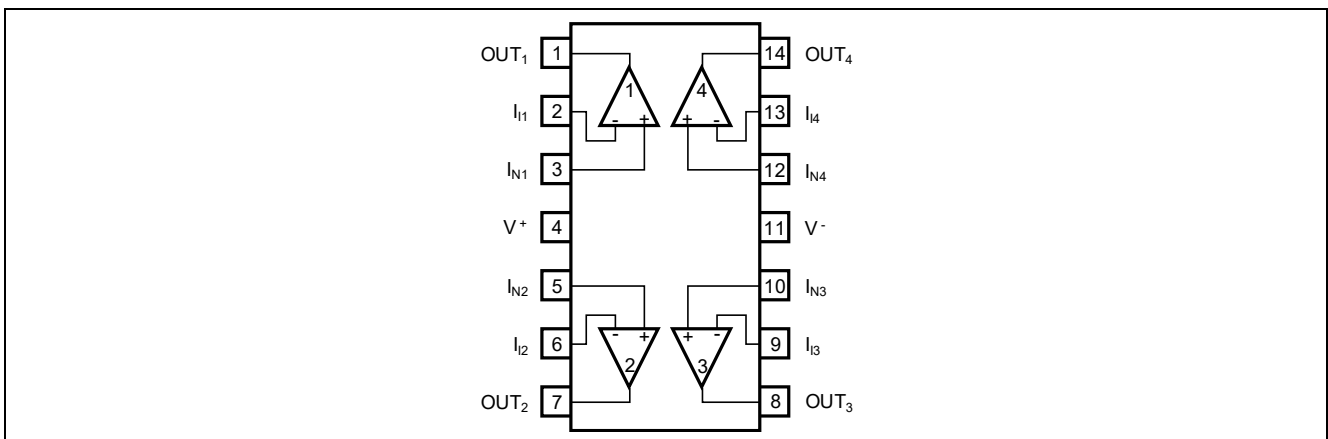
FEATURES

- Slew Rate ($A_v = 1$) 7 V/μs (TYP.) ($V^+ = +5$ V, $V^- =$ GND)
- Gain Bandwidth Product ($f = 100$ kHz) 3.5 MHz (TYP.)
- Input Offset Voltage ± 2 mV (TYP.)
- Input Offset Current ± 6 nA (TYP.)
- Operating Ambient Temperature
 μPC844G2 : $T_A = -40 \sim +85$ °C, μPC4744G2 : $T_A = -20 \sim +80$ °C,
 μPC844GR-9LG : $T_A = -40 \sim +125$ °C, μPC4744GR-9LG : $T_A = -40 \sim +85$ °C,
- Stability to capacitive load (Capacitive load, 1000 pF)
- Built-in phase correction circuit.
- Built-in output short-circuit protection circuit.
- Standard pin (pin compatible) configuration of a quad operational amplifier.

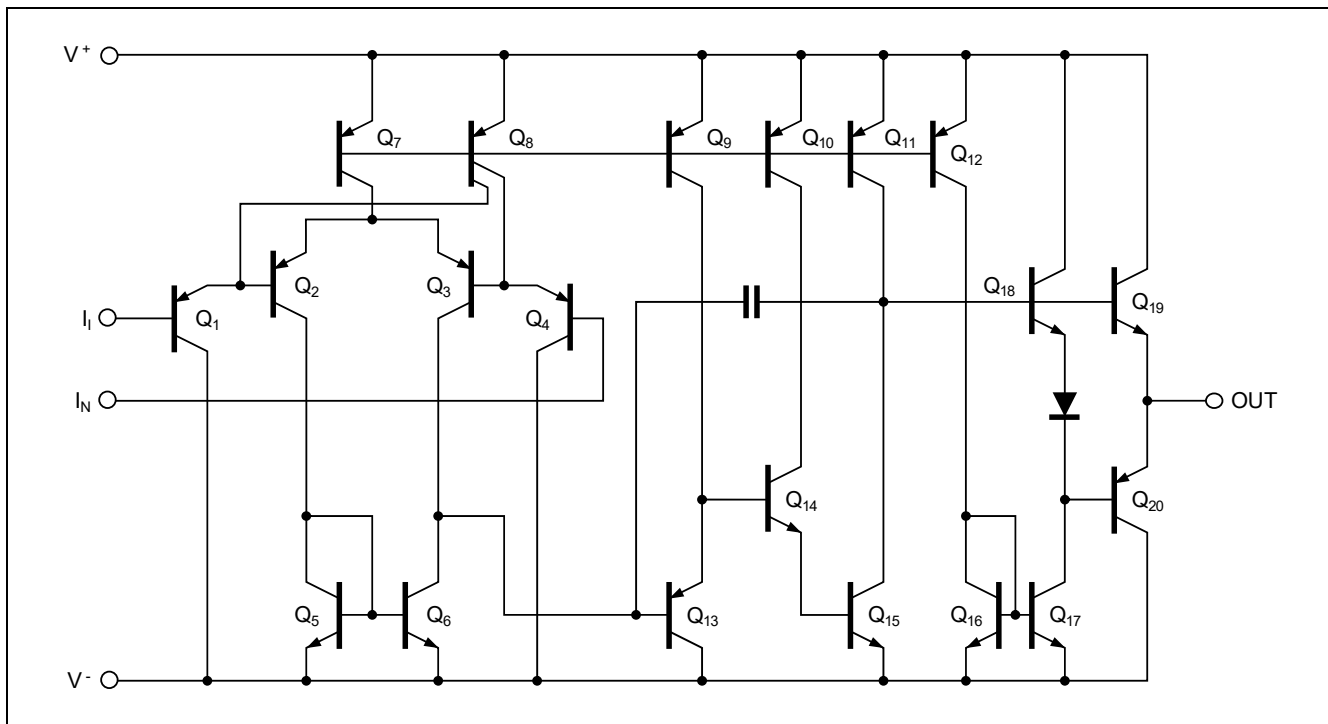
ORDERING INFORMATION

Order Name	Selected Grade	Package
μPC844G2-A	Standard	14-pin plastic SOP (5.72 mm (225))
μPC4744G2-A	Standard	14-pin plastic SOP (5.72 mm (225))
μPC844GR-9LG-A	Standard	14-pin plastic TSSOP (5.72 mm (225))
μPC4744GR-9LG-A	Standard	14-pin plastic TSSOP (5.72 mm (225))

PIN CONFIGURATION (Top View)



EQUIVALENT CIRCUIT (1/4CIRCUIT)



ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C)

Parameter	Symbol	μPC844G2	μPC4744G2	μPC844GR-9LG	μPC4744GR-9LG	Unit
Power Supply Voltage ^{Note 1}	V ⁺ - V ⁻	-0.3 ~ +36				V
Differential Input Voltage	V _{ID}	±36				V
Input Voltage ^{Note 2}	V _I	V ⁻ -0.3 ~ V ⁻ +36				V
Output Applied Voltage ^{Note 3}	V _O	V ⁻ -0.3 ~ V ⁺ +0.3				V
Total Power Dissipation ^{Note 4}	P _T	550				mW
Output Short Circuit Duration ^{Note 5}	t _s	Indefinite				s
Operating Ambient Temperature	T _A	-40 ~ +85	-20 ~ +80	-40 ~ +125	-40 ~ +85	°C
Storage Temperature	T _{stg}	-55 ~ +125		-55 ~ +150	-55 ~ +125	°C

[Note] 1. Note that reverse connections of the power supply may damage the ICs.

2. The allowable input voltage range without damaging or destructing the device. Independent to power supply voltage range.

Do not apply voltage equivalent to V⁻ (GND) - 0.3 V or less.

Note that the operational amplifier will operate normally when the input voltage applied is within the common mode input voltage range.

3. The input voltage range that can be applied to the output pin externally without deteriorating or damaging the device characteristic. The permitted input voltage that can be applied regardless of the power supply voltage. This specification also includes precaution during transition state such as ON/OFF, etc.

4. This is the value when the glass epoxy substrate (size: 100 mm x 100 mm, thickness: 1 mm, 15% of the substrate area where only one side is copper foiled is filling wired) is mounted.

Note that restrictions will be made to the following conditions for each product, and the de-rating ratio depending on the operating ambient temperature.

μPC844G2, 4744G2 : De-rate -5.5 mW/°C when T_A > 25 °C

(Junction - ambient thermal resistance R_{th(J-A)} = 182 °C/W)

μPC844GR-9LG : De-rate -7.0 mW/°C when T_A > 71 °C

(Junction - ambient thermal resistance R_{th(J-A)} = 144 °C/W)

μPC4744GR-9LG : De-rate -7.0 mW/°C when T_A > 46 °C

(Junction - ambient thermal resistance R_{th(J-A)} = 144 °C/W)

5. Please use the total loss and the de-rating factor of Note 4.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Power Supply Voltage ($V^- = \text{GND}$)	V^+	+3	+5 ~ +30	+32	V
Power Supply Voltage (Dual Supply)	V^\pm	±1.5		±16	V
Output Current	I_o			±10	mA
Capacitive Load ($A_v = +1$)	C_L			1000 ^{Note 6}	pF

【Note】 6. This is the value when feedback resistor (R_f) = 0 Ω.

ELECTRICAL CHARACTERISTICS

($T_A = 25\text{ }^\circ\text{C}$, $V^\pm = \pm 15\text{ V}$)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Test Condition
Input Offset Voltage	V_{IO}		±2	±6	mV	
Input Offset Current	I_{IO}		±6	±75	nA	
Input Bias Current ^{Note 7}	I_B		130	500	nA	
Large Signal Voltage Gain	A_v	25000	300000			$R_L \geq 2\text{ k}\Omega$, $V_o = \pm 10\text{ V}$
Circuit Current ^{Note 8}	I_{CC}		7.5	11	mA	$I_o = 0\text{ A}$
Common Mode Rejection Ratio	CMR	70	86		dB	
Supply Voltage Rejection Ratio	SVR	70	93		dB	
Output Voltage Swing	V_{Om1}	±13.7	+14		V	$R_L \geq 10\text{ k}\Omega$
			-14.3			
	V_{Om2}	±13.5			V	$R_L \geq 2\text{ k}\Omega$
Common Mode Input Voltage Range	V_{ICM}	V^-		$V^+ - 1.8$	V	
Slew Rate	SR		8.5		V/μs	$A_v = 1$ (Rise edge)
Gain Bandwidth Product	GBW		3.5		MHz	$f = 100\text{ kHz}$
Channel Separation			120		dB	$f = 20\text{ Hz} \sim 20\text{ kHz}$

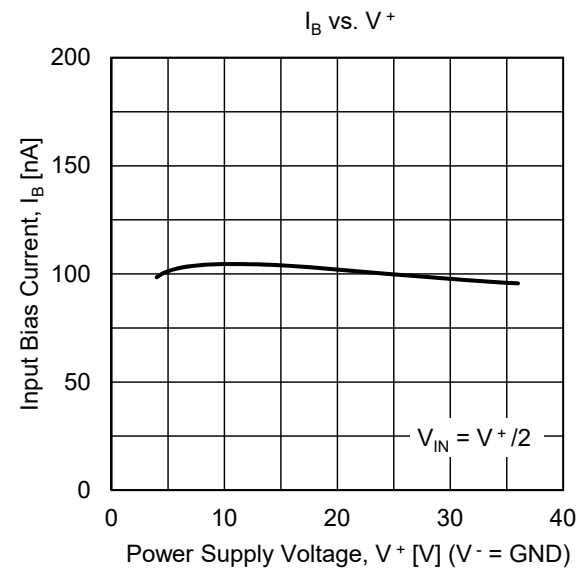
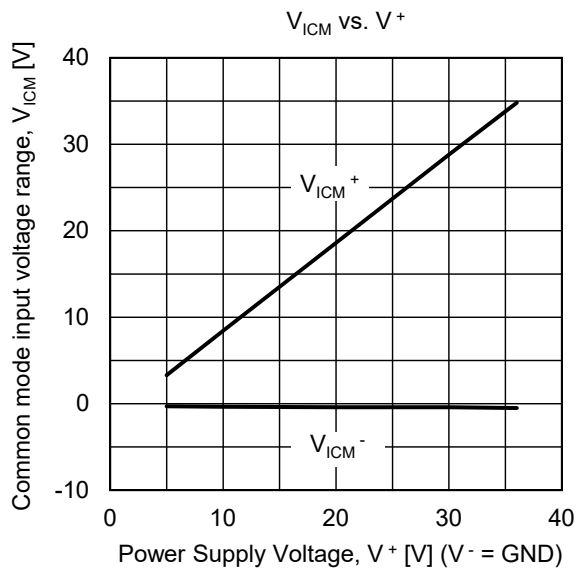
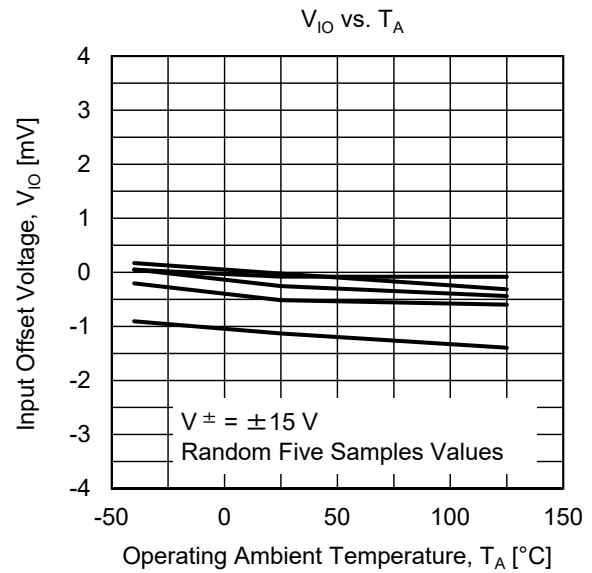
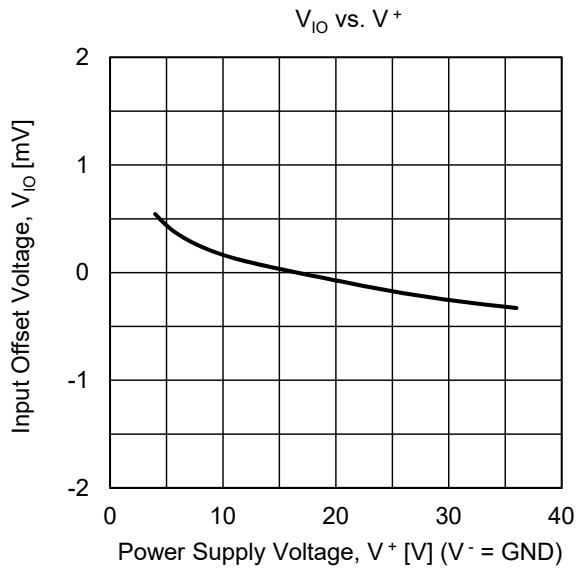
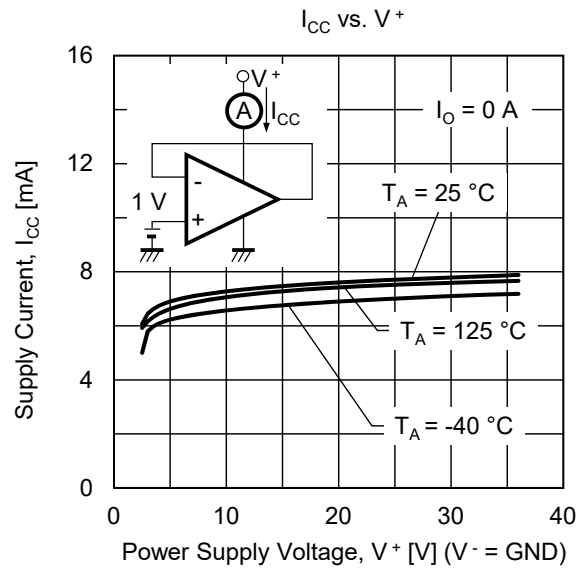
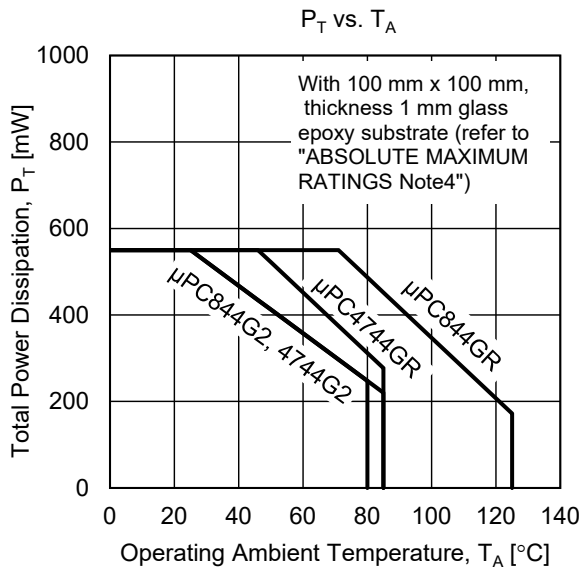
($T_A = 25\text{ }^\circ\text{C}$, $V^+ = +5\text{ V}$, $V^- = \text{GND}$)

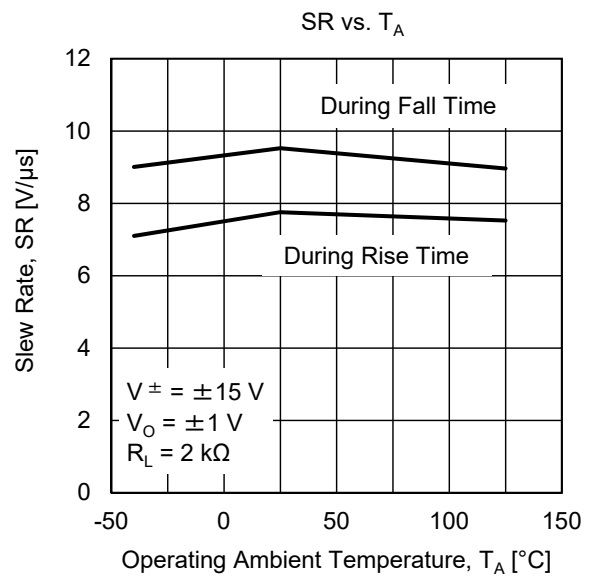
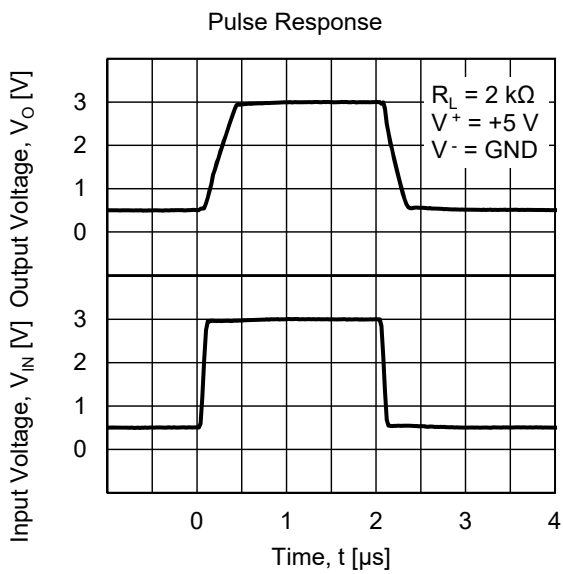
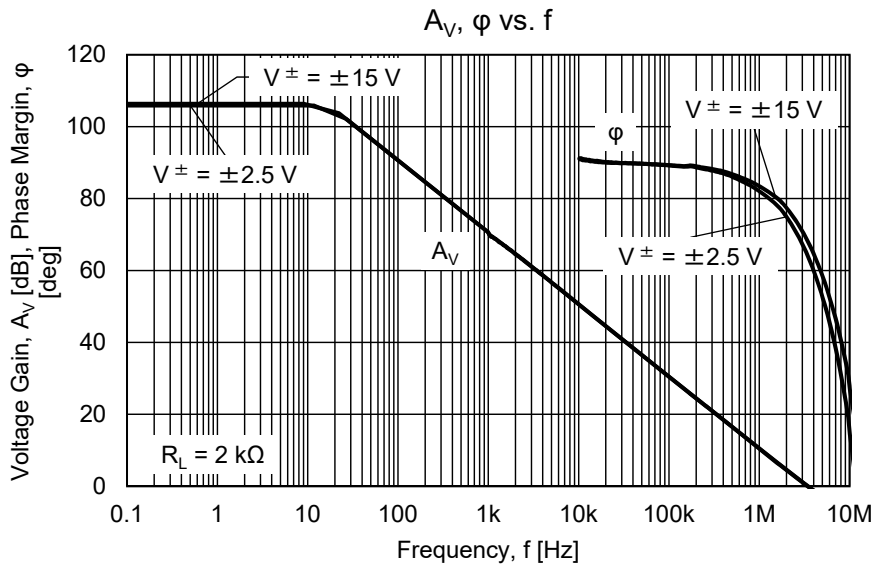
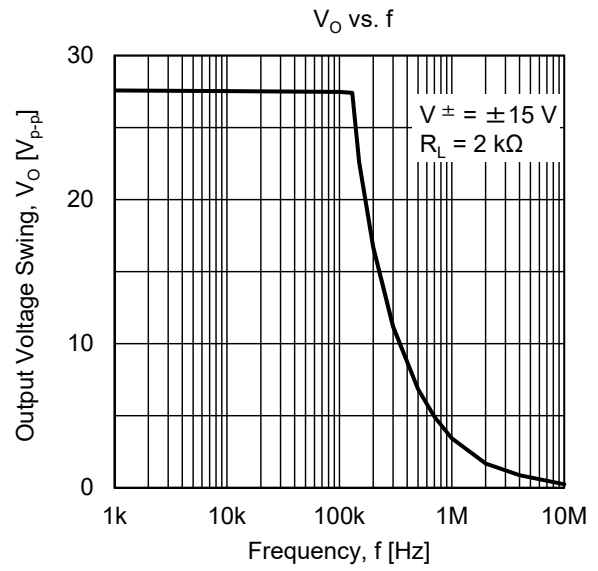
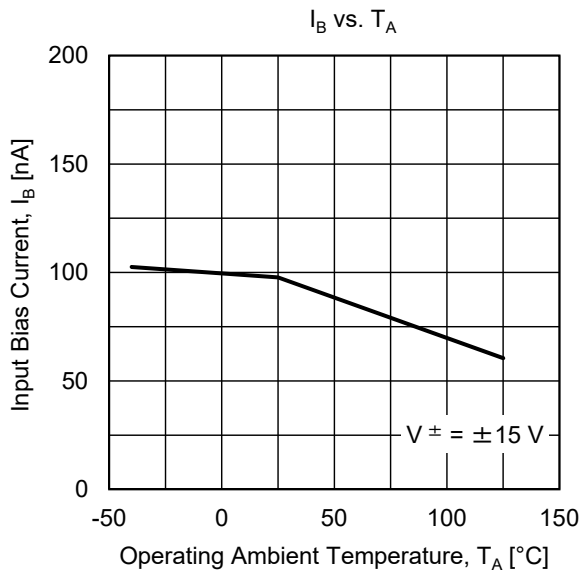
Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Test Condition
Input Offset Voltage	V_{IO}		±2	±5	mV	
Input Offset Current	I_{IO}		±6	±75	nA	
Input Bias Current ^{Note 7}	I_B		150	500	nA	
Large Signal Voltage Gain	A_v	25000	300000			$R_L \geq 2\text{ k}\Omega$
Circuit Current ^{Note 8}	I_{CC}		6	9	mA	$I_o = 0\text{ A}$
Common Mode Rejection Ratio	CMR	70	80		dB	
Supply Voltage Rejection Ratio	SVR	70	95		dB	
Output Voltage Swing	V_{Om}	3.7	4		V	$R_L \geq 2\text{ k}\Omega$ (Connected to GND)
		0	0			
Common Mode Input Voltage Range	V_{ICM}	0		$V^+ - 1.8$	V	
Output Source Current	I_o SOURCE	10	30		mA	$V_{IN(+)} = +1\text{ V}$, $V_{IN(-)} = 0\text{ V}$
Output Sink Current	I_o SINK	10	30		mA	$V_{IN(+)} = 0\text{ V}$, $V_{IN(-)} = +1\text{ V}$
Slew Rate	SR		7		V/μs	$A_v = 1$ (Rise)

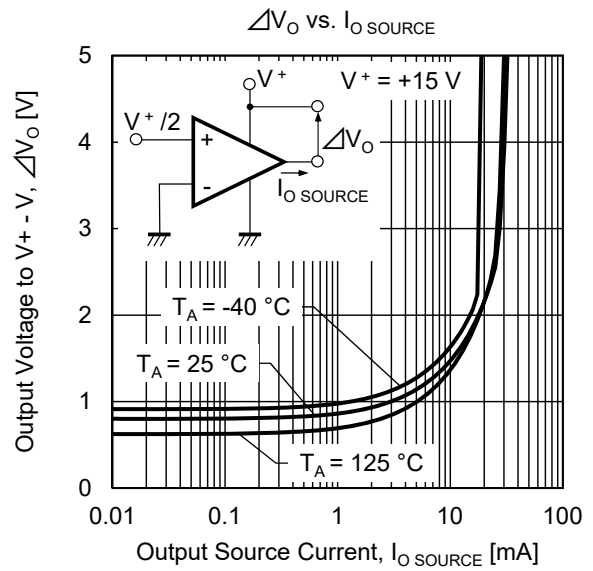
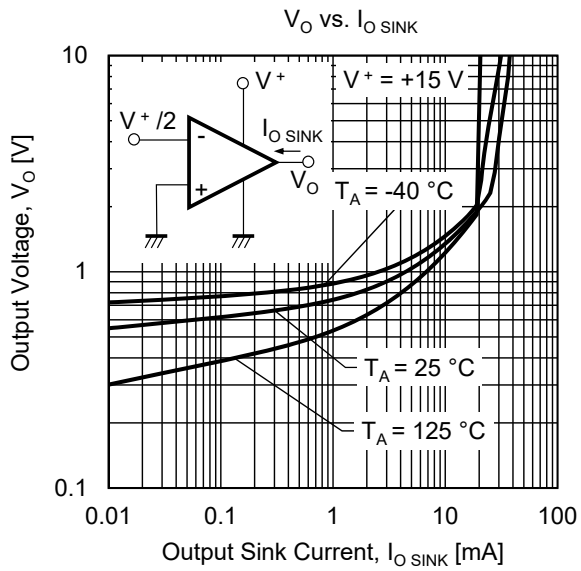
【Note】 7. The current flow direction of the input bias is out from the IC because the first stage of the IC composed of PNP transistor.

8. Current flowing through the internal circuit. This current flow regardless of the channel used.

CHARACTERISTICS CURVE (T_A = 25 °C, TYP.) (REFERENCE VALUE)





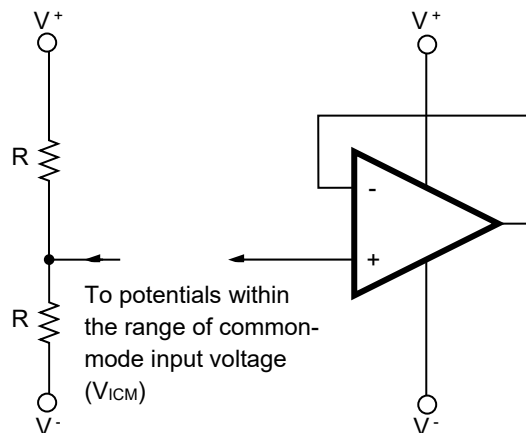


USE WITH PRECAUTIONS

- **Managing unused circuits**

If there is an unused circuit, the following connection is recommended.

Process example of unused circuits



Remark: In this example, an intermediate potential between V + and V - is applied.

- **Ratings of input/output pin voltage**

When the voltage of input/output pin exceeds the absolute maximum rating, the parasitic diode within the IC may conduct, causing characteristics degradation or damage. In addition, if the input pin is lower than V-, or the output pin exceeds the power supply voltage, it is recommended to make a clamping circuit using a diode with low forward voltage (e.g.: Schottky diode) as protection.

- **Range of common-mode input voltage**

When the supply voltage does not meet the condition of electrical characteristics, the range of common-mode input voltage is as follows.

$$V_{ICM} \text{ (TYP.)} : V^- \sim V^+ - 1.8 \text{ [V]} \text{ (} T_A = 25 \text{ }^\circ\text{C)}$$

During designing, do include some tolerance by considering temperature characteristics etc.

- **Maximum Output Voltage**

The TYP. value range of the maximum output voltage when the supply voltage does not meet the condition of electrical characteristics is as follows:

$$V_{Om^+} \text{ (TYP.)} : V^+ - 1 \text{ [V]} \text{ (} T_A = 25 \text{ }^\circ\text{C}), V_{Om^-} \text{ (TYP.)} : V^- + 0.7 \text{ [V]} \text{ (} T_A = 25 \text{ }^\circ\text{C)}$$

During designing, do include some tolerance by considering characteristics variation, temperature characteristics and so on. In addition, also note that the output voltage range ($V_{om^+} - V_{om^-}$) will become narrow when the output current increases.

- **Output Operation**

This IC will not be able to sink output current when the output voltage is $V^- + 0.7 \text{ V}$ and below. In this case, the output voltage level can be improved to the V- side by connecting the load resistor between the output terminal and V- to sink the current at the load resistor. (The effect will differ depending on the flow of current in the load resistance.)

- **Handling of ICs**

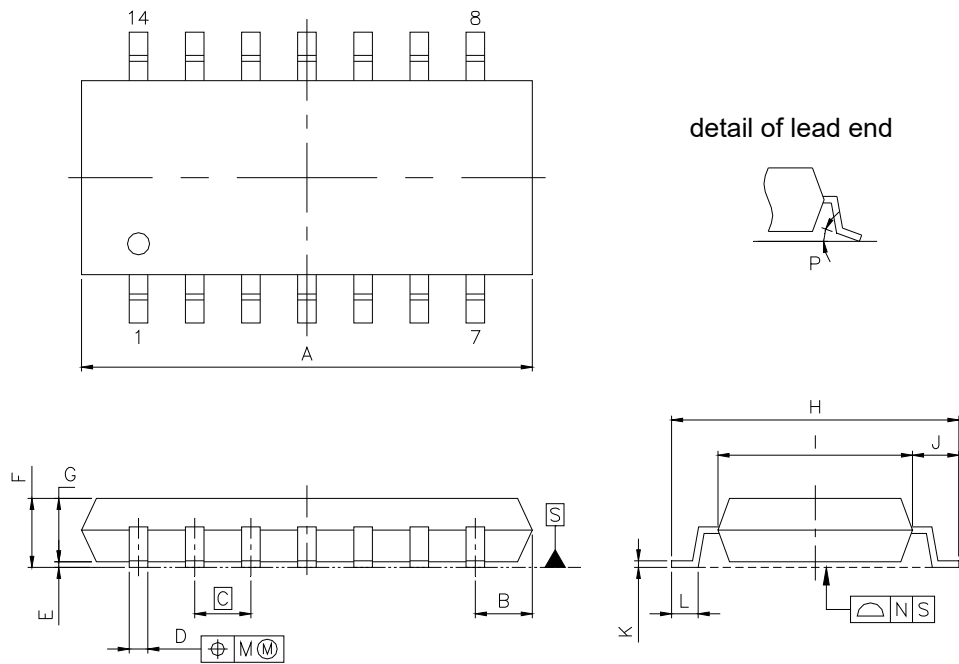
When stress is added to the ICs due to warpage or bending of a board, the characteristic may fluctuates due to piezoelectric effect. Therefore, pay attention to warpage or bending of a board.

PACKAGE DRAWINGS

14-PIN PLASTIC SOP

JEITA Package code	RENESAS code	Previous code	MASS (TYP.) [g]
P-SOP14-0225-1.27	PRSP0014DI-A	P14GR-50-225B	0.14

Unit : mm



NOTE

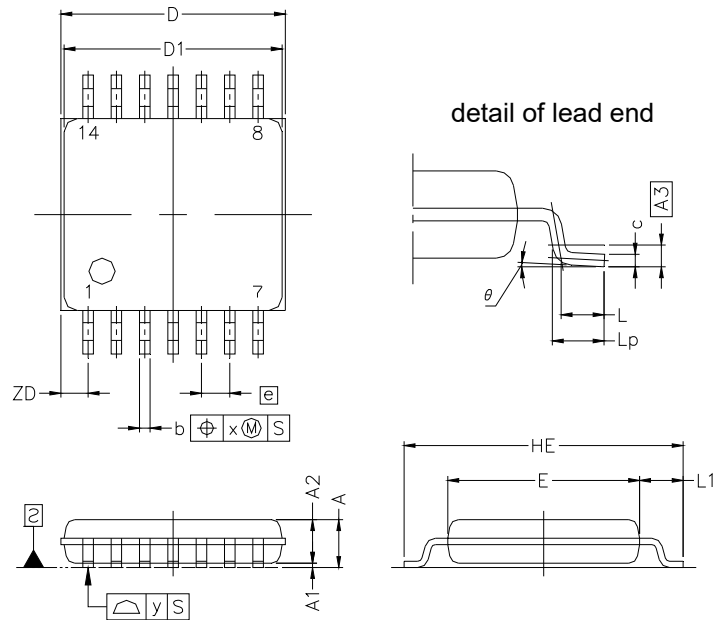
Each lead centerline is located within 0.12 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
A	10.2 ±0.26
B	1.42 MAX
C	1.27 (T.P)
D	0.42 ^{+0.08} / _{-0.07}
E	0.1 ±0.1
F	1.59 ^{+0.21} / _{-0.2}
G	1.49
H	6.5 ±0.2
I	4.4 ±0.1
J	1.1 ±0.16
K	0.17 ^{+0.08} / _{-0.07}
L	0.6 ±0.2
M	0.1
N	0.10
P	3° ^{+7°} / _{-3°}

14-PIN PLASTIC TSSOP

JEITA Package code	RENESAS code	Previous code	MASS(TYP.) [g]
P-TSSOP14-0225-0.65	PTSP0014JB-A	P14GR-65-9LG-1	—

Unit : mm



NOTE

Each lead centerline is located within 0.10 mm of its true position at maximum material condition.

ITEM	MILLIMETERS
D	5.15 ±0.15
D1	5.00 ±0.10
E	4.40 ±0.10
HE	6.40 ±0.20
A	1.20 MAX.
A1	0.10 ±0.05
A2	1.00 ±0.05
A3	0.25
b	0.24 ^{+0.06} _{-0.05}
c	0.145 ±0.055
L	0.5
Lp	0.60 ±0.15
L1	1.00 ±0.20
θ	3° ^{+5°} _{-3°}
e	0.65
x	0.10
y	0.10
ZD	0.625

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7. 虽然瑞萨电子一直致力于提高瑞萨电子产品的质量和可靠性，但是，半导体产品有其自身的具体特性，如一定的故障发生率以及在某些使用条件下会发生故障等。除非是瑞萨电子产品数据表或其他瑞萨电子文档中指定为高可靠性产品或用于恶劣环境的产品，否则瑞萨电子产品未进行防辐射设计。用户负责执行安全措施，以避免因瑞萨电子产品失效或发生故障而造成身体伤害、火灾导致伤害或损害和/或其他对公众构成危险事故。例如进行软硬件安全设计（包括但不限于冗余设计、防火控制以及故障预防等）、适当的老化处理或其他适当的措施等。由于对微机电软件单独进行评估非常困难且不实际，所以请用户自行负责对最终产品或系统进行安全评估。
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10. 瑞萨电子产品的买方或分销商，或者分销、处置产品、或以其他方式向第三方出售或转让产品的任何其他方有责任事先向所述第三方通知本文件规定的内容和条件。
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