## RFIC Preliminary 2016.06 Rev1.0

## DESCRIPTION

The SW373C is a SOI CMOS MMIC SP3T switch. The high power performance and low insertion loss makes the device ideally suitable for handset and data card applications, as well as any other general purpose usage for $T x / R x$ selection or antenna diversity function operating up to 6 GHz . The SW373C is housed in a miniature $2 m m x 2 m m$, 12-pin, QFN leadless (Pb free) package, An internal negative voltage generator and decoder are included in the design and no external DC blocking capacitors on the RF ports are needed.

## Block Diagram



RF2

| RF2 | Logic Control Table |  | $\begin{aligned} & \mathrm{High}=1.6 \mathrm{~V} \text { to } 3.0 \mathrm{~V} \\ & \mathrm{Low}=0 \mathrm{~V} \text { to } 0.4 \mathrm{~V} \end{aligned}$ |
| :---: | :---: | :---: | :---: |
| V1 | V2 | VDD | State |
| Low | High | 2.5-5V | RF1-ANT |
| High | Low | 2.5-5V | RF2-ANT |
| High | High | 2.5-5V | RF3-ANT |
| Low | Low | $2.5-5 \mathrm{~V}$ | Shut down |

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## Pin Definition and Function

| Pin No. | Name | Description |
| :---: | :---: | :---: |
| 1 | RF1 | RF port 1 |
| 2 | GND | Ground |
| 3 | RF3 | RF port 3 |
| 4 | V1 | Control voltage 1 |
| 5 | V2 | Control voltage 2 |
| 6 | VDD | Power Supply |
| 7 | GND | Ground |
| 8 | GND | Ground |
| 9 | RF2 | RF port 2 |
| 10 | GND | Ground |
| 11 | RFC | Antenna port |
| 12 | GND | Ground |

$\mathrm{High}=1.6 \mathrm{~V}$ to 3.0 V
Low $=0 \mathrm{~V}$ to 0.4 V

## KEY FEATURES

## - Low Insertion:

0.6dB @ 2.5GHz

- High Isolation:

25dB @ 0.1~3GHz

- Low Harmonics > 75dBc @ 35dBm
- 1.8V control voltage
- Wide supply range from 2.5 V to 4 V
- ESD Protection at all ports


## Absolute Maximum Ratings

| $\underline{\text { Parameter }}$ | Rating | Unit |
| :---: | :---: | :---: |
| Operating Voltage | +5 | V |
| RF Input Power <br> (under acceptable bias state, $>500 \mathrm{MHz}$ | +37 | dBm |
| Operating Ambient Temperature | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |
| ESD |  |  |
| нвм | 1000 V |  |

## Important Note:

The information provided in this datasheet is deemed to be accurate and reliable only at present time. RFIC Technology Corp. reserves the right to make any changes to the specifications in this datasheet without prior notice.


Caution: ESD Sensitive
Appropriate precaution in handling, packaging And testing devices must be observed.

Evaluation Board Schematic


| Component | Size | Value | Note |
| :---: | :---: | :---: | :---: |
| C1 | 0402 | 1000 pF | Optional |
| C2 | 0805 | 10 nF | Optional |
| C3 | 0402 | 1000 pF | Optional |
| C4 | 0402 | 1000 pF | Optional |

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## Electrical Characteristics for $25^{\circ} \mathrm{C}$ Ambit Temperature

Logic High $=1.8 \mathrm{~V}$; Logic Low $=0 \mathrm{~V} ; \mathrm{VDD}=3 \mathrm{~V} ; \mathrm{TA}=25^{\circ} \mathrm{C}$; unless otherwise noted

| Parameter | Specification |  |  | Units | Notes |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min | Typ. | Max |  |  |
| Insertion Loss (IL) |  | $\begin{aligned} & 0.4 \\ & 0.5 \\ & 0.6 \\ & 0.8 \end{aligned}$ | $\begin{aligned} & 0.5 \\ & 0.6 \\ & 0.7 \\ & 1.5 \end{aligned}$ | dB | $\begin{aligned} & 0.5-1.0 \mathrm{GHz} \\ & 1.0-2.0 \mathrm{GHz} \\ & 2.0-3.0 \mathrm{GHz} \\ & 3.0-6.0 \mathrm{GHz} \end{aligned}$ |
| Isolation (ISO) | $\begin{aligned} & 35 \\ & 30 \\ & 25 \\ & 19 \end{aligned}$ | $\begin{aligned} & 40 \\ & 35 \\ & 30 \\ & 25 \end{aligned}$ |  | dB | $\begin{aligned} & 0.5-1.0 \mathrm{GHz} \\ & 1.0-2.0 \mathrm{GHz} \\ & 2.0-3.0 \mathrm{GHz} \\ & 3.0-6.0 \mathrm{GHz} \end{aligned}$ |
| Return Loss | 10 | 20 |  | dB | $0.5-6.0 \mathrm{GHz}$ |
| IP0.1dB |  | 36.5 |  | dBm | 0.8-6.0GHz |
| IIP3 |  | 66 |  | dBm | $\begin{aligned} & \text { 0.8-3GHz, } \triangle \mathrm{F}=1 \mathrm{MHz}, \mathrm{PIN}=+20 \\ & \mathrm{dBm} / \text { tone } \end{aligned}$ |
| 2 fo | 70 | $\begin{aligned} & 75 \\ & 20 \end{aligned}$ |  | dBc | $\begin{aligned} & 900 \mathrm{MHz}, \mathrm{P}_{\mathrm{ln}}=30 \mathrm{dBm} \\ & 900 \mathrm{MHz}, \mathrm{P}_{\mathrm{ln}}=20 \mathrm{dBm} \end{aligned}$ |
| 3fo | 65 | $\begin{aligned} & 70 \\ & 86 \end{aligned}$ |  | dBc | $\begin{aligned} & 900 \mathrm{MHz}, \mathrm{P}_{\mathrm{ln}}=30 \mathrm{dBm} \\ & 900 \mathrm{MHz}, \mathrm{P}_{\mathrm{ln}}=20 \mathrm{dBm} \end{aligned}$ |
| IMD3 |  | 108 |  | dBm | $\begin{aligned} & \text { Fcw } 1=1.85 \mathrm{GHz}, \text { Pcw } 1=+20 \mathrm{dBm} \\ & \text { Fcw } 2=1.74 \mathrm{GHz}, \text { Pcw } 2=-15 \mathrm{dBm} \end{aligned}$ |
| Switching Speed <br> $\mathrm{T}_{\text {RISE }} / \mathrm{T}_{\text {FALL }}$ <br> $\mathrm{T}_{\mathrm{ON}} / \mathrm{T}_{\text {OFF }}$ |  | $\begin{gathered} 0.25 \\ 0.5 \end{gathered}$ |  | $\begin{aligned} & \text { us } \\ & \text { us } \end{aligned}$ | $10 \%$ to $90 \%$ RF and $90 \%$ to $10 \%$ RF $50 \%$ control to $90 \%$ RF and $50 \%$ control to $10 \%$ RF |
| Startup time |  | 15 |  | us | Shutdown state to any RF switch state |
| Supply Current (Icc) |  | 80 |  | uA | VDD=3.0V |
| Control Current |  | 0.5 |  | uA | $\mathrm{V} 1=\mathrm{V} 2=1.8 \mathrm{~V}$ |
| Shut down mode supply current |  | 5 |  | uA | $\mathrm{VDD}=3.0 \mathrm{~V}, \mathrm{~V} 1=\mathrm{V} 2=0 \mathrm{~V}$ |

Note: All measurements made in a 50 ohm system.
Board loss de-embedded

SW373C

## Package Outline



Bottom View


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## Tape Dimension




A-A SECTION

$$
\begin{aligned}
& \mathrm{AO}=\frac{2.25 \pm 0.10}{\mathrm{~mm}} \\
& \mathrm{BO}=2.25 \pm 0.10 \\
& \mathrm{~mm} \\
& \mathrm{Ko}=\underline{1.15 \pm 0.10} \mathrm{~mm}
\end{aligned}
$$

Unit: mm

| Symbol | Spec. |
| :---: | :---: |
| K1 | - |
| Po | $4.0 \pm 0.10$ |
| P1 | $4.0 \pm 0.10$ |
| P2 | $2.0 \pm 0.05$ |
| Do | $1.50+0.10$ |
| D1 | $1.10 \pm 0.10$ |
| E | $1.75 \pm 0.10$ |
| F | $3.50 \pm 0.05$ |
| $10 P 0$ | $40.0 \pm 0.10$ |
| $W$ | $8.0 \pm 0.20$ |
| $T$ | $0.25 \pm 0.02$ |

Notice:

1. 10 Sprocket hole pitch cumulative tolerance is $\pm 0.1 \mathrm{~mm}$
2. Pocket position relative to sprocket hole measured as true position of pocket not pocket hole.
3. Ao \& Bo measured on a place 0.3 mm above the bottom of the pocket to top surface of the carrier.
4. Ko measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
5. Carrier camber shall be not than 1 mm per

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## Packing

## Reel Dimension



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