## RFIC Preliminary 2016.05 Rev1.1

## DESCRIPTION

## KEY FEATURES

The SW438 is a DPDT GaAs switch, and designed for DC to 6 GHz , dual-band wireless LAN applications. The switch can be used for two voltage inputs (V1 and V2). Depending on the logic voltage level applied to the control pins, the ANT1 and ANT2 pins connect to one of two switched RF outputs (RX or TX) diversity function in a variety of wireless communication systems.
The SW438 is housed in a miniature $1.5 \times 1.5(\mathrm{~mm})$, $6-\mathrm{pin}$, DFN leadless package ( Pb free), and features low insertion loss, high isolation and high linearity, particularly suitable for GSM/3G/LTENVLAN applications where high power switching is required.

## Pin Assignment



DC blocking capacitors are necessary for all RF ports.
The typical value of $\mathrm{C}_{\mathrm{BL}}$ is 22 pF for $>2.4 \mathrm{GHz}$ application.

## Logic Control Table

. Insertion loss: 0.6dB @ 2.4GHz
0.8 dB @ 5.8 GHz
. Isolation: 27dB @ 2.4GHz
23dB @ 5.8 GHz
. High P-1dB:30dBm@3.3V
. DFN 1.5 mmX 1.5 mm -6 Pin

- Lead-Free and RoHS compliant
- Support 1.8V, 3.3V and 5 V control voltage


## Pin Details

| Pin No. | Name | Description |
| :---: | :---: | :---: |
| 1 | ANT2 | Antenna port 2 |
| 2 | V2 | Control voltage 2 |
| 3 | RX | Receive port |
| 4 | TX | Transmit port |
| 5 | V1 | Control voltage 1 |
| 6 | ANT1 | Antenna port 1 |


| V1 | V2 | ANT1- TX | ANT1-RX | ANT2- TX | ANT2-RX |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\text {HIGH }}$ | $\mathrm{V}_{\text {LOW }}$ | OFF | ON | ON | OFF |
| $\mathrm{V}_{\text {LOW }}$ | $\mathrm{V}_{\text {HIGH }}$ | ON | OFF | OFF | ON |

NOTE: High $=+2 \mathrm{~V}$ to +5 V , Low $=+0 \mathrm{~V}$ to +0.2 V
Any state other than described in this Table places the switch into an undefined state.
An undefined state will not damage the device

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## Electrical Characteristics for +3 V Control Voltages

Logic High $=3 \mathrm{~V}$; Logic Low $=0 \mathrm{~V}$; $\mathrm{TA}=25^{\circ} \mathrm{C}$; unless otherwise noted.

| Parameter | Specification |  |  | Units | Notes |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min | Typ. | Max |  |  |
| Insertion Loss |  | $\begin{aligned} & 0.4 \\ & 0.6 \\ & 0.7 \\ & 0.6 \\ & 0.8 \end{aligned}$ | $\begin{aligned} & 0.6 \\ & 0.8 \\ & 0.9 \\ & 0.8 \\ & 1.0 \end{aligned}$ | dB | $\begin{aligned} & \mathrm{DC}-1 \mathrm{GHz} \\ & 1-3 \mathrm{GHz} \\ & 3-5 \mathrm{GHz} \\ & 2.4-2.5 \mathrm{GHz} \\ & 4.9-5.9 \mathrm{GHz} \end{aligned}$ |
| Isolation | $\begin{aligned} & 24 \\ & 22 \\ & 24 \\ & 21 \end{aligned}$ | $\begin{aligned} & 32 \\ & 27 \\ & 24 \\ & 27 \\ & 23 \end{aligned}$ |  | dB | $\begin{aligned} & \mathrm{DC}-1 \mathrm{GHz} \\ & 1-3 \mathrm{GHz} \\ & 3-5 \mathrm{GHz} \\ & 2.4-2.5 \mathrm{GHz} \\ & 4.9-5.9 \mathrm{GHz} \end{aligned}$ |
| Input/Output RL |  | 15 |  | dB | DC-6GHz |
| P1dB |  | $\begin{aligned} & 31 \\ & 30 \\ & 21 \\ & \hline \end{aligned}$ |  | dBm | $\begin{aligned} & 0.5-6 \mathrm{GHz} \\ & \text { VLow }=0 \mathrm{~V}, \mathrm{~V}_{\text {HIGH }}=5 \mathrm{~V} \\ & \text { VLOW }=0 \mathrm{~V}, \mathrm{~V}_{\text {HIGH }}=3.3 \mathrm{~V} \\ & \text { VLOW }=0 \mathrm{~V}, \mathrm{~V}_{\text {HIGH }}=1.8 \mathrm{~V} \end{aligned}$ |
| IIP3 |  | 46 |  | dBm | $\Delta \mathrm{F}=1 \mathrm{MHz}, \mathrm{PIN}=+20 \mathrm{dBm} /$ tone @ $0.5-6 \mathrm{GHz}$ |
| Switching Time |  | $\begin{aligned} & 100 \\ & 100 \end{aligned}$ |  | ns | 10/90\% or 90/10\% RF 50\% CLT to 90/10\% RF |
| Control Current | $\begin{gathered} 0 \\ 1.8 \end{gathered}$ |  | $\begin{gathered} 0.2 \\ 5 \end{gathered}$ | V | VLow 10uA control current <br> Vhigh 100uA control current |

Note: Insertion Loss and Isolation are measured from RFC to RF1, RF2

Absolute Maximum Ratings

| Parameter | Rating | Unit |
| :---: | :---: | :---: |
| DC Power Supply For Collector | +6 | V |
| RF Input Power $0.5-6 \mathrm{GHz}$ | +35 | dBm |
| Operating Ambient Temperature | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | -60 to +150 | ${ }^{\circ} \mathrm{C}$ |
| MSL | LEVEL 1 |  |
| ESD | HBM Class 1A |  |

## Important Note:

The information provided in this datasheet is deemed to be accurate and reliable only at present time. RFIC Technology Corp. reserves the right to make any changes to the specifications in this datasheet without prior notice.

Caution: ESD Sensitive
Appropriate precaution in handling, packaging
And testing devices must be observed.

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## Typical Characteristic Chart (0, +3V)





## Package Outline

Top View

\#1

Bottom View

$4 \times M$

Side View


SW438

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## Packing



END
START


| ITEM |  | SPECIFICATIDN (пг) (гііпіпии) |
| :---: | :---: | :---: |
| LEADER | CLVER TAPE WITH EMPTY CAVITIES | B40(210新) |
| TRAILER | CLVER TAPE WITH EMPTY CAVITIES | 400(100格) |
| FIXING TAPE |  | 100 |
| PRDTECTIVE BAND ( $\mathrm{t}=1.0 \mathrm{~mm}$ ) |  | 1200 |


| $\begin{aligned} & \text { PKG } \\ & \text { TYPE } \end{aligned}$ | Tape Width (mm) | Reel Size | Devices <br> Per <br> Reel |
| :---: | :---: | :---: | :---: |
|  | 8 | $7^{*}$ | 3000 |



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NOTES:

1. ALL DIUS IN mm.
2. COVER TAPE TIDTH:5. $50 \pm 0.10$
3. MOLD\# DFN1. $5 \times 1.5 \times 0.5$
4. 10 SPROCKET HOLE PITCH CLMULATIVE TOLERANCE $\pm 0$. 20MAX.
5. CAVBER NOT TO EXCEED 1 VM IN 100 MM
6. THE DERECTION OF VIEN: $\rightarrow$ (9).

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