



RVT35HHTFWCA0

IPS RGB 3.5" LCD TFT DATASHEET

Rev.1.0
2021-07-22

| ITEM | CONTENTS | UNIT |
|--------------------------------|---------------------------------------|-------------------|
| LCD Type | TFT/Transmissive/Normally Black/IPS | / |
| Size | 3.5 | Inch |
| Viewing Direction | Free | / |
| Outside Dimensions (W x H x D) | 93.50 x 64.70 x 5.94 | mm |
| Active Area (W x H) | 70.08 x 52.56 | mm |
| Pixel Pitch (W x H) | 0.219 x 0.219 | mm |
| Resolution | 320 x 240 (RGB) | / |
| Brightness | 800 | cd/m ² |
| Color Depth | 16.7 M | / |
| Pixel Arrangement | RGB Vertical Stripe | / |
| Driver IC of Board | ST7272A | / |
| Interface | RGB | / |
| With/Without Touch | With Projected Capacitive Touch Panel | / |
| CTP Driver | ILI2132A | / |
| Touch Interface | USB/I2C/Optional UART | / |
| Weight | 68 | g |

Note 1: RoHS3 compliant**Note 2:** LCM weight tolerance: $\pm 5\%$.



1. REVISION RECORD

| REV NO. | REV DATE | CONTENTS | REMARKS |
|---------|------------|-----------------|---------|
| 1.0 | 2021-07-22 | Initial Release | |



2. CONTENTS

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3. MODULE CLASSIFICATION INFORMATION

| RV | T | 35 | H | H | T | F | W | C | A0 |
|----|----|----|----|----|----|----|----|----|-----|
| 1. | 2. | 3. | 4. | 5. | 6. | 7. | 8. | 9. | 10. |

| NO. | PARAMETER | SYMBOL |
|-----|------------------|---------------------------------|
| 1. | BRAND | RV – Riverdi |
| 2. | PRODUCT TYPE | T – TFT Standard |
| 3. | DISPLAY SIZE | 35 – 3.5" |
| 4. | MODEL SERIAL NO. | H – High Brightness, IPS |
| 5. | RESOLUTION | H – 320 x 240 px |
| 6. | INTERFACE | T – TFT LCD, RGB |
| 7. | FRAME | F – With Mounting Metal Frame |
| 8. | BACKLIGHT TYPE | W – LED White |
| 9. | TOUCH PANEL | C – With Capacitive Touch Panel |
| 10. | VERSION | A0 – aTouch |

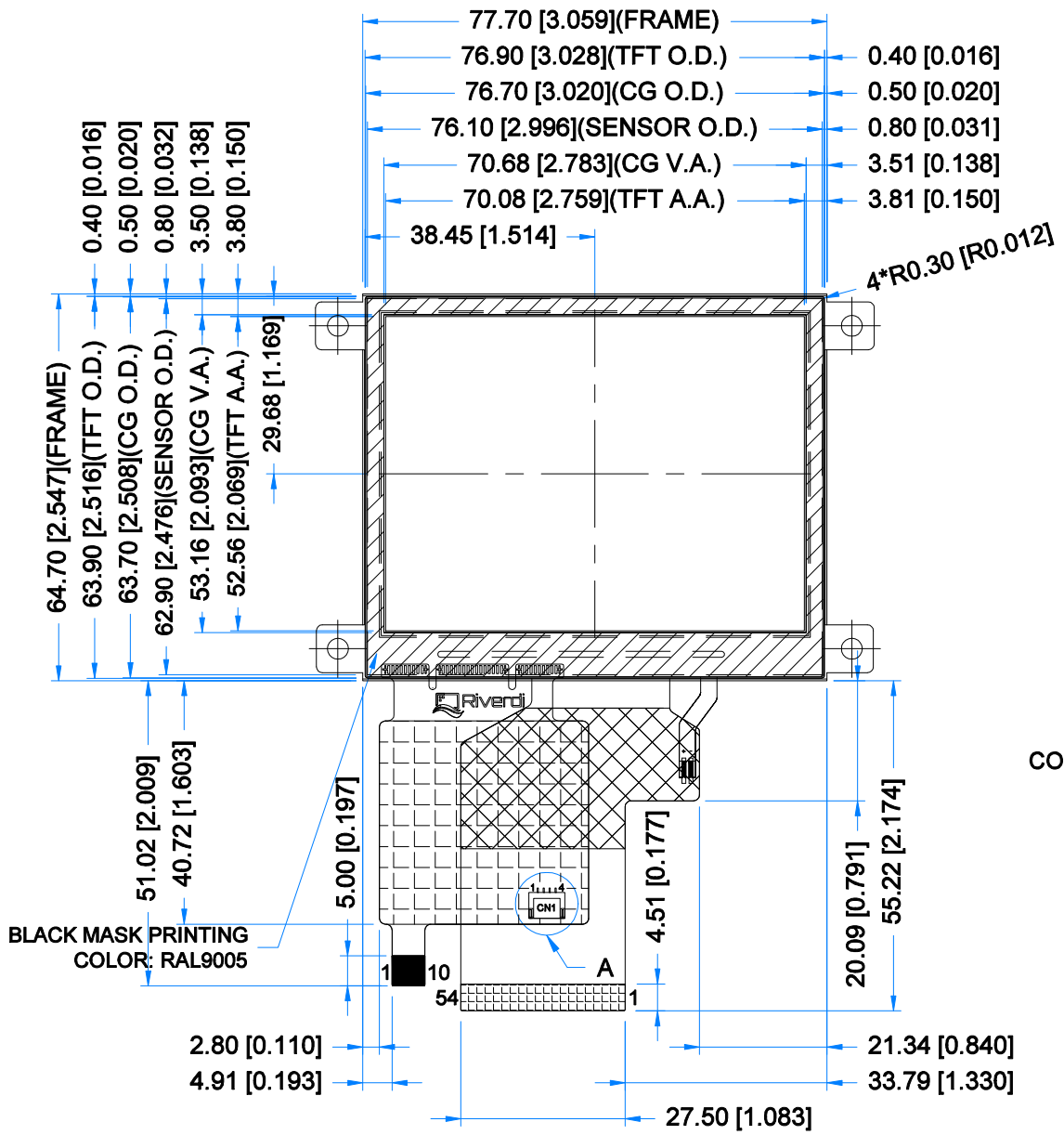
4. ASSEMBLY GUIDE

4.1 Mounting frame

For dimensions 3.5", 4.3", 5.0", 7.0" and 10.1" the product with mounting frame version is available. Thanks to the four catches attached to the side, frame provides strong assembly to the surface by mounting element (like the screw, see Figure 1). The frames are specially designed to fit Riverdi products perfectly. The diameter of the mounting hole is 3.5mm.

Figure 1. Mounting frame





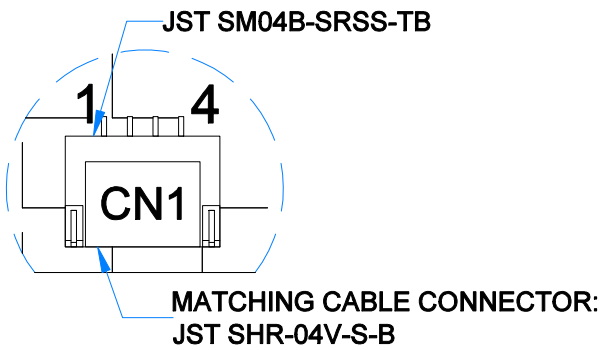
COMPONENT AR

1.70 [0.067]

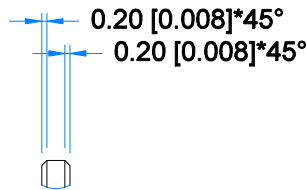
3.50 [0.138]

STIFFENER

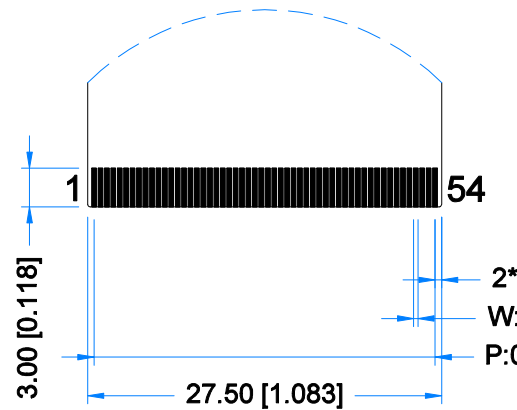
DETAIL A
SCALE 4:1



DETAIL B
SCALE 4:1



DETAIL C
SCALE 2:1



TFT NOTES:

1. LCD TYPE: TRANSMISSIVE, NORMALLY BLACK, IPS
2. RESOLUTION: 320x240
3. VIEWING ANGLE: FREE
4. DRIVER IC: ST7272A
5. DRIVING VOLTAGE: 3.3V
7. BACKLIGHT: 9 LEDs, $V_f=9.6V$, $I_f=60mA$

TP NOTES:

1. TP STRUCTURE: G+G
2. CG THICKNESS: 1.10mm [0.043inch]
3. DRIVER IC: ILI2132A
4. INTERFACE: USB; I2C; OPTIONAL UART
5. OPERATING VOLTAGE: 3.3V(CTP I2C); 5.0V(CTP USB);

GENERAL NOTES:

1. MODULE SUR
2. OPERATING T
3. STORAGE TE
4. WITHOUT IND
5. RoHS COMPL



6. ABSOLUTE MAXIMUM RATINGS

| PARAMETER | SYMBOL | MIN | MAX | UNIT | NOTE |
|-------------------------------|----------|------|-----|------|-----------|
| Operating Ambient temperature | T_{OP} | -20 | 70 | °C | At 25±5°C |
| Storage Temperature | T_{ST} | -30 | 80 | °C | |
| Operating Ambient Humidity | H_{OP} | 10 | - | % RH | |
| Power for Circuit Driving | V_{DD} | -0.3 | 5.0 | V | |

Note Exceeding the maximum values may cause improper operation or permanent damage to the unit.

7. ELECTRICAL CHARACTERISTICS

| PARAMETER | SYMBOL | MIN | TYP | MAX | UNIT | |
|---------------------------------|--------------|----------------|---------------------|-----|------|---------------------|
| Power Supply for Analog Circuit | VDD | 3.0 | 3.3 | 3.6 | V | |
| Logic Input Voltage | Low Voltage | VIL | 0 | - | | 0.3 V _{DD} |
| | High Voltage | VIH | 0.7 V _{DD} | - | | V _{DD} |
| Logic Output Voltage | Low Voltage | VOL | 0 | - | | 0.2 V _{DD} |
| | High Voltage | VOH | 0.8 V _{DD} | - | - | |
| Current of Power Supply | Black Mode | I _b | - | 25 | 30 | mA |
| | Standby Mode | I _w | - | 50 | 60 | uA |

8. BACKLIGHT ELECTRICAL CHARACTERISTICS

| PARAMETER | SYMBOL | MIN | TYP | MAX | UNIT | NOTE |
|-----------------------------|----------|-----|--------|------|-------|-----------|
| Backlight Driving Voltage | V_F | 9.0 | 9.6 | 10.2 | V | Notes 1,2 |
| Backlight Driving Current | I_F | - | 60 | - | mA | |
| Backlight Power Consumption | W_{BL} | - | 576 | - | mW | |
| Backlight Lifetime | - | - | 50,000 | - | hours | Note 3 |

Note 1. Unless specified, the ambient temperature $T_a=25^\circ\text{C}$.

Note 2. The recommended operating conditions refer to a range in which operation of this product is guaranteed. Should this range be exceeded, the operation cannot be guaranteed even if the values may be without the absolute maximum ratings.

Note 3. Operating life means the period in which the LED brightness goes down to 50% of the initial brightness. Typical operating lifetime is the estimated parameter.



9. ELECTRO-OPTICAL CHARACTERISTICS

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 500mm from the LCD surface at a viewing angle of Φ and θ equal to 0°.

| ITEM | SYMBOL | CONDITION | MIN | TYP | MAX | UNIT | RMK | NOTE |
|----------------------------|-------------------|--|-------|-------|-------|-------------------|--------|------|
| Response Time | Tr+Tf | $\theta=0^\circ$ $\phi=0^\circ$ Ta=25 °C | - | 50 | - | ms | FIG 2. | 4 |
| Contrast Ratio | Cr | | - | 700 | - | --- | FIG 3. | 1 |
| Luminance Uniformity | δ WHITE | | - | 75 | - | % | FIG 3. | 3 |
| Surface Luminance | Lv | | 680 | 800 | - | cd/m ² | FIG 3. | 2 |
| Viewing Angle Range | θ | $\phi = 90^\circ$ | - | 80 | - | deg | FIG 4. | 6 |
| | | $\phi = 270^\circ$ | - | 80 | - | deg | FIG 4. | |
| | | $\phi = 0^\circ$ | - | 80 | - | deg | FIG 4. | |
| | | $\phi = 180^\circ$ | - | 80 | - | deg | FIG 4. | |
| CIE (x, y) Chromaticity | Rx | $\theta=0^\circ$ $\phi=0^\circ$ Ta=25 °C | 0.573 | 0.613 | 0.653 | - | FIG 3. | 5 |
| | Ry | | 0.317 | 0.357 | 0.397 | - | | |
| | Gx | | 0.324 | 0.364 | 0.404 | - | | |
| | Gy | | 0.263 | 0.603 | 0.643 | - | | |
| | Bx | | 0.110 | 0.150 | 0.190 | - | | |
| | By | | 0.069 | 0.109 | 0.149 | - | | |
| | Wx | | 0.277 | 0.317 | 0.357 | - | | |
| | Wy | | 0.299 | 0.339 | 0.379 | - | | |

Note 1. Contrast Ratio (CR) is defined mathematically as below, for more information see Figure 2.

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 3.

$$L_v = \text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}$$

Note 3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the minimum luminance of 5 points luminance by maximum luminance of 5 points luminance. For more information see Figure 3.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see Figure 2. The test equipment is Autronic-Melchers's ConoScope series.

Note 5. CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to LCD surface. For more information see Figure 4.

Note 7. For viewing angle and response time testing, the testing data is based on Autronic-Melchers's ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, CIE the test data is based on TOPCON's BM-5 photo detector.

Figure 2. The definition of response time

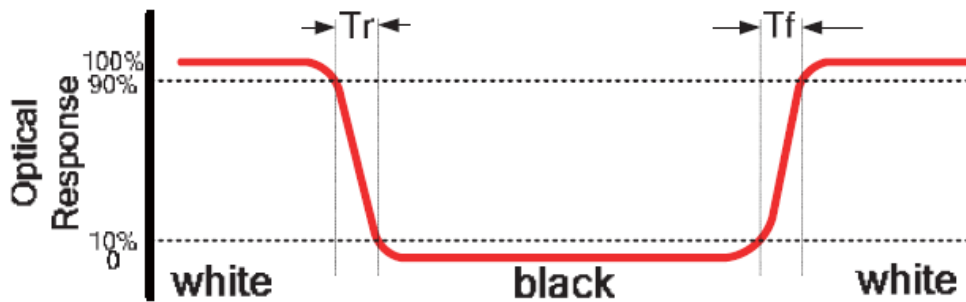


Figure 3. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

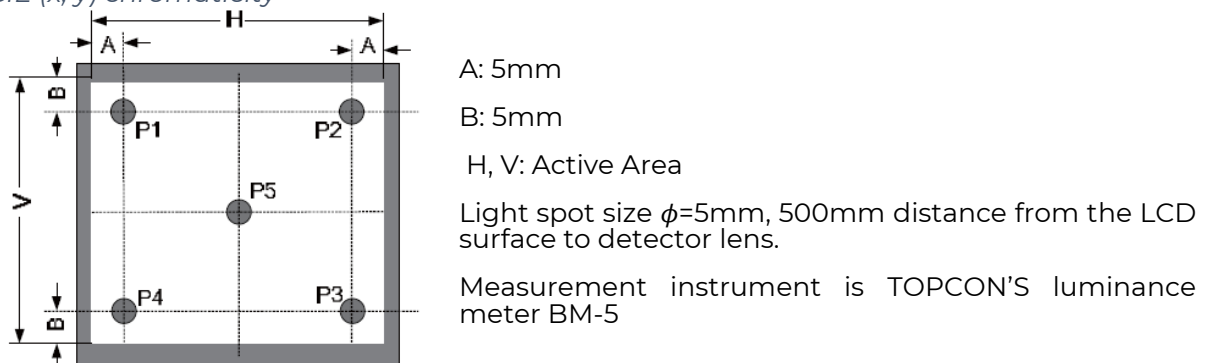
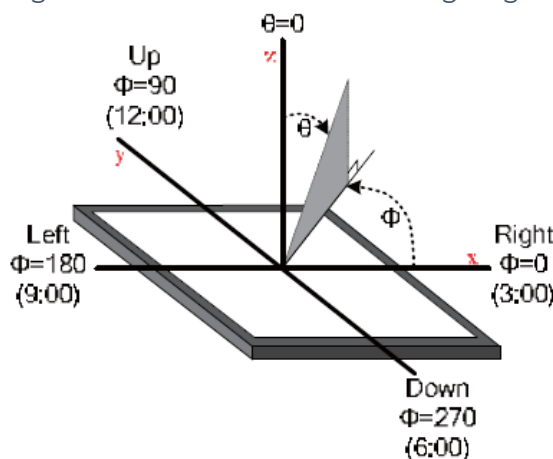
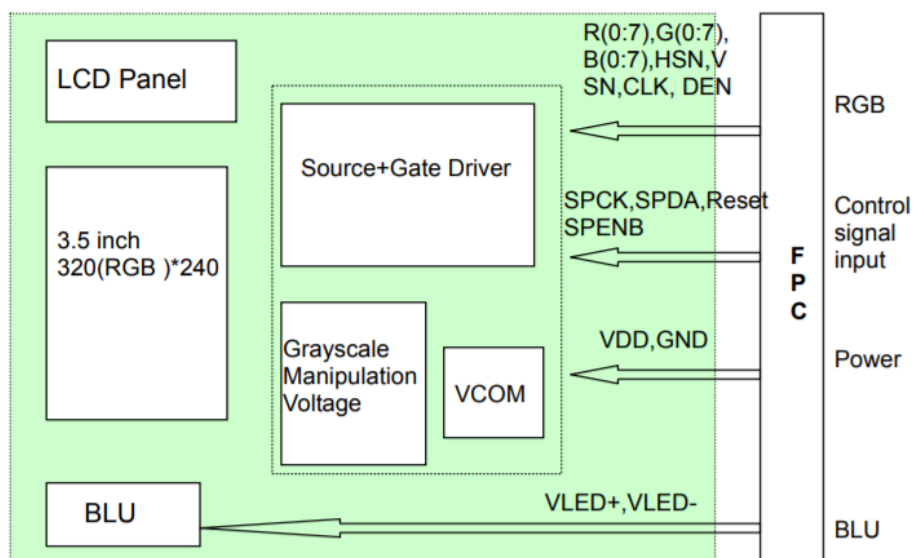


Figure 4. The definition of viewing angle



10. BLOCK DIAGRAM



11. INTERFACE DESCRIPTION

| PIN NO. | SYMBOL | I/O/P | DESCRIPTION |
|---------|--------|-------|-----------------------------------|
| 1 | LED-K | P | Backlight power input PIN cathode |
| 2 | LED-K | P | Backlight power input PIN cathode |
| 3 | LED-A | P | Backlight power input PIN anode |
| 4 | LED-A | P | Backlight power input PIN anode |
| 5 | NC | - | No connection |
| 6 | NC | - | No connection |
| 7 | NC | - | No connection |
| 8 | RESET | I | Reset |
| 9 | NC | I | No connection |
| 10 | NC | I | No connection |
| 11 | NC | I/O | No connection |
| 12-19 | B0-B7 | I | Blue Data |
| 20-27 | G0-G7 | I | Green Data |
| 28-35 | R0-R7 | I | Red Data |
| 36 | HSYNC | I | Horizontal synchronizing signal |
| 37 | VSNC | I | Vertical synchronizing signal |
| 38 | DOTCLK | I | Data Clock |
| 39 | NC | - | No connection |
| 40 | NC | - | No connection |
| 41 | VDD | I | Power supply |
| 42 | VDD | I | Power supply |
| 43-44 | NC | I | No connection |
| 45-47 | NC | - | No connection |
| 48-50 | NC | I | No connection |
| 51 | NC | - | No connection |
| 52 | DEN | I | Data Enable Signal |
| 53 | GND | I | Ground |
| 54 | GND | I | Ground |



11.1 Touch panel assignment

| PIN NO. | SYMBOL | DESCRIPTION | NOTE |
|---------|---------|--|--------|
| 1 | USB_GND | USB_ Ground | |
| 2 | USB_VDD | USB Power for CTP, 5.0V | |
| 3 | USB_D- | USB _Data Signal - | |
| 4 | USB_D+ | USB _Data Signal + | |
| 5 | I2C_GND | I2C _ Ground | |
| 6 | I2C_VDD | I2C _Power for CTP, 3.3 V | |
| 7 | I2C_RST | I2C _Reset Pin, Active low | Note 1 |
| 8 | I2C_SCL | I2C _Clock Input | |
| 9 | I2C_INT | I2C _Interrupt Signal from CTP, Active low | |
| 10 | I2C_SDA | I2C _Data Signal | |

Note 1. External pull-up resistors are required.

11.2 CON1 assignment

| PIN NO. | SYMBOL | DESCRIPTION |
|---------|---------|----------------------------|
| 1 | USB_VDD | USB_Power for CTP, DC 5.0V |
| 2 | USB_D- | USB _Data Signal - |
| 3 | USB_D+ | USB _Data Signal + |
| 4 | USB_GND | USB_Ground |



12. TIMING CHARACTERISTICS

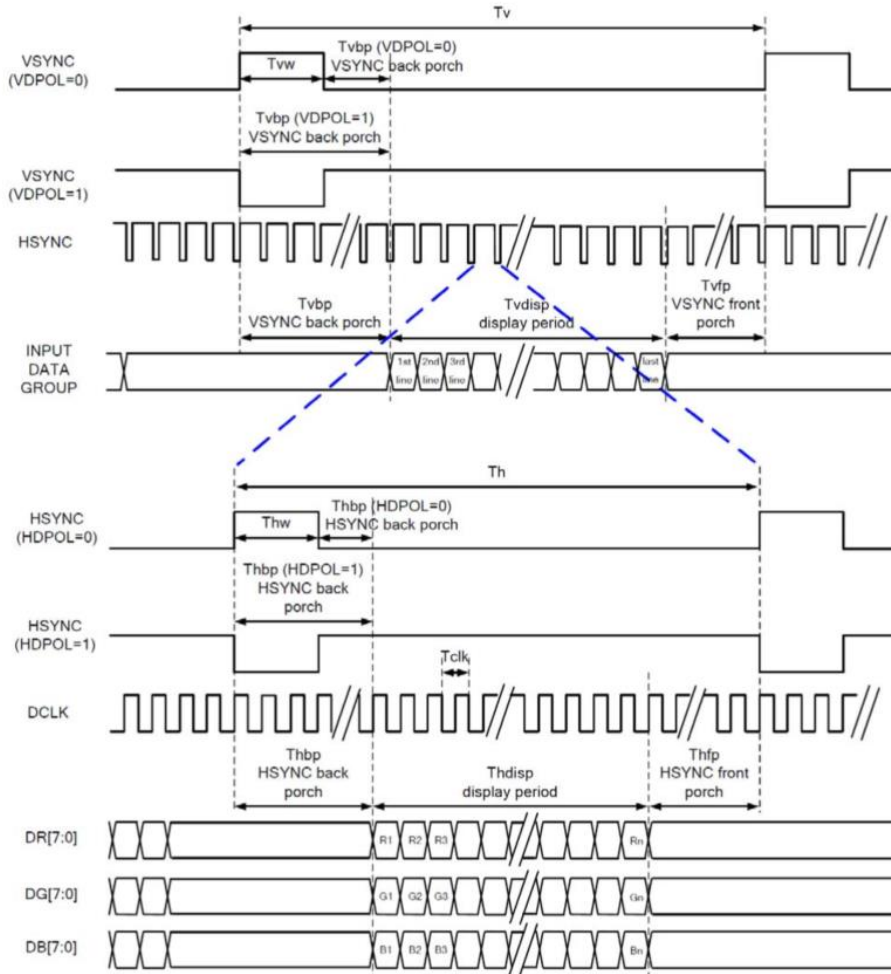
12.1 Input setup timing setting

| RGB MODE SELECTION | DCLK | HSYNC | VSYNC | DE |
|--------------------|-------|-------|-------|-------|
| SYNC-DE Mode | Input | Input | Input | Input |
| SYNC Mode | Input | Input | Input | GND |
| DE Mode | Input | GND | GND | Input |

12.1.1 Parallel 24-bit RGB timing table

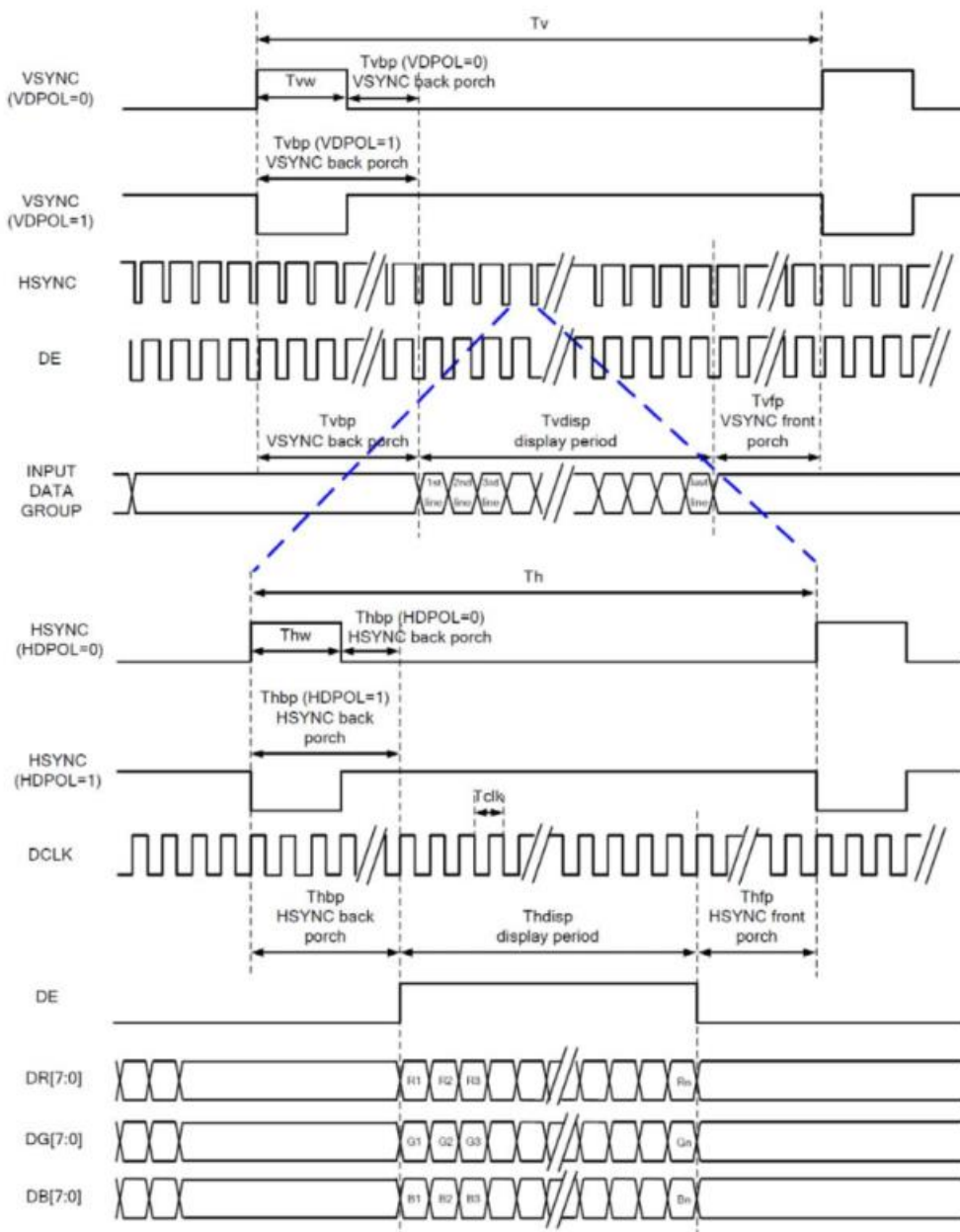
| PARAMETER | SYMBOL | MIN | TYP | MAX | UNIT | NOTE | |
|----------------|----------------|--------|-----|-----|------|-------|---|
| DCLK Frequency | Fclk | 5 | 6 | 8 | MHz | | |
| DCLK Period | Tclk | 125 | 167 | 200 | ns | | |
| HSYNC | Period Time | Th | 325 | 371 | 438 | DCLK | |
| | Display Period | Thdisp | 320 | | | | |
| | Back Porch | Thbp | 3 | 43 | 43 | | SYNC mode back porch control by H_BLANKING [7:0] setting Thbp= H_BLANKING [7:0] |
| | Front Porch | Thfp | 2 | 8 | 75 | | |
| | Pluse Width | Thw | 2 | 4 | 43 | | |
| VSYNC | Period Time | Tv | 244 | 260 | 289 | HSYNC | |
| | Display Period | Tvdisp | 240 | | | | |
| | Back Porch | Tvbp | 2 | 12 | 12 | | SYNC mode back porch control by V_BLANKING [7:0] setting Tvbp= V_BLANKING [7:0] |
| | Front Porch | Tvfp | 2 | 8 | 37 | | |
| | Pluse Width | Tvw | 2 | 4 | 12 | | |

Note. It's necessary to keep Tvbp=12 and Thbp=43 in sync mode. DE mode is unnecessary to keep it.



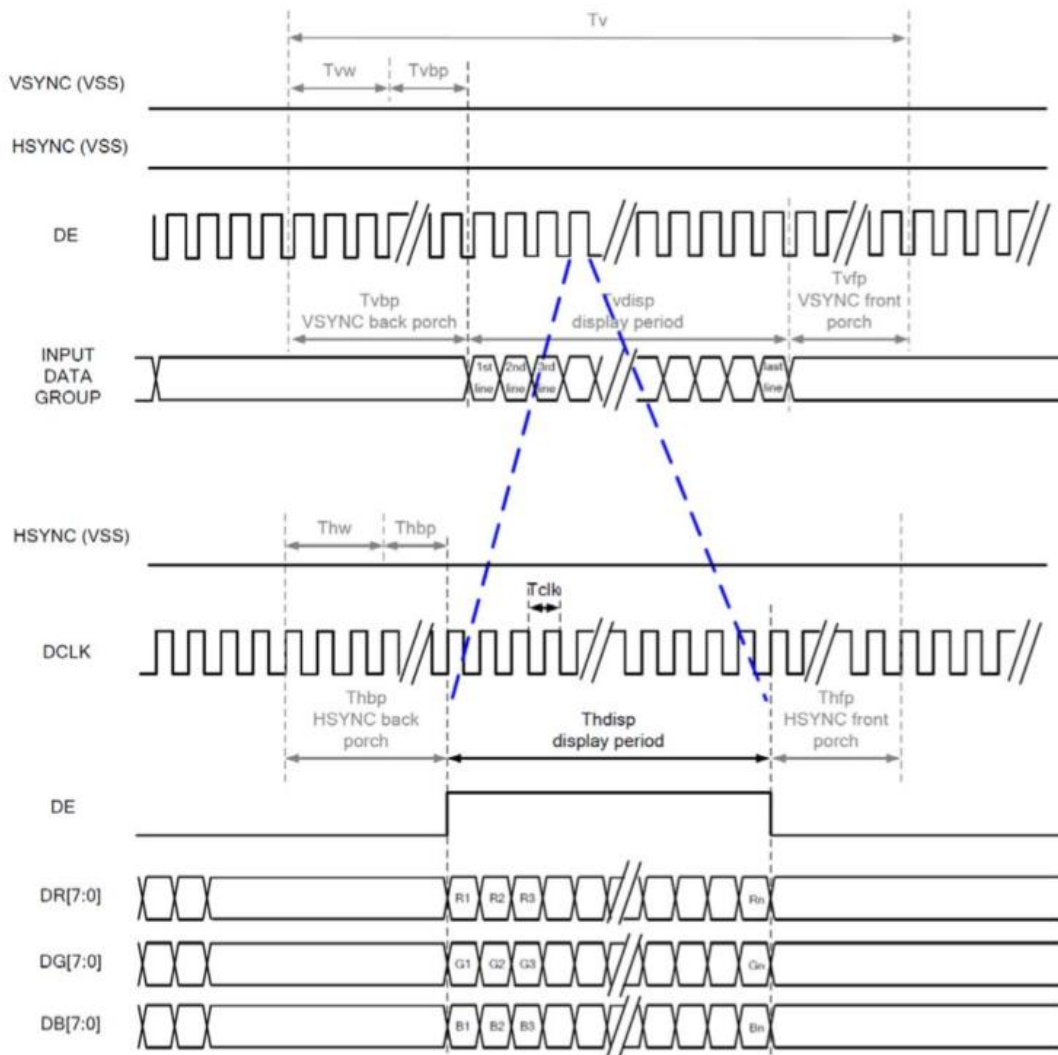


12.1.2 SYNC-DE mode timing diagram





12.1.3 DE mode timing diagram



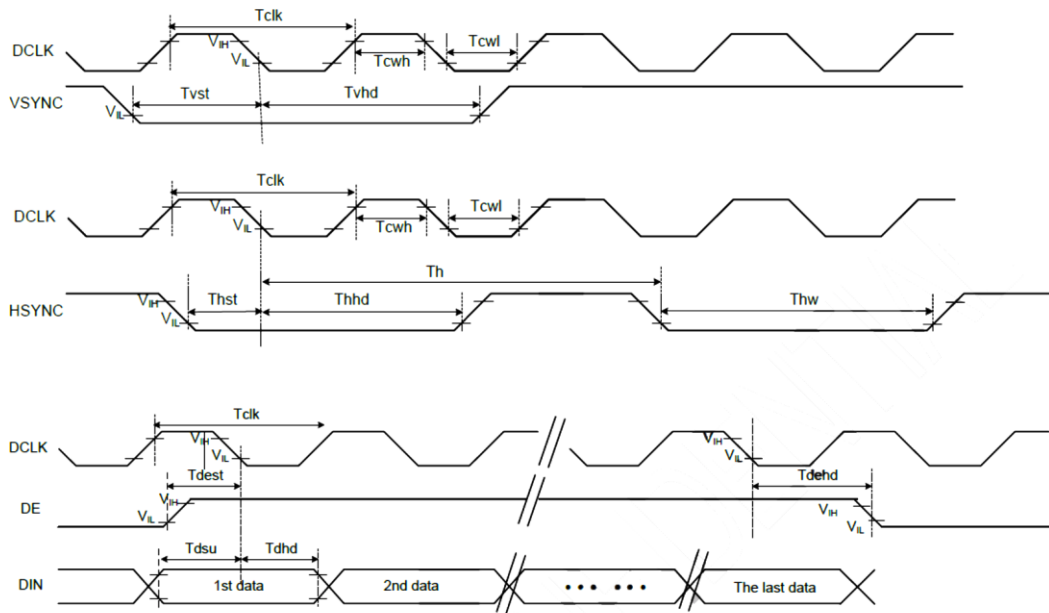
12.2 RGB mode selection table

| RGB MODE SELECTION | DCLK | HSYNC | VSYNC | DE |
|--------------------|-------|-------|-------|-------|
| SYNC-DE Mode | Input | Input | Input | Input |
| SYNC Mode | Input | Input | Input | GND |
| DE Mode | Input | GND | GND | Input |

Note. "Input" means these signals are driven by host side.



12.3 System bus timing for RGB interface

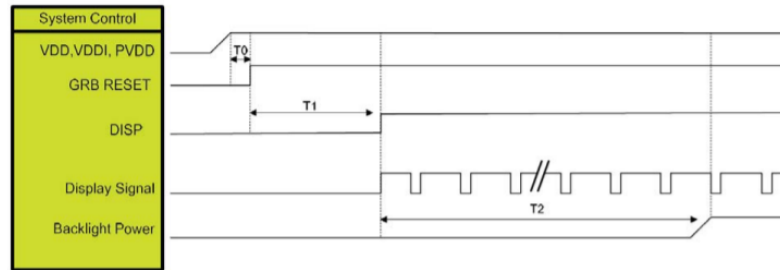


| PARAMETER | SYMBOL | MIN | TYP | MAX | UNIT |
|------------------|------------|-----|-----|-----|------|
| CLK Pulse Duty | T_{clk} | 40 | 50 | 60 | % |
| HSYNC Width | T_{hw} | 2 | - | - | DCLK |
| HSYNC Period | T_h | 55 | 60 | 65 | CLK |
| VSYNC Setup Time | T_{vst} | 12 | - | - | ns |
| VSYNC Hold Time | T_{vhd} | 12 | - | - | |
| HSYNC Setup Time | T_{hst} | 12 | - | - | |
| HSYNC Hold Time | T_{hhd} | 12 | - | - | |
| Data Setup Time | T_{dsu} | 12 | - | - | |
| Data Hold Time | T_{dhd} | 12 | - | - | |
| DE Setup Time | T_{dest} | 12 | - | - | |
| DE Hold Time | T_{dehd} | 12 | - | - | |



12.4 Power ON/OFF sequence

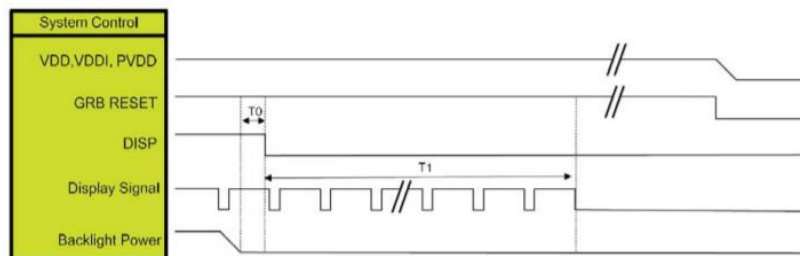
12.4.1 Power on sequence



Note. Display signal: DCLK; VSYNC; HSYNC; DE; DR [7:0]; DG [7:0]; DB [7:0].

| SYMBOL | DESCRIPTION | MIN. TIME | UNIT |
|--------|---|-----------|------|
| T0 | System power stability to GRB RESET signal | 0 | ms |
| T1 | GRB RESET=" High" to DISP=" High" | 10 | |
| T2 | Display Signal output to Backlight Power on | 250 | |

12.4.2 Power off sequence



Note. Display signal: DCLK; VSYNC; HSYNC; DE; DR [7:0]; DG [7:0]; DB [7:0].

| SYMBOL | DESCRIPTION | MIN. TIME | UNIT |
|--------|--|-----------|------|
| T0 | Backlight Power off to DISP=" Low" | 5 | ms |
| T1 | DISP =" Low" to IC internal voltage discharge complete | 80 | ms |



13. CAPACITIVE TOUCH SCREEN PANEL SPECIFICATIONS

13.1 Mechanical characteristics

| DESCRIPTION | SPECIFICATION | REMARK |
|--------------------------|---------------------|--------|
| Touch Panel Size | 3.5 inch | aTouch |
| Outline Dimension of CTP | 76.70 mm x 63.70 mm | |
| Product Thickness | 2.15 mm | |
| Glass Thickness | 1.1 mm | |
| CTP View Area | 70.68 mm x 53.16 mm | |
| Sensor Active Area | 72.08 mm x 54.56 mm | |
| Structure type | Glass + Glass | |
| Surface Hardness | 7H | |

13.2 Electrical characteristics

| DESCRIPTION | SPECIFICATION | REMARK | |
|-------------------------|---------------|--------|--------|
| Power Consumption (IDD) | Active Mode | 90 mA | Note 1 |
| | Sleep Mode | 10 mA | Note 1 |
| Linearity | +/- 1.5mm | | |
| Controller | ILI2132A | | |
| Resolution | 320 x 240 | | |

Note 1. These 2 values will be verified on the real samples.

14. INSPECTION

Standard acceptance/rejection criteria for TFT module

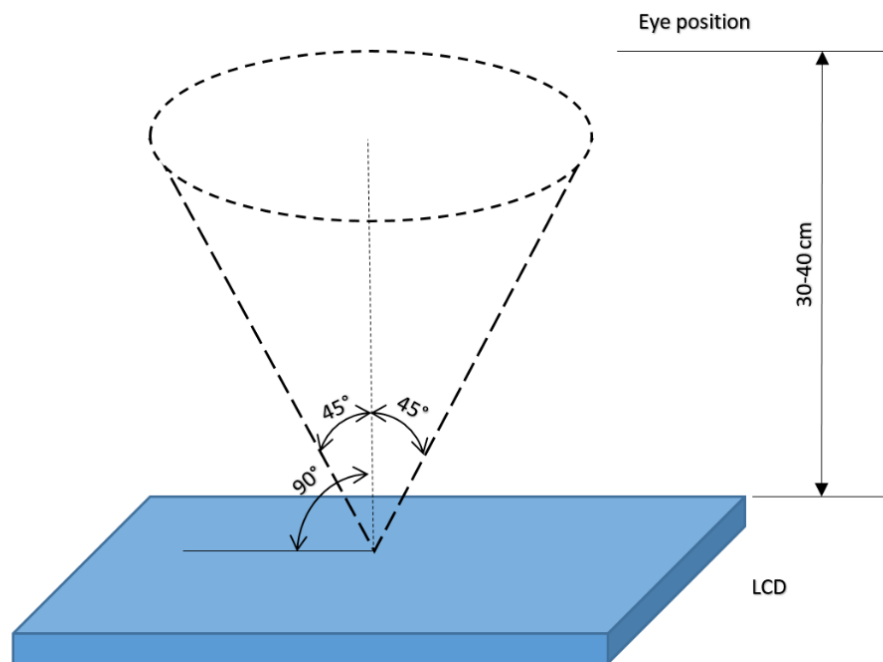
14.1 Inspection condition

Ambient conditions:

- Temperature: $25 \pm 2^\circ\text{C}$
- Humidity: $(60 \pm 10) \%RH$
- Illumination: Single fluorescent lamp non-directive (300 to 700 lux)

Viewing distance: $35 \pm 5\text{cm}$ between inspector bare eye and LCD.

Viewing Angle: U/D: $45^\circ/45^\circ$, L/R: $45^\circ/45^\circ$





14.2 Inspection standard

| ITEM | | CRITERION | | |
|--|--|----------------------|-----------------|---------------|
| Black spots, white spots, light leakage, Foreign Particle (round Type) | <p>$D=(x+y)/2$ Spots density: 10 mm</p> | 3.5" ≤ Size ≤ 5" | | |
| | | Average Diameter | Qualified Qty | |
| | | D ≤ 0.15 mm | Ignored | |
| | | 0.15 mm < D ≤ 0.3 mm | N≤3 | |
| | | 0.3 mm < D | Not allowed | |
| LCD black spots, white spots, light leakage (line Type) | <p>Spots density: 10 mm</p> | 3.5" ≤ Size ≤ 5" | | |
| | | Length | Width | Qualified Qty |
| | | - | W ≤ 0.03 | Ignored |
| | | L ≤ 3.0 | 0.03 < W ≤ 0.05 | 2 |
| | | L ≤ 3.0 | 0.05 < W ≤ 0.1 | 1 |
| | | 3.0 < L | 0.1 < W | Not allowed |
| Bright/Dark Dots | 3.5" ≤ Size ≤ 5" | | | |
| | Item | Qualified Qty | | |
| | Bright dots | N ≤ 1 | | |
| | Dark dots | N ≤ 2 | | |
| Total Bright and Dark Dots | | N ≤ 3 | | |
| Clear spots | Size < 5" | | | |
| | Average Diameter | Qualified Qty | | |
| | D < 0.2 mm | Ignored | | |
| | 0.2 mm < D < 0.3 mm | 3 | | |
| | 0.3 mm < D < 0.5 mm | 2 | | |
| | 0.5 mm < D | 0 | | |
| Spots density: 10 mm | | | | |
| Polarizer bubbles | 3.5" ≤ Size ≤ 5" | | | |
| | Average Diameter | Qualified Qty | | |
| | D ≤ 0.2 mm | Ignored | | |
| | 0.2 mm < D ≤ 0.3 mm | 2 | | |
| | 0.3 mm < D ≤ 0.5 mm | 1 | | |
| | 0.5 mm < D | 0 | | |
| Total Q'ty | | 3 | | |
| Touch panel spots | Size < 5" | | | |
| | Average Diameter | Qualified Qty | | |



| | | | |
|--------------------------------------|---------------------|-----------------|---------------|
| | D < 0.2 mm | | Ignored |
| | 0.2 mm < D < 0.4 mm | | 5 |
| | 0.4 mm < D < 0.5 mm | | 2 |
| | 0.5 mm < D | | 0 |
| Touch panel white line scratch | Size < 5" | | |
| | Length | Width | Qualified Qty |
| | - | W < 0.02 | Ignored |
| | L < 3.0 | 0.02 < W < 0.05 | 2 |
| | L < 2.5 | 0.05 < W < 0.08 | 2 |
| | - | 0.08 < W | 0 |

15. RELIABILITY TEST

| NO. | TEST ITEM | TEST CONDITION | NOTE |
|-----|-------------------------------------|--|--------|
| 1 | High Temperature Storage | 80°C/120 hours | Note 1 |
| 2 | Low Temperature Storage | -30°C/120 hours | |
| 3 | High Temperature Operating | 70 °C /120 hours | |
| 4 | Low Temperature Operating | -20°C/120 hours | |
| 5 | High Temperature and High Humidity | Humidity 40°C, 90%RH, 120Hrs | |
| 6 | Thermal Cycling Test (No operation) | -20°C for 30min, 70°C for 30 min. 100 cycles. Then test at room temperature after 1 hour | Note 2 |
| 7 | Vibration Test | Frequency: 10 ÷ 55 Hz. Stroke: 1.5 mm. Sweep: 10Hz ÷ 55Hz ÷ 10 Hz. 2 hours for each direction of X, Y, Z (Total 6 hours) | |
| 8 | Package Drop Test | Height: 60 cm 1 corner, 3 edges, 6 surfaces | |

Note 1. Sample quantity for each test item is 5 ÷ 10 pcs.

Note 2. Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.



16.LEGAL INFORMATION

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