



RVT43HLBFWCA0

## EVE4 IPS 4.3" LCD TFT DATASHEET

Rev.1.0

2021-09-03

ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally Black/IPS	/
Size	4.3	Inch
Viewing Direction	Free	/
Outside Dimensions (W x H x D)	106.30 x 83.98 x 11.20	mm
Active Area (W x H)	95.04 x 53.86	mm
Pixel Pitch (W x H)	0.198 x 0.198	mm
Resolution	480 x 272 (RGB)	/
Brightness	800	cd/m <sup>2</sup>
Color Depth	16.7 M	/
Pixel Arrangement	RGB Vertical Stripe	/
Driver IC of Board	BT817Q	/
Interface	SPI/QSPI	/
Host Connector	RiBUS, ZIF 20 pin, 0.5mm pitch, down-side contact	/
With/Without Touch	With Projected Capacitive Touch Panel	/
CTP Driver	ILI2132A	/
Supply Voltage for Module	3.3	V
Supply Voltage for Backlight	5.0 (TYP.)	V
Weight	99	g

**Note 1:** RoHS3 compliant**Note 2:** LCM weight tolerance:  $\pm 5\%$ .



## 1. REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2021-09-03	Initial Release	



## 2. CONTENTS

1. REVISION RECORD.....	2
2. CONTENTS.....	3
3. MODULE CLASSIFICATION INFORMATION.....	4
4. ASSEMBLY GUIDE.....	5
4.1 Mounting frame.....	5
5. MODULE DRAWING.....	6
6. ABSOLUTE MAXIMUM RATINGS.....	7
7. ELECTRICAL CHARACTERISTICS.....	7
8. BACKLIGHT ELECTRICAL CHARACTERISTICS.....	7
9. ELECTRO-OPTICAL CHARACTERISTICS.....	8
10. INTERFACES DESCRIPTION.....	10
10.1 P1 connector - RiBUS description.....	10
10.2 P2 connector description.....	10
11. BT817Q CONTROLLER SPECIFICATION.....	11
11.1 Serial host interface.....	11
11.2 Block diagram.....	12
11.3 Host interface SPI mode 0.....	12
11.4 Backlight driver block diagram.....	13
12. TIMING CHARACTERISTICS.....	14
12.1 RGB mode.....	14
12.2 System bus timing for RGB interface.....	14
12.2.1 SYNC mode timing diagram.....	14
12.2.2 SYNC-DE mode timing diagram.....	15
12.2.3 DE mode timing diagram.....	16
12.3 Parallel 24-bit RGB input timing table.....	17
13. CAPACITIVE TOUCH SCREEN PANEL SPECIFICATIONS.....	17
13.1 Mechanical characteristics.....	17
13.2 Electrical characteristics.....	17
14. MODULE INITIALIZATION.....	18
15. INSPECTION.....	19
15.1 Inspection condition.....	19
15.2 Inspection standard.....	20
16. RELIABILITY TEST.....	21
17. LEGAL INFORMATION.....	22



### 3. MODULE CLASSIFICATION INFORMATION

RV	T	43	H	L	B	F	W	C	A0
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.

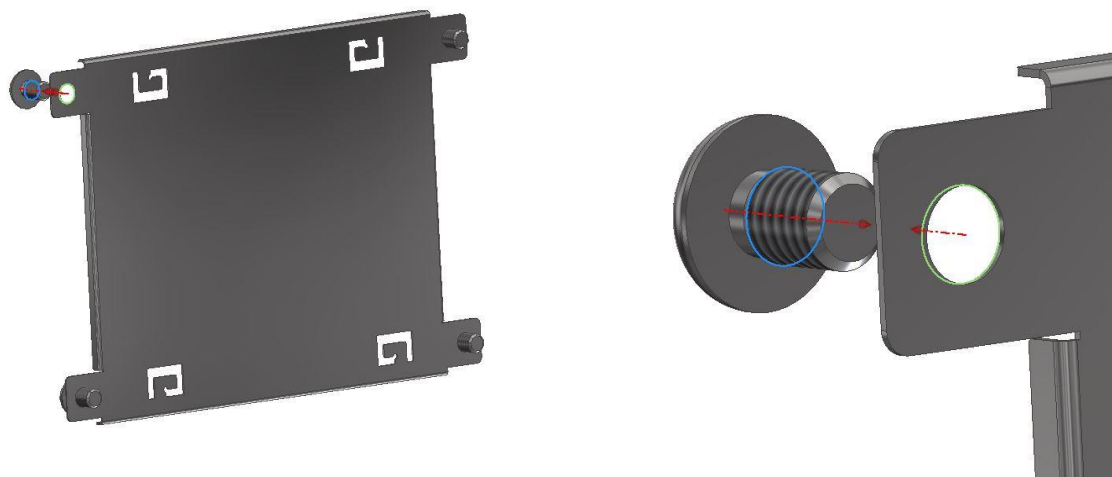
NO.	PARAMETER	SYMBOL
1.	BRAND	RV – Riverdi
2.	PRODUCT TYPE	T – TFT Standard
3.	DISPLAY SIZE	43 – 4.3”
4.	MODEL SERIAL NO.	H – High Brightness, IPS
5.	RESOLUTION	L – 480 x 272 px
6.	INTERFACE	B – SPI/QSPI
7.	FRAME	F – With Mounting Metal Frame
8.	BACKLIGHT TYPE	W – LED White
9.	TOUCH PANEL	C – With Capacitive Touch Panel
10.	VERSION	A0 – aTouch

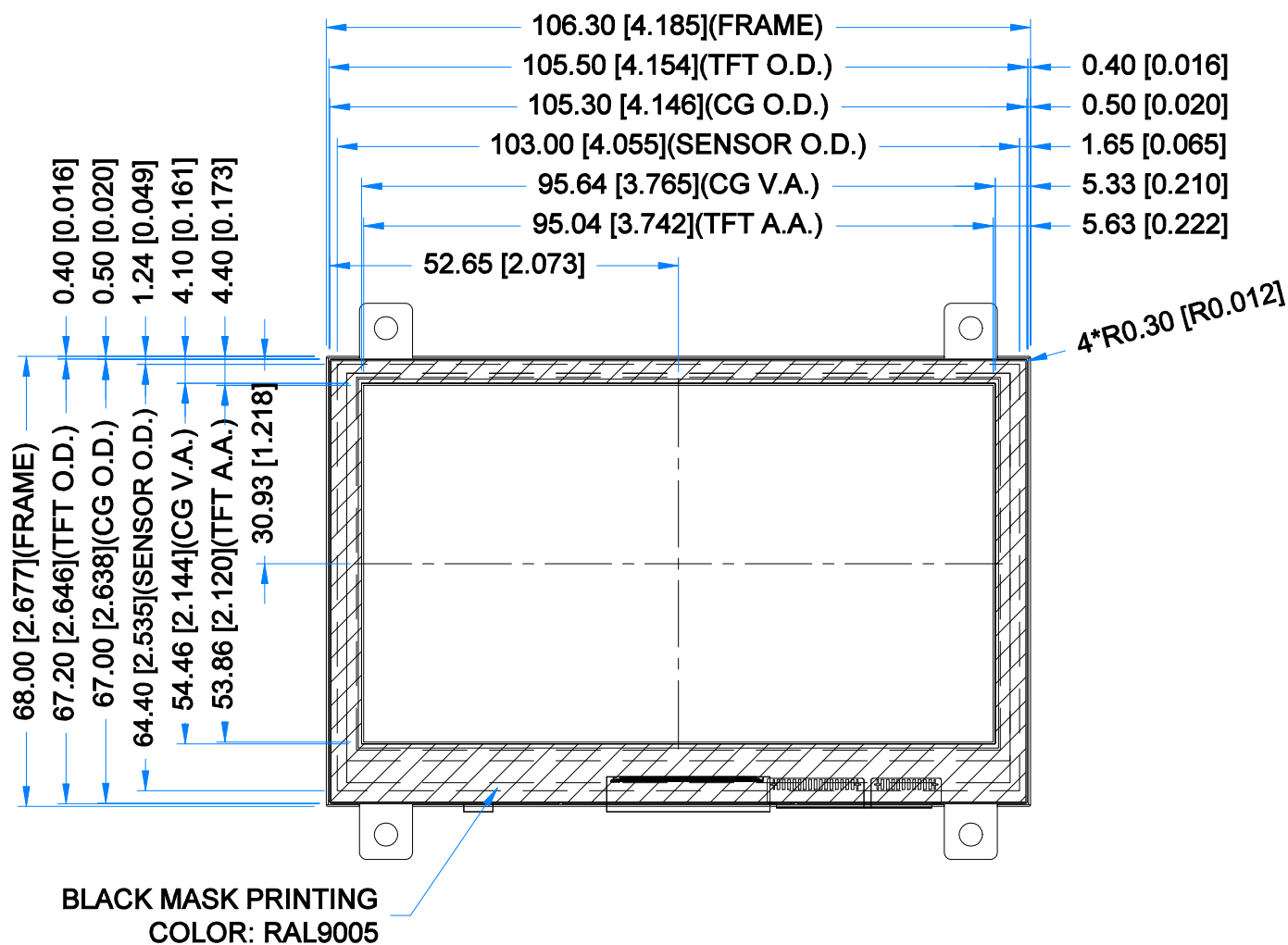
## 4. ASSEMBLY GUIDE

### 4.1 Mounting frame

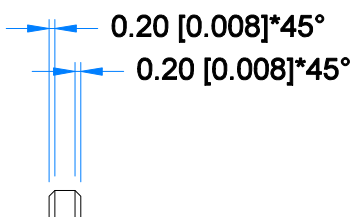
For dimensions 3.5", 4.3", 5.0", 7.0" and 10.1", the product with mounting frame version is available. Thanks to the four catches attached to the side, frame provides strong assembly to the surface by mounting element (like the screw, see Figure 1). The frames are specially designed to fit Riverdi products perfectly. The diameter of the mounting hole is 3.5mm.

Figure 1. Mounting frame

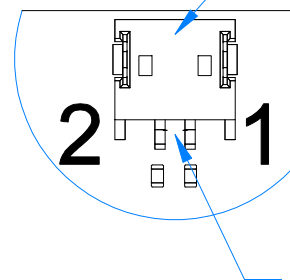




DETAIL A  
SCALE 4:1



DETAIL B  
SCALE 4:1



#### TFT NOTES:

1. LCD TYPE: TRANSMISSIVE, NORMALLY BLACK, IPS
2. RESOLUTION: 480x272
3. VIEWING ANGLE: FREE
4. SURFACE LUMINANCE: 800 cd/m<sup>2</sup>
5. DRIVING IC ON THE BOARD: BT817Q
6. INTERFACE: SPI/QSPI
7. SUPPLY VOLTAGE FOR MODULE: 3.3V
8. SUPPLY VOLTAGE FOR BACKLIGHT: 5.0V(TYP.), BUILT-IN LED INVERTER

#### TP NOTES:

1. TP STRUCTURE: G+G
2. CG THICKNESS: 1.10mm[0.043inch]
3. SURFACE HARDNESS: 7H
4. DRIVER IC: ILI2132A
5. INTERFACE: SPI/QSPI VIA RIBUS AND BT817Q

#### GENERAL NOTES:

1. OPERATING T
2. STORAGE TE
3. WITHOUT IND
4. RoHS3 COMP

## 6. ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT	NOTE
Supply Voltage for Module	VDD	0	3.6	V	NOTE 1
Digital I/O signals (SPI/QSPI/GPIO) Voltage	VIN	-0.5	3.3		NOTE 1, 2
Supply voltage for Backlight	BLVDD	-0.3	5.5		NOTE 1
Operating Temperature	T <sub>OP</sub>	-20	70	°C	
Storage Temperature	T <sub>ST</sub>	-30	80	°C	
Storage Humidity (@ 25 ± 5°C)	H <sub>ST</sub>	10	-	% RH	
Operating Ambient Humidity (@ 25 ± 5°C)	H <sub>OP</sub>	10	-	% RH	

**Note 1.** Exceeding maximum values may cause improper operation or permanent damage to the unit.

**Note 2.** Digital I/O signals are to be connected to pins 3 ÷ 9, 11 and 12 pins at RiBUS connector (PI).

## 7. ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Supply Voltage for Module	VDD	3.0	3.3	3.6	V	
Current drawn from VDD	I <sub>VDD</sub>	TBD	175	TBD	mA	Note 1
Input Voltage "H" Level	V <sub>IH</sub>	2.0	-	3.3	V	
Input Voltage "L" Level	V <sub>IL</sub>	0	-	0.8	V	

**Note 1.** These values will be announced after the samples have been tested.

## 8. BACKLIGHT ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Supply Voltage for Backlight	BLVDD	3.0	5.0	6.0	V	
Current drawn from BLVDD @5.0V	I <sub>BLVDD=5.0V</sub>	TBD	230	TBD	mA	100% of backlight, NOTE 1
Current drawn from BLVDD @7.0V	I <sub>BLVDD=7.0V</sub>	TBD	110	TBD	mA	50% of backlight, NOTE 1
Current drawn from BLVDD @9.0V	I <sub>BLVDD=9.0V</sub>	TBD	385	TBD	mA	100% of backlight, NOTE 1
Current drawn from BLVDD @12.0V	I <sub>BLVDD=12.0V</sub>	TBD	175	TBD	mA	50% of backlight, NOTE 1
Lifetime	-	-	50,000	-	hours	NOTE 2

**Note 1.** Backlight intensity is driven by BT817Q controller by PWM wave from GPIO pin. Please refer to subchapter 11.4.

**Note 2.** Operating life means the period in which the LED brightness goes down to 50% of the initial brightness. Typical operating lifetime is the estimated parameter.

## 9. ELECTRO-OPTICAL CHARACTERISTICS

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 500mm from the LCD surface at a viewing angle of  $\Phi$  and  $\theta$  equal to 0°.

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	RMK	NOTE
Response Time	Tr+Tf	$\theta=0^\circ$ $\phi=0^\circ$ Ta=25 °C	-	30	40	ms	FIG 2.	4
Contrast Ratio	Cr		640	800	-	---	FIG 3.	1
Luminance Uniformity	$\delta$ WHITE		-	75	-	%	FIG 3.	3
Surface Luminance	Lv		-	800	-	cd/m <sup>2</sup>	FIG 3.	2
Viewing Angle Range	$\theta$	$\phi = 90^\circ$	70	80	-	deg	FIG 4.	6
		$\phi = 270^\circ$	70	80	-	deg	FIG 4.	
		$\phi = 0^\circ$	70	80	-	deg	FIG 4.	
		$\phi = 180^\circ$	70	80	-	deg	FIG 4.	
CIE (x, y) Chromaticity	Rx	$\theta=0^\circ$ $\phi=0^\circ$ Ta=25 °C	0.579	0.619	0.659	-	FIG 3.	5
	Ry		0.290	0.330	0.370	-		
	Gx		0.346	0.386	0.426	-		
	Gy		0.539	0.579	0.619	-		
	Bx		0.070	0.110	0.150	-		
	By		0.091	0.131	0.171	-		
	Wx		0.280	0.320	0.360	-		
	Wy		0.305	0.345	0.384	-		

**Note 1.** Contrast Ratio (CR) is defined mathematically as below, for more information see Figure 3.

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

**Note 2.** Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 3.

$$Lv = \text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}$$

**Note 3.** The uniformity in surface luminance  $\delta$  WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the minimum luminance of 5 points luminance by maximum luminance of 5 points luminance. For more information see Figure 3.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

**Note 4.** Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see Figure 2. The test equipment is Autronic-Melchers's ConoScope series.

**Note 5.** CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.



**Note 6.** Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to LCD surface. For more information see Figure 4.

**Note 7.** For viewing angle and response time testing, the testing data is based on Autronic-Melchers's ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, CIE the test data is based on TOPCON's BM-5 photo detector.

Figure 2. The definition of response time

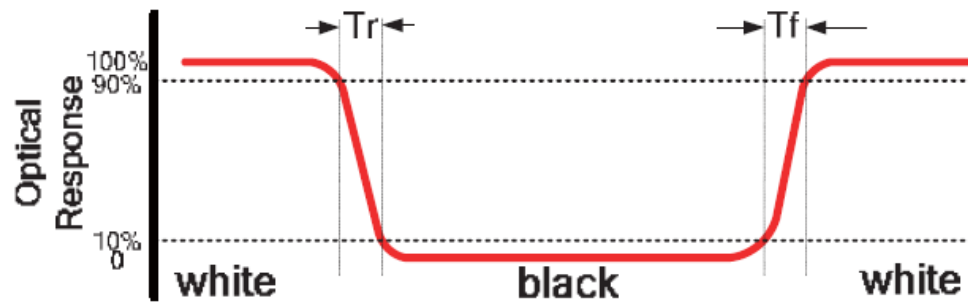
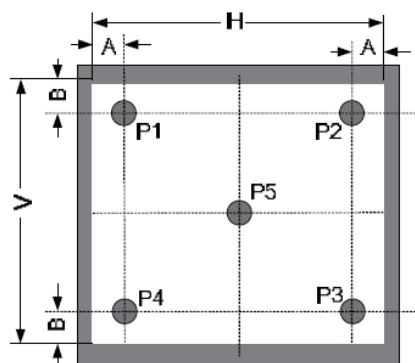


Figure 3. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity



A: 5mm

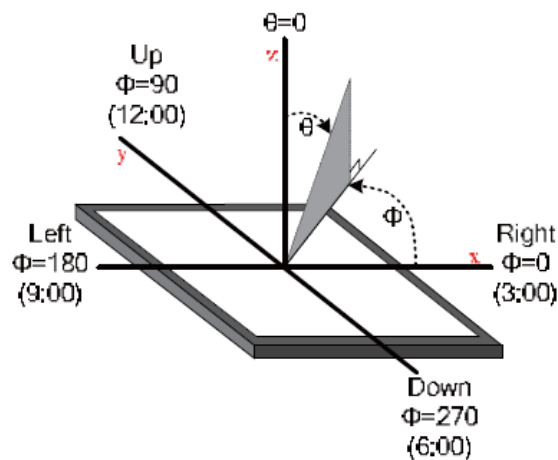
B: 5mm

H, V: Active Area

Light spot size  $\varnothing=5\text{mm}$ , 500mm distance from the LCD surface to detector lens.

Measurement instrument is TOPCON'S luminance meter BM-5

Figure 4. The definition of viewing angle



## 10. INTERFACES DESCRIPTION

### 10.1 P1 connector - RiBUS description

PIN NO.	CONNECTOR	DESCRIPTION	NOTE
1	VDD	Supply voltage for module; TYP 3.3 V	
2	GND	Ground	
3	SPI_SCLK	SPI SCK signal	
4	MISO/ IO.1	SPI MISO signal / SPI Quad mode: SPI data line 1	
5	MOSI/ IO.0	SPI MOSI signal / SPI Quad mode: SPI data line 0	
6	CS	SPI chip select signal	
7	INT	Interrupt signal from device to the system, Active Low, Internally 47k Pull UP	
8	RST/PD	Reset / Power down signal, Active Low, Internally Pulled UP 47k	
9	GPIO.0	GPIO.0	
10	DISP_AUDIO	Display audio in/out	NOTE 1
11	GPIO.1/IO.2	SPI Single/Dual mode: General purpose IO0. QSPI mode: SPI data line 2	
12	GPIO.2/IO.3	SPI Single/Dual mode: General purpose IO1. QSPI mode: SPI data line 3	
13	NC	Not connected	
14	NC	Not connected	
15	NC	Not connected	
16	NC	Not connected	
17	BLVDD	Supply voltage for backlight	
18	BLVDD	Supply voltage for backlight	
19	BLGND	Backlight Ground, Internally connected to GND	
20	BLGND	Backlight Ground, Internally connected to GND	

**Note 1.** Requirements for audio external signal voltage will be announced after samples have been tested.

### 10.2 P2 connector description

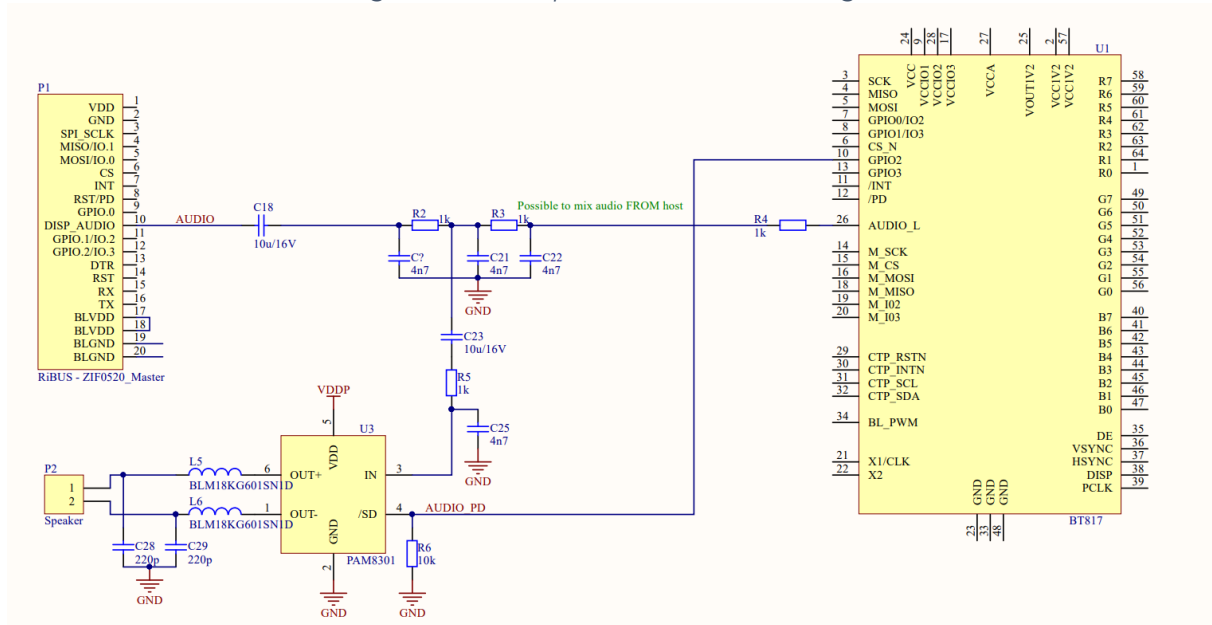
PIN NO.	SYMBOL	DESCRIPTION	NOTE
1	SPEAKER +	Speaker coil "+" terminal	NOTE 1
2	SPEAKER -	Speaker coil "-" terminal	

The audio circuit allows for the following 3 things:

1. To play sounds from BT817Q on internal amplifier U3.
2. To play sounds from host on internal amplifier U3.
3. To play sounds from BT817Q on external amplifier.

**Note 1.** The loudspeaker assembly (loudspeaker + cables + plug compatible with P2 connector) will be sold separately. The documentation of the loudspeaker assembly will be released soon.

Figure 5. The simplified audio circuit design



**Note 2.** By standard, the EVE4 board is equipped with the separate 256 Mb flash memory chip, which allows to show up to 270 full resolution (480 \* 272 pixels, JPG) images. If you need to change the memory size, please contact us: [contact@riverdi.com](mailto:contact@riverdi.com)

## 11. BT817Q CONTROLLER SPECIFICATION

BT817Q or EVE4 (Embedded Video Engine 4) simplifies the system architecture for advanced human machine interfaces (HMIs) by providing functionality for display, audio, and touch as well as an object-oriented architecture approach that extends from display creation to the rendering of the graphics.

### 11.1 Serial host interface

Figure 6. SPI single/dual interface connection

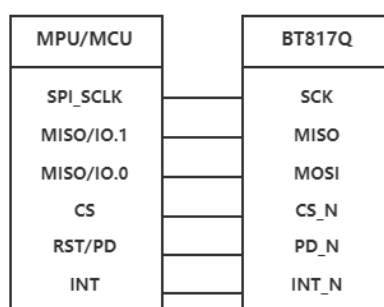
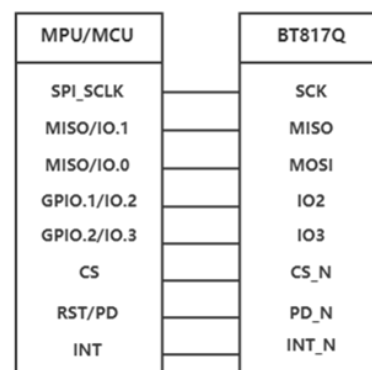


Figure 7. QSPI interface connection



**SPI Interface** – the SPI slave interface operates up to 30MHz (It depends on EVE4 system clock frequency and needs verification in Riverdi lab).

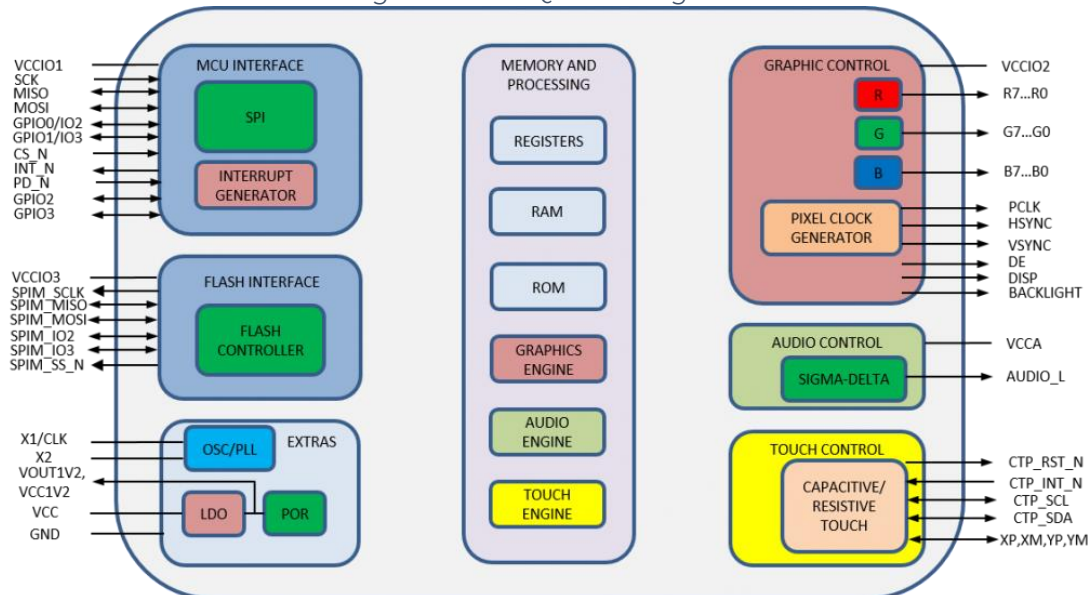
Only SPI mode 0 is supported. The SPI interface is selected by default.

**QSPI Interface** – the QSPI slave interface operates up to 30MHz (It depends on EVE 4 system clock frequency and will be verified in Riverdi lab). Only SPI mode 0 is supported. The QSPI can be configured as a SPI slave in SINGLE, DUAL or QUAD channel modes.

By default, the SPI slave operates in the SINGLE channel mode with MOSI as input from the master and MISO as output to the master. DUAL and QUAD channel modes can be configured through the SPI slave itself. To change the channel modes, write to register REG\_SPI\_WIDTH.

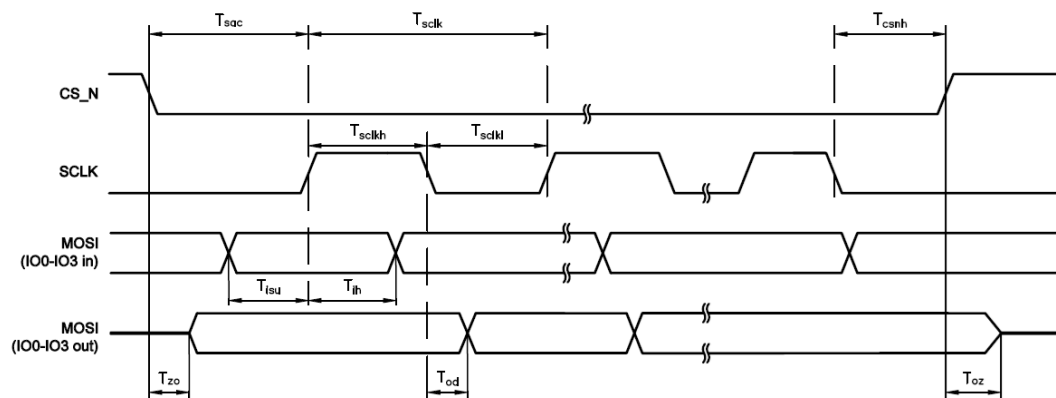
## 11.2 Block diagram

Figure 8. BT817Q Block diagram



## 11.3 Host interface SPI mode 0

Figure 9. SPI timing diagram



The meanings of the timings in the Figure 9 are defined in the table below.

PARAMETER	DESCRIPTION	VCCIO=1.8V		VCCIO=2.5V		VCCIO=3.3V		UNIT
		Min	Max	Min	Max	Min	Max	
$T_{sclk}$	SPI clock period	33.3	-	33.3	-	33.3	-	ns
$T_{sckl}$	SPI clock low duration	13	-	13	-	13	-	
$T_{sckh}$	SPI clock high duration	13	-	13	-	13	-	
$T_{sac}$	SPI access time	4	-	3.5	-	3	-	
$T_{isu}$	Input Setup	4	-	3.5	-	3	-	
$T_{ih}$	Input Hold	0	-	0	-	0	-	
$T_{zo}$	Output enable delay	-	16	-	13	11	-	
$T_{oz}$	Output disable delay	-	13	-	11	10	-	
$T_{od}$	Output data delay	-	15	-	12	11	-	
$T_{csnh}$	CSN hold time	0	-	0	-	0	-	

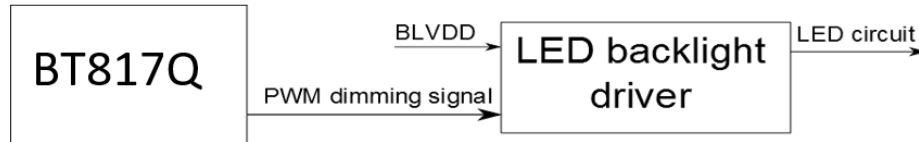
For more information about BT817Q controller please go to official BT81x website.

<https://brtchip.com/bt81x/>

#### 11.4 Backlight driver block diagram

Backlight enable signal is internally connected to BT817Q backlight control pin. This pin is controlled by two BT817Q's registers. One of them specifies the PWM output frequency, second one specifies the duty cycle. Refer to BT817Q datasheet for more information. After we have done the test on samples, more detailed description will be given in this document.

Figure 10. Backlight driver block diagram



The LED backlight driver used in this module does not burst the LED current. Therefore, it does not generate audible noises on the output capacitor. It is equipped with soft start subsystem, which increases LED lifetime, as LED current peaks are reduced significantly.

## 12. TIMING CHARACTERISTICS

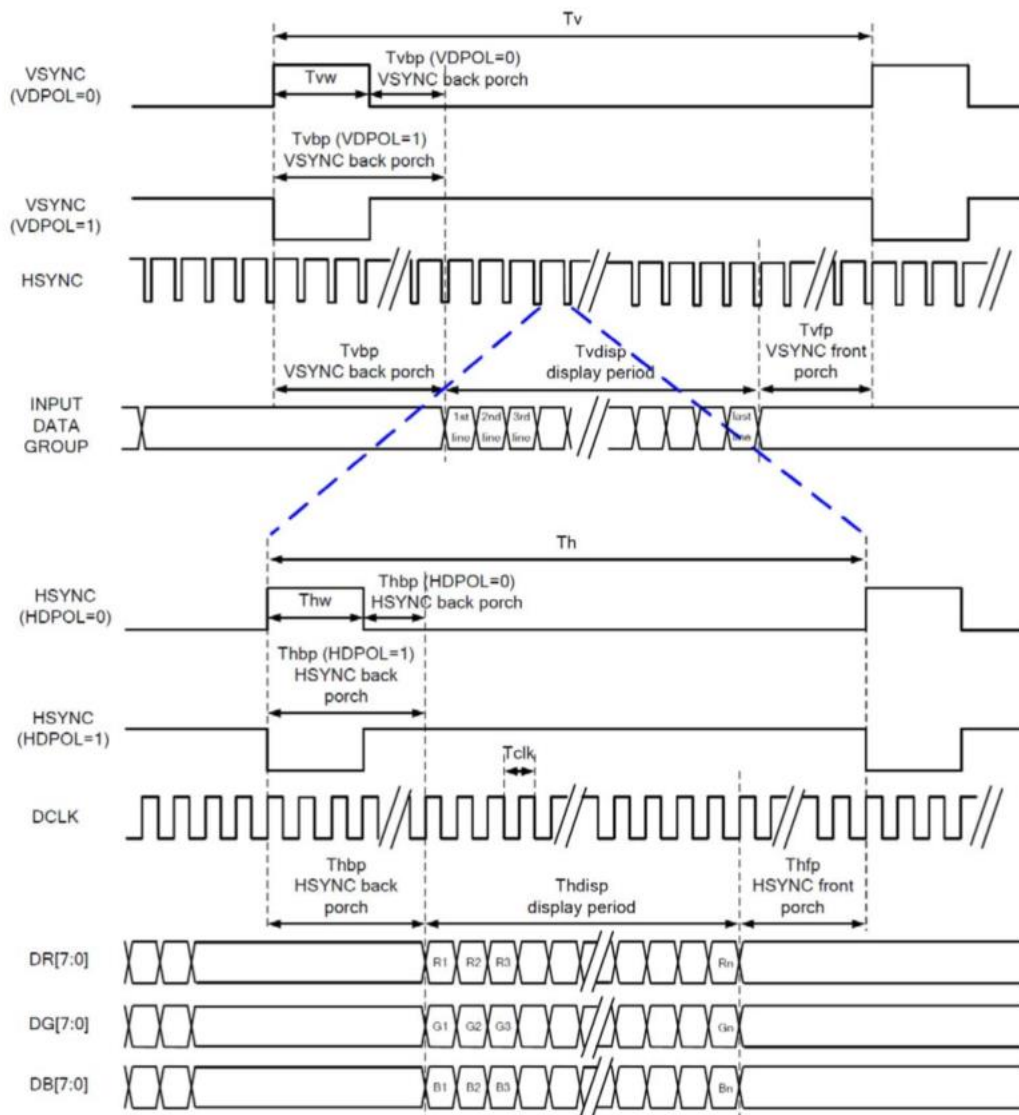
### 12.1 RGB mode

RGB MODE	DCLK	HSYNC	VSYNC	DE
SYNC-DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

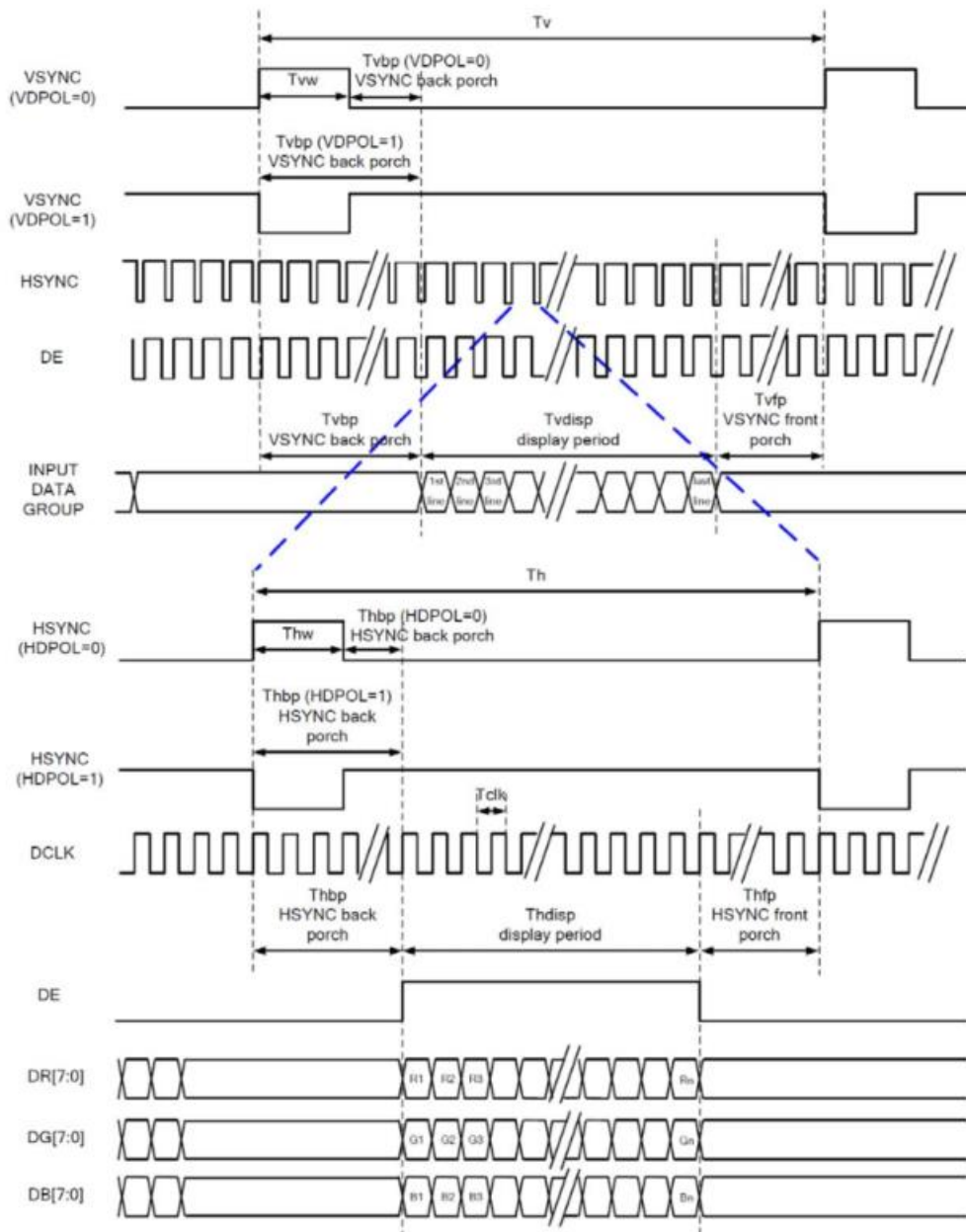
**Note.** "Input" means these signals are driven by host side.

### 12.2 System bus timing for RGB interface

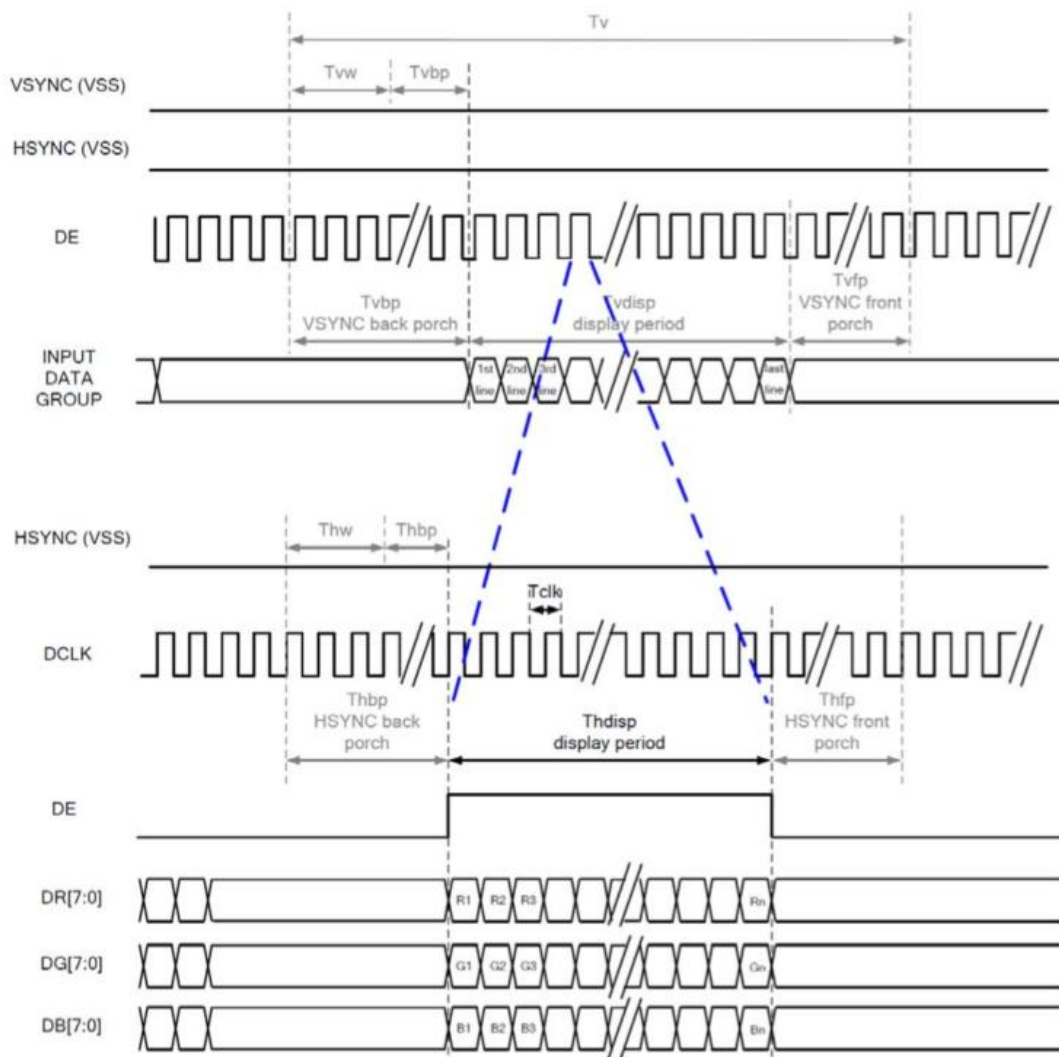
#### 12.2.1 SYNC mode timing diagram



## 12.2.2 SYNC-DE mode timing diagram



### 12.2.3 DE mode timing diagram





### 12.3 Parallel 24-bit RGB input timing table

Parallel 24-bit RGB input Timing (PVDD=VDD=VDDI=3.3V, AGND=0V, Ta=25 °C)

PARAMETER		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
DCLK Frequency		Fclk	8	9	12	MHz	
DCLK Period		Tclk	83	111	125	ns	
HSYNC	Period Time	Th	485	531	598	DCLK	
	Display Period	Thdisp	480				
	Back Porch	Thbp	3	43	43		By H_BLANKING setting
	Front Porch	Thfp	2	8	75		
	Pulse Width	Thw	2	4	43		
VSYNC	Period Time	Tv	276	292	321	HSYNC	
	Display Period	Tvdisp	272				
	Back Porch	Tvbp	2	12	12		By V_BLANKING setting
	Front Porch	Tvfp	2	8	37		
	Pulse Width	Tvw	2	4	12		

**Note.** It's necessary to keep Tvbp=12 and Thbp=43 in sync mode. It is unnecessary to keep these values in DE mode.

## 13. CAPACITIVE TOUCH SCREEN PANEL SPECIFICATIONS

### 13.1 Mechanical characteristics

DESCRIPTION	SPECIFICATION	REMARK
Touch Panel Size	4.3 inch	aTouch
Outline Dimension of CTP	105.30 mm x 67.00 mm	
Product Thickness	2.15 mm	
Glass Thickness	1.1 mm	
CTP View Area	95.64 mm x 54.46 mm	
Sensor Active Area	98.00 mm x 56.50 mm	
Structure type	Glass + Glass	
Surface Hardness	7H	

### 13.2 Electrical characteristics

DESCRIPTION		SPECIFICATION	NOTE
Power	Active Mode	91 mA	Note 1
Consumption (IDD)	Sleep Mode	10 mA	Note 1
Linearity		+/-1.5mm	
Controller		ILI2132A	
Resolution		480 x 272	

**Note 1.** These 2 values will be verified on the real samples.



## 14. MODULE INITIALIZATION

1. There is no need to set touch calibration matrix (REG\_TOUCH\_TRANSFORM\_A-F registers in BT817Q) as touch panel resolution and orientation are the same as display, so default values in BT817Q are correct,
2. Initialization data, timings and example codes are available on the Riverdi GitHub, at address: <https://github.com/riverdi/riverdi-eve>
3. REGISTER VALUES:

REG_HSIZE	480
REG_VSIZE	272
REG_HCYCLE	531
REG_HOFFSET	43
REG_HSYNC0	0
REG_HSYNC1	4
REG_VCYCLE	292
REG_VOFFSET	12
REG_VSYNC0	0
REG_VSYNC1	4
REG_PCLK	1
REG_SWIZZLE	0
REG_PCLK_POL	1
REG_CSPREAD	0
REG_DITHER	0
REG_PCLK_FREQ	0x32
REG_PCLK_2X	0

## 15. INSPECTION

Standard acceptance/rejection criteria for TFT module

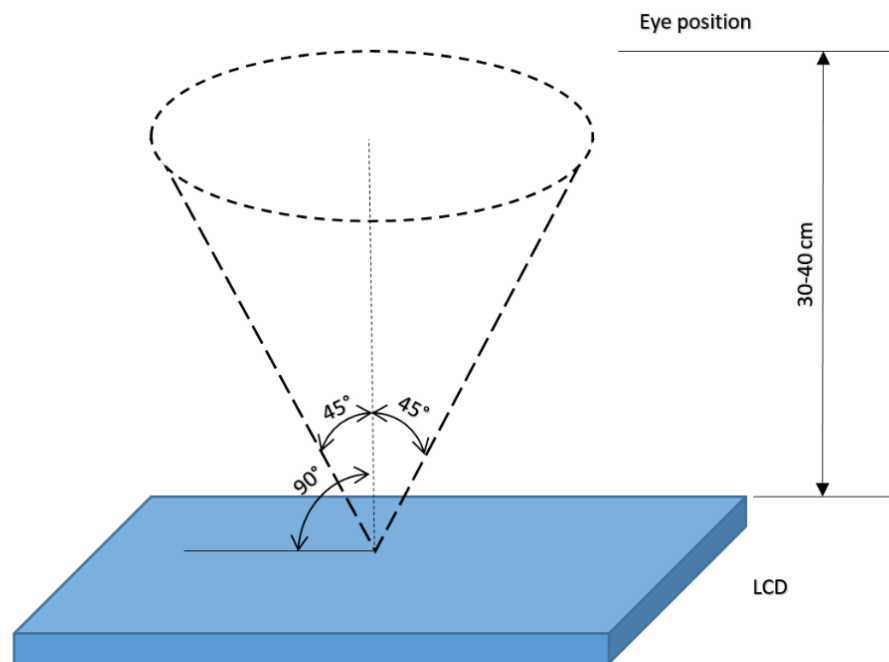
### 15.1 Inspection condition

Ambient conditions:

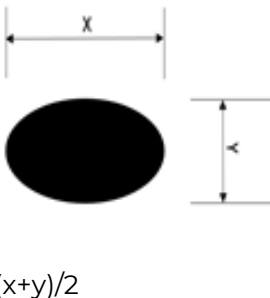
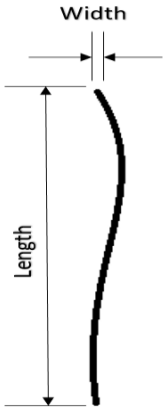
- Temperature:  $25 \pm 2^{\circ}\text{C}$
- Humidity:  $(60 \pm 10) \% \text{RH}$
- Illumination: Single fluorescent lamp non-directive (300 to 700 lux)

Viewing distance:  $35 \pm 5\text{cm}$  between inspector bare eye and LCD.

Viewing Angle: U/D:  $45^{\circ}/45^{\circ}$ , L/R:  $45^{\circ}/45^{\circ}$



## 15.2 Inspection standard

ITEM		CRITERION		
Black spots, white spots, light leakage, Foreign Particle (round Type)	 D=(x+y)/2 Spots density: 10 mm	3.5" ≤ Size ≤ 5"		
		Average Diameter		Qualified Qty
		D ≤ 0.15 mm		Ignored
		0.15 mm < D ≤ 0.3 mm		N≤3
		0.3mm < D		Not allowed
LCD black spots, white spots, light leakage (line Type)	 Spots density: 10 mm	3.5" ≤ Size ≤ 5"		
		Length	Width	Qualified Qty
		-	W ≤ 0.03	Ignored
		L ≤ 3.0	0.03 < W ≤ 0.05	2
		L ≤ 3.0	0.05 < W ≤ 0.1	1
		3.0 < L	0.1 < W	Not allowed
Bright/Dark Dots	3.5" ≤ Size ≤ 5"			
	Item		Qualified Qty	
	Bright dots		N ≤ 1	
	Dark dots		N ≤ 2	
	Total Bright and Dark Dots		N ≤ 3	
Clear spots	Size < 5"			
	Average Diameter		Qualified Qty	
	D < 0.2 mm		Ignored	
	0.2 mm < D < 0.3 mm		3	
	0.3 mm < D < 0.5 mm		2	
	0.5 mm < D		0	
	Spots density: 10 mm			
Polarizer bubbles	3.5" ≤ Size ≤ 5"			
	Average Diameter		Qualified Qty	
	D ≤ 0.2 mm		Ignored	
	0.2 mm < D ≤ 0.3 mm		2	
	0.3 mm < D ≤ 0.5mm		1	
	0.5 mm < D		0	
Total Q'ty		3		
Touch panel spots	Size < 5.0"			
	Average Diameter		Qualified Qty	

	D < 0.2 mm		Ignored
	0.2 mm < D < 0.4 mm		5
	0.2 mm < D < 0.5 mm		2
	0.5 mm < D		0
Touch panel white line scratch	Size < 5.0"		
	Length	Width	Qualified Qty
	-	W < 0.02	Ignored
	L < 3.0	0.02 < W < 0.05	2
	L < 2.5	0.05 < W < 0.08	2
	-	0.08 < W	0

## 16. RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION	NOTE
1	High Temperature Storage	80°C/120 hours	Note 1
2	Low Temperature Storage	-30°C/120 hours	
3	High Temperature Operating	70 °C /120 hours	
4	Low Temperature Operating	-20°C/120 hours	
5	High Temperature and High Humidity	Humidity 40°C, 90%RH, 120Hrs	
6	Thermal Cycling Test (No operation)	-20°C for 30min, 70°C for 30 min. 100 cycles. Then test at room temperature after 1 hour	Note 2
7	Vibration Test	Frequency: 10 ÷ 55 Hz. Stroke: 1.5 mm. Sweep: 10Hz ÷ 55Hz ÷ 10 Hz. 2 hours for each direction of X, Y, Z (Total 6 hours)	
8	Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces	

**Note 1.** Sample quantity for each test item is 5 ÷ 10 pcs.

**Note 2.** Before running cosmetic and function tests, the product must have enough recovery time, at least 2 hours at room temperature.



## 17.LEGAL INFORMATION

Riverdi grants the guarantee for the proper operation of the goods for a period of 12 months from the date of possession of the goods. If in a consequence of this guaranteed execution the customer has received the defects-free item as replacement for the defective item, the effectiveness period of this guarantee shall start anew from the moment the customer receives the defects-free item.

Information about device is the property of Riverdi and may be the subject of patents pending or granted. It is not allowed to copy or disclosed this document without prior written permission.

Riverdi endeavors to ensure that all contained information in this document is correct but does not accept liability for any error or omission. Riverdi products are in developing process and published information may be not up to date. Riverdi reserves the right to update and makes changes to Specifications or written material without prior notice at any time. It is important to check the current position with Riverdi.

Images and graphics used in this document are only for illustrative the purpose. All images and graphics are possible to be displayed on the range products of Riverdi, however the quality may vary. Riverdi is no liable to the buyer or to any third party for any indirect, incidental, special, consequential, punitive or exemplary damages (including without limitation lost profits, lost savings, or loss of business opportunity) relating to any product, service provided or to be provided by Riverdi, or the use or inability to use the same, even if Riverdi has been advised of the possibility of such damages.

Riverdi products are not fault tolerant nor designed, manufactured or intended for use or resale as on line control equipment in hazardous environments requiring fail-safe performance, such as in the operation of nuclear facilities, aircraft navigation or communication systems, air traffic control, direct life support machines or weapons systems in which the failure of the product could lead directly to death, personal injury or severe physical or environmental damage ('High-Risk Activities'). Riverdi and its suppliers specifically disclaim any expressed or implied warranty of fitness for High-Risk Activities. Using Riverdi products and devices in 'High-Risk Activities' and in any other application is entirely at the buyer's risk, and the buyer agrees to defend, indemnify and hold harmless Riverdi from all damages, claims or expenses resulting from such use. No licenses are conveyed, implicitly or otherwise, under any Riverdi intellectual property rights.

Hi, I am here to help you!  
If you have any additional  
questions, please contact  
our support via email:  
[contact@riverdi.com](mailto:contact@riverdi.com)



## X-ON Electronics

Largest Supplier of Electrical and Electronic Components

*Click to view similar products for [Display Modules](#) category:*

*Click to view products by [Riverdi](#) manufacturer:*

Other Similar products are found below :

[P6153-PR](#) [TDP0700T800480PCAP](#) [P6171DPR-DC-RS](#) [P6171DPR-DC-U](#) [MIKROMEDIA 3 FOR STM32F4 CAPACITIVE FPI](#)  
[TN0216ANVNANN-GN00](#) [TN0104ANVAANN-GN00](#) [TN0181ANVNANN-GN00](#) [SM-RVT101HVBFWCA0](#) [SM-RVT101HVBWNCA0](#)  
[SM-RVT35HHBFWCA0](#) [SM-RVT35HHBNWCA0](#) [SM-RVT43HLBFWCA0](#) [SM-RVT43HLBNWCA0](#) [SM-RVT50HQBFWCA0](#) [SM-](#)  
[RVT50HQBNWCA0](#) [SM-RVT70HSBFWCA0](#) [SM-RVT70HSBNWCA0](#) [PIM579](#) [1215686](#) [PIM543](#) [DFR0678](#) [P6191PR-DC-U-V3](#) [16381](#)  
[17916](#) [18205](#) [21229](#) [12885](#) [FPM-215-R9AE](#) [11769](#) [MIKROMEDIA FOR DSPIC33](#) [MIKROMEDIA FOR PIC24](#) [MIKROMEDIA FOR](#)  
[PIC32](#) [MIKROMEDIA FOR STELLARIS M3](#) [MIKROMEDIA HMI 3.5 RES](#) [MIKROMEDIA HMI 5](#) [MIKROMEDIA PLUS FOR FT90X](#)  
[MIKROMEDIA PLUS FOR PIC32MX7 SHIELD](#) [MIKROMEDIA PROTO SHIELD](#) [14628](#) [MIKROMEDIA 5 FOR TIVA SHIELD](#)  
[MIKROMEDIA 7 FOR STM32F4](#) [MIKROMEDIA CONNECT SHIELD](#) [MIKROMEDIA FOR ARM](#) [MIKROMEDIA FOR PSOC5LP](#)  
[MIKROMEDIA GAMING SHIELD](#) [MIKROMEDIA HMI 4.3 UXB](#) [MIKROMEDIA HMI 5 RES](#) [MIKROMEDIA HMI 5 UXB](#)  
[MIKROMEDIA HMI 7](#)