



## RVT7.0A800480TFWC00

### LCD TFT Datasheet

Rev.1.5

2015-06-12

ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally white	/
Size	7.0	Inch
Viewing Direction	12:00 (without image inversion)	O' Clock
Gray Scale Inversion Direction	6:00	O' Clock
LCM (W × H × D )	165.60 × 100.60 × 7.69	mm3
Active Area (W × H)	154.08 × 85.92	mm2
Dot Pitch (W × H)	0.1926 × 0.179	mm2
Number Of Dots	800 (RGB) × 480	/
Driver IC	HX8264+HX8664	/
Backlight Type	21 LEDs	/
Surface Luminance	350	cd/m2
Interface Type	24bit RGB	/
Color Depth	16.7M	/
Pixel Arrangement	RGB Vertical Stripe	/
Surface Treatment	Clear	
Input Voltage	3.3	V
With/Without TSP	Projected Capacitive Touch Panel	/
Weight	246	g

**Note 1:** RoHS compliant

**Note 2:** LCM weight tolerance: ± 5%.

## REVISION RECORD

REVNO.	REVDATE	CONTENTS	REMARKS
1.0	2014-10-16	Initial Release	
1.1	2014-10-23	Add CTP pinout information	
1.2	2014-10-29	Add additional information on mechanical drawing	
1.3	2015-01-19	Update LED lifetime	
1.4	2015-04-14	Update mechanical drawing	
1.5	2015-06-12	Update Surface Treatment information	

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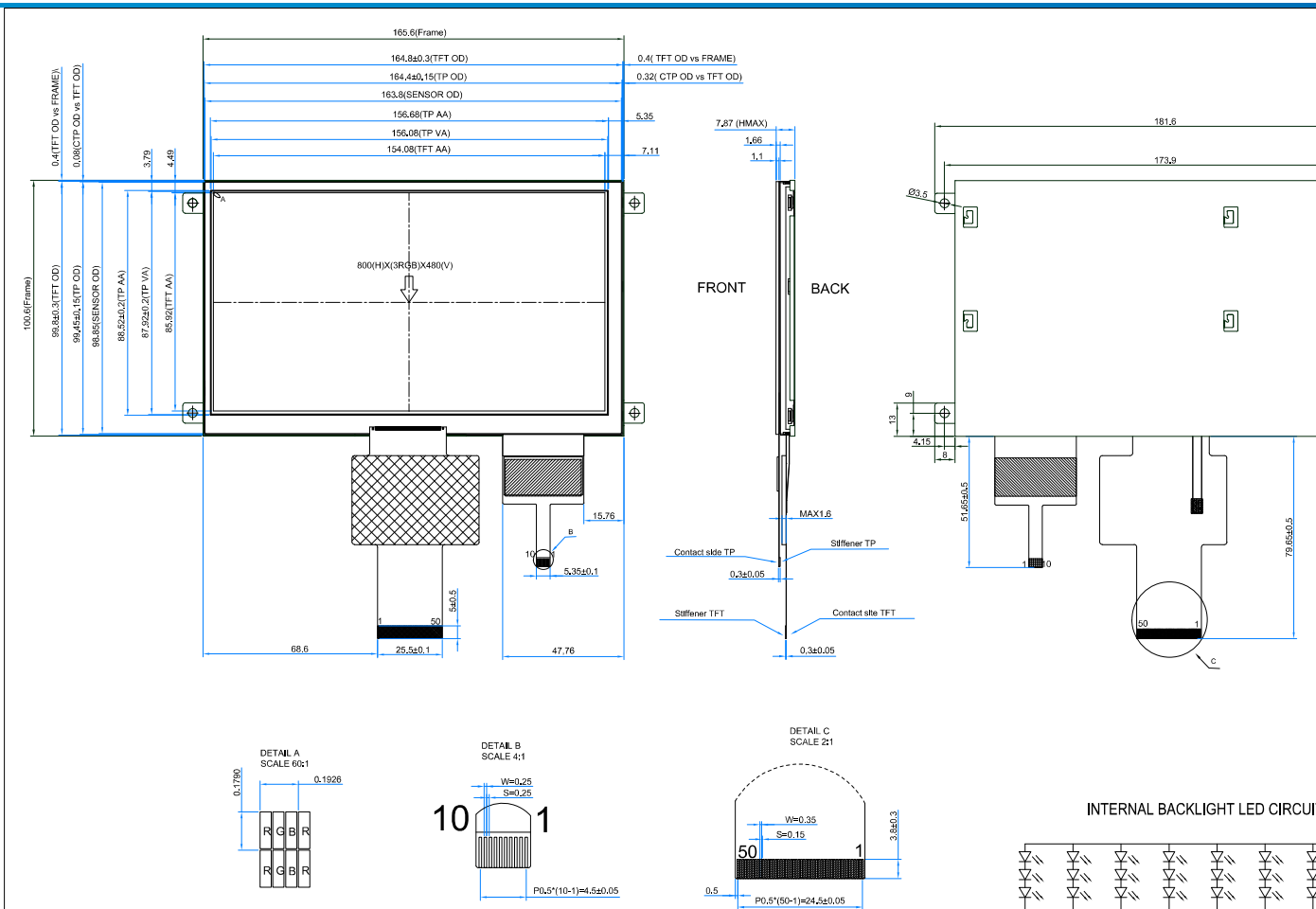
## 1 MODULE CLASSIFICATION INFORMATION

<b>RV</b>	<b>T</b>	<b>7.0</b>	<b>A</b>	<b>800480</b>	<b>T</b>	<b>F</b>	<b>W</b>	<b>C</b>	<b>00</b>
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.

1.	<b>BRAND</b>	<b>RV – Riverdi</b>
2.	<b>PRODUCT TYPE</b>	<b>T – TFT Standard</b> F – TFT Custom
3.	<b>DISPLAY SIZE</b>	<b>3.5 – 3.5”</b> <b>4.3 – 4.3”</b> <b>5.7 – 5.7”</b> <b>7.0 – 7.0”</b>
4.	<b>MODEL SERIAL NO.</b>	<b>A (A-Z)</b>
5.	<b>RESOLUTION</b>	<b>320240 – 320x240 px</b> <b>480272 – 480x272 px</b> <b>800480 – 800x480 px</b>
6.	<b>INTERFACE</b>	<b>T – TFT LCD, RGB</b> L – TFT LCD, LVDS C – TFT + Controller
7.	<b>FRAME</b>	<b>N – No Frame</b> <b>F – Mounting Frame</b>
8.	<b>BACKLIGHT TYPE</b>	<b>W – LED White</b>
9.	<b>TOUCH PANEL</b>	<b>N – No Touch Panel</b> <b>R – Resistive Touch Panel</b> <b>C – Capacitive Touch Panel</b>
10.	<b>VERSION</b>	<b>00(00-99)</b>


# LCD TFT Datasheet Rev.1.5

## RVT7.0A800480TFWC00



**NOTES:**

1. DISPLAY TYPE: TFT, TRANSMISSIVE, POSITIVE
2. OPERATING VOLTAGE: VDD=3.3V
3. VIEWING DIRECTION: 12 O'CLOCK.
4. IC DRIVER: HX8264+HX8664
5. OPERATING TEMP: -20°C ~ 70°C.
6. STORAGE TEMP: -30°C ~ 80°C.
7. LED BACKLIGHT: 21-CHIP WHITE LED, If=180mA, Vf=9.6V.
8. LCM SURFACE LUMINANCE: 350cd/m<sup>2</sup>.
9. GENERAL TOLERANCE: ±0.2.
10. RoHS COMPLIANT.

CUSTOMER	
DRAWN	
DFTG CHK	
ENGR CHK	
APPROVAL	
	

### 3 ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT
Supply Voltage For Logic	VDD	-0.3	5.0	V
LED reverse voltage (each LED)	VR	-	1.2	V
LED forward voltage (each LED)	IF	-	30	mA
Operating Temperature	T <sub>OP</sub>	-20	70	°C
Storage Temperature	T <sub>ST</sub>	-30	80	°C

### 4 ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Power voltage	VDD	3.0	3.3	3.6	V
Input Current	IVDD	-	71.4	-	mA
Input Voltage 'H' level	V <sub>IH</sub>	0.7VDD	-	VDD	V
Input Voltage 'L' level	V <sub>IL</sub>	0	-	0.3VDD	V

### 5 BACKLIGHT CHARACTERISTICS

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
Voltage for LED backlight	V <sub>I</sub>	9.0	9.6	10.2	V
Current for LED backlight	I <sub>I</sub>	170	180	200	mA
LED Life Time	-	30000	50000	-	Hrs

**Note:** The LED life time is defined as the module brightness decrease to 50% original brightness at Ta=25°C.

### 6 ELECTRO-OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	REMARK	NOTE
Response Time	Tr+Tf	θ=0° φ=0° Ta=25	-	20	35	ms	Figure 1	4
Contrast Ratio	Cr		400	500	-	---	Figure 2	1
Luminance Uniformity	δ WHITE		70	75	-	%	Figure 2	3
Surface Luminance	Lv		-	350	-	cd/m <sup>2</sup>	Figure 2	2
Viewing Angle Range	θ	φ = 90°	40	50	-	deg	Figure 3	6
		φ = 270°	60	70	-	deg	Figure 3	
		φ = 0°	60	70	-	deg	Figure 3	
		φ = 180°	60	70	-	deg	Figure 3	
CIE (x, y) Chromaticity	Red	θ=0° φ=0° Ta=25	x	-	-	-	Figure 2	5
			y	-	-	-		
	Green		x	-	-	-		
			y	-	-	-		
	Blue		x	-	-	-		
			y	-	-	-		
	White		x	-	0.280	-		
			y	-	0.310	-		

**Note 1.** Contrast Ratio(CR) is defined mathematically as below, for more information see Figure 1.

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

**Note 2.** Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 2.

$L_v$  = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

**Note 3.** The uniformity in surface luminance  $\delta$  WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see Figure 2.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

**Note 4.** Response time is the time required for the display to transition from white to black (Rise Time,  $T_r$ ) and from black to white (Decay Time,  $T_f$ ). For additional information see FIG 1. The test equipment is Autronic-Melchers's ConoScope series.

**Note 5.** CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

**Note 6.** Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see Figure 3.

**Note 7.** For viewing angle and response time testing, the testing data is based on Autronic-Melchers's ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, CIE the test data is based on TOPCON's BM-5 photo detector.

Figure 1. The definition of response time

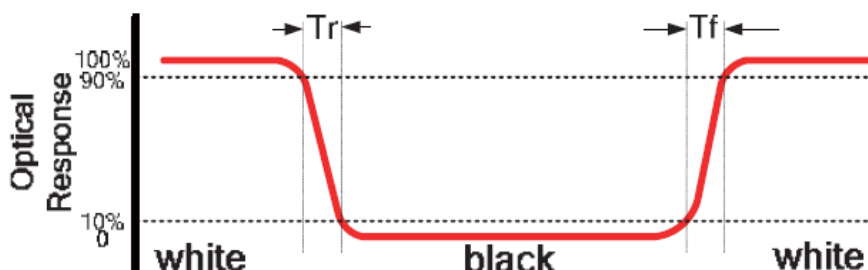


Figure 2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

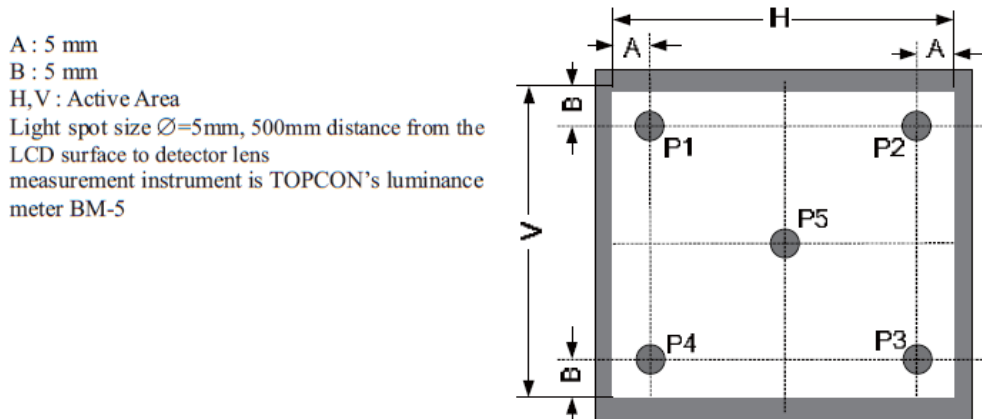
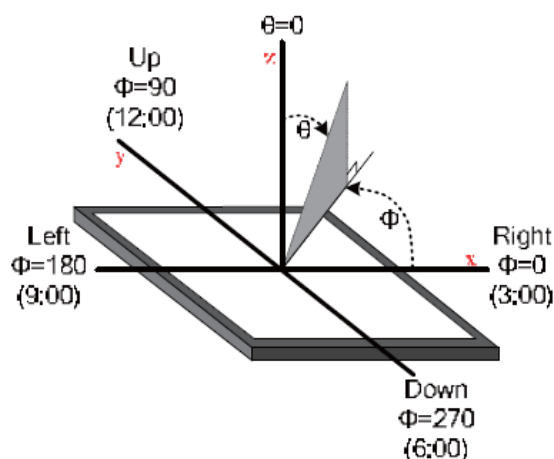


Figure 3. The definition of viewing angle



## 7 INTERFACE DESCRIPTION

PIN NO.	SYMBOL	DESCRIPTION	REMARK
1	VLED+	Anode Of LED Backlight	
2	VLED+	Anode Of LED Backlight	
3	VLED-	Cathode Of LED Backlight	
4	VLED-	Cathode Of LED Backlight	
5	GND	Power Ground	
6	VDD	Power For Circuit	
7	VDD	Power For Circuit	
8	MODE	DE/SYNC Mode Select	Note 1
9	DE	Data Input Enable	
10	VSNC	Vertical Sync Signal	
11	HSNC	Horizontal Sync Signal	
12	B7	Blue Data (MSB)	
13	B6	Blue Data	
14	B5	Blue Data	
15	B4	Blue Data	
16	B3	Blue Data	
17	B2	Blue Data	
18	B1	Blue Data	Note 2
19	B0	Blue Data (LSB)	Note 2

20	G7	Green Data (MSB)	
21	G6	Green Data	
22	G5	Green Data	
23	G4	Green Data	
24	G3	Green Data	
25	G2	Green Data	
26	G1	Green Data	Note 2
27	G0	Green Data (LSB)	Note 2
28	R7	Red Data (MSB)	
29	R6	Red Data	
30	R5	Red Data	
31	R4	Red Data	
32	R3	Red Data	
33	R2	Red Data	
34	R1	Red Data	Note 2
35	R0	Red Data (LSB)	Note 2
36	GND	Power Ground	
37	DCLK	Clock For Input Data	Note 3
38	GND	Power Ground	
39	LR	Left / Right Selection	Note 4,5,8
40	UD	Up / Down Selection	Note 4,5,9
41	VDD	Power For Circuit	
42	VDD	Power For Circuit	
43	NC	No Connection	
44	RESET	Global Reset Pin	Note 6
45	NC	No Connection	
46	NC	No Connection	
47	DITHB	Dithering Function	Note 7
48	GND	Power Ground	
49	NC	No Connection	
50	NC	No Connection	

**Note 1:** DE/SYNC mode select. Normally (internally) pulled high.  
When select DE mode, MODE="1", VS and HS must pull high.  
When select SYNC mode, MODE="0", DE must be grounded.

**Note 2:** When input 18bit RGB data, the two low bits of R, G and B data must be grounded.

**Note 3:** Data shall be latched at the falling edge of DCLK.

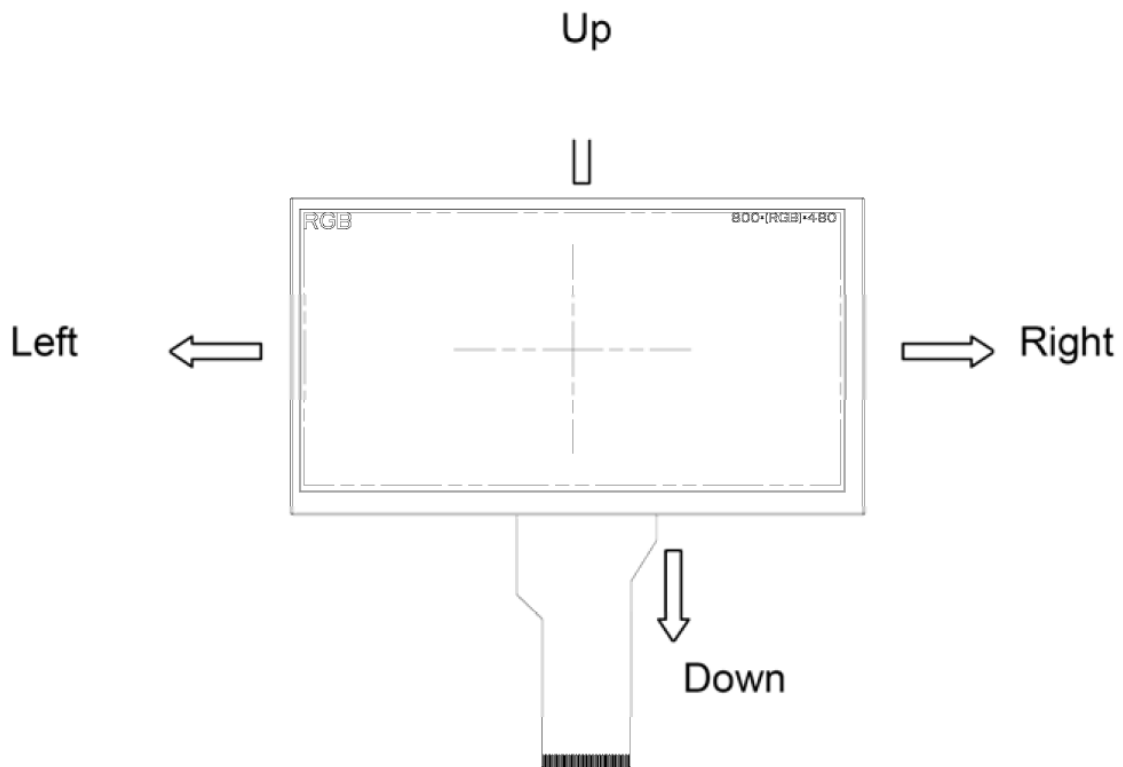
**Note 4:** Selection of scanning mode.

SET OF SCAN CONTROL INPUT		SCANNING DIRECTION
UD	LR	
GND	VCC	Up To Down, Left To Right
VCC	GND	Down To Up, Right To Left
GND	GND	Up To Down, Right To Left
VCC	VCC	Down To Up, Left To Right



**Note 5:** Definition of scanning direction. Refer to the Figure 4.

Figure 4. Definition of scanning direction



**Note 6:** Global reset pin. Active low to enter reset state. Suggest to connect with an RC reset circuit for stability. Normally (internally) pulled high.

**Note 7:** Dithering function enable control, normally (internally) pull high.

When DITHB="1", Disable internal dithering function,

When DITHB="0", Enable internal dithering function,

**Note 8:** Normally (internally) pull high.

**Note 9:** Normally(internally) pull low.

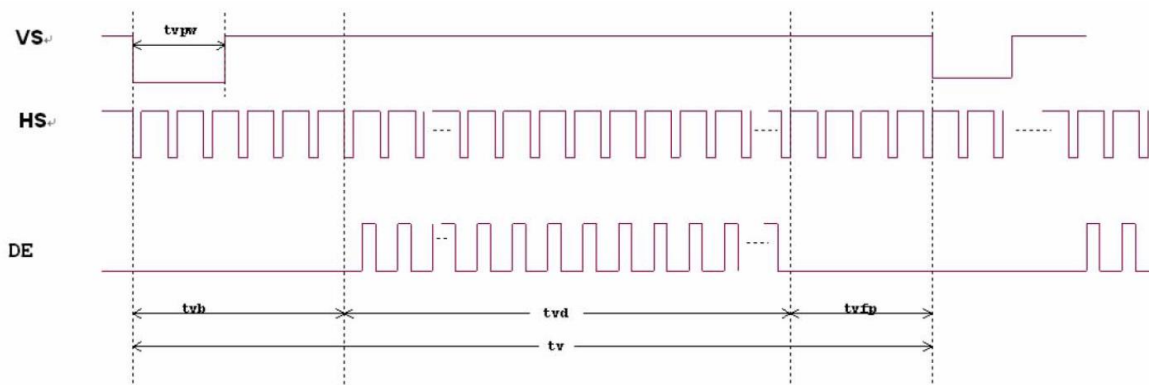
## 8 LCD TIMING CHARACTERISTICS

### 8.1 Clock and data input time diagram

Figure 5. Horizontal input timing diagram.



Figure 6. Vertical input timing diagram



### 8.2 Parallel RGB input timing table

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
DCLK Frequency	Fclk	26.4	33.3	46.8	MHz
VSD Period Time	Tv	510	525	650	TH
VSD Display Area	Tvd		480		TH
VSD Blanking	Tvb		23		TH
VSD Front Porch	Tvfp	7	22	147	TH
VSD Pulse Width	Tvpw	1	-	20	TH
HSD Pulse Width	Thpw	1	-	40	DCLK
HSD Period Time	Th	862	1056	1200	DCLK
HSD Display Area	Thd		800		DCLK
HSD Blanking	Thb		46		DCLK
HSD Front Porch	Thfp	16	210	354	DCLK

## 9 CAPACITIVE TOUCH SCREEN PANEL SPECIFICATIONS

### 9.1 Mechanical characteristics

DESCRIPTION	INL SPECIFICATION	REMARK
Touch Panel Size	7 inch	
Outline Dimension (OD)	164.4mm x 99.8mm	Cover Lens Outline
Product Thickness	1.66mm	
Glass Thickness	1.1mm	
Ink View Area	156.08mm x 87.92mm	
Sensor Active Area	156.68mm x 88.52mm	
Input Method	5 Finger	
Activation Force	Touch	
Surface Hardness	≥7H	

### 9.2 Electrical characteristics

DESCRIPTION	SPECIFICATION	
Operating Voltage	DC 2.8~3.3V	
Power Consumption (IDD)	Active Mode	10~18mA
	Sleep Mode	30~50μA
Interface	I <sup>2</sup> C	
Linearity	<1.5%	
Controller	FT5406	
I2C address	0x38 (7 bit address)	
Resolution	1792*1024	

### 9.3 Interface description

PIN NO.	SYMBOL	DESCRIPTION	REMARK
1	VSS	Power Ground	
2	VDD	Power For CTP	
3	SCL	I2C SCL	
4	NC	-	
5	SDA	I2C SDA	
6	NC	-	
7	/RST	Reset pin	
8	/WAKE	Wake signal from host	
9	/INT	Interrupt signal from CTP	
10	VSS	Power Ground	

### 9.4 Interface timing characteristics

PARAMETER	MIN	MAX	UNIT
SCL Frequency	0	400	kHz
Bus Free Time Between a STOP and START Condition	4.7	/	μs
Hold Time (repeated) START Condition	4.0	/	μs
Data Setup Time	250	/	ns
Setup Time for Repeated START Condition	4.7	/	μs
Setup Time for STOP Condition	4.0	/	μs

### 9.5 I2C Read/Write Interface Description

Figure 7. Write N bytes to I2C slave

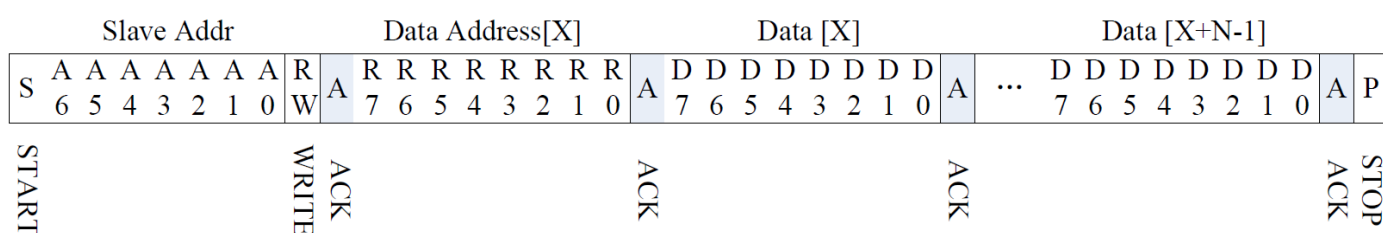


Figure 8. Set Data Address

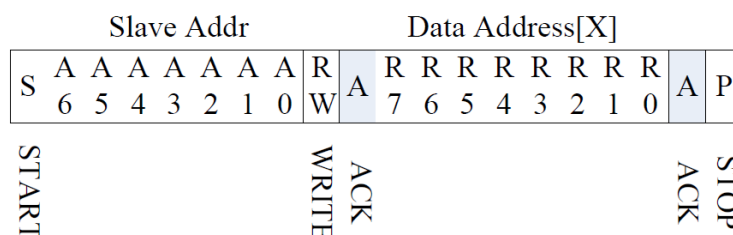
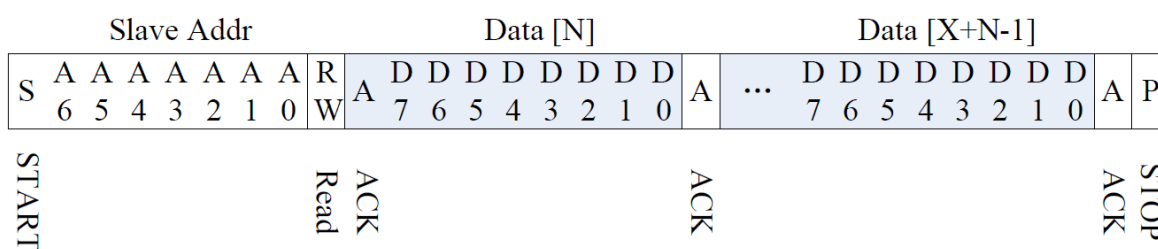
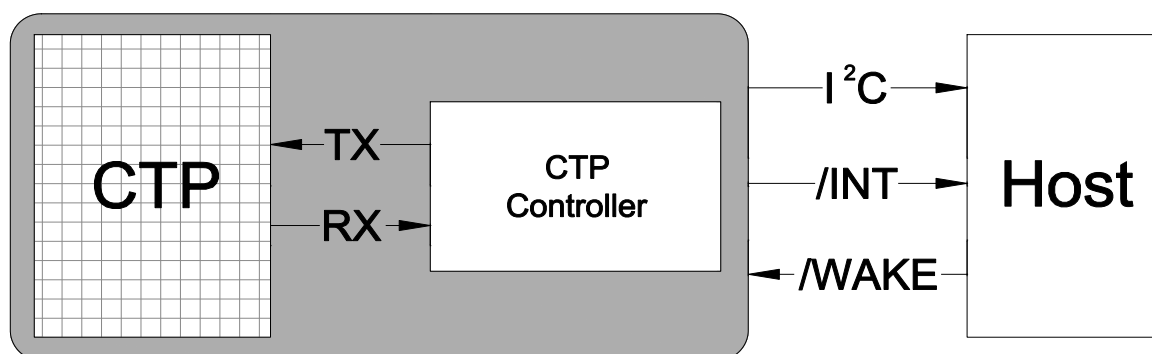


Figure 9. Read X bytes from I2C Slave



## 9.6 Communication of the I<sup>2</sup>C interface with Host

Figure 10. Communication of the I<sup>2</sup>C interface with Host



## 9.7 Touch data read protocol

ADDRESS	NAME	BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0	HOST ACCESS	
00h	DEVIDE_MODE	Device Mode[2:0]									RW
01h	GEST_ID	Gesture ID[7:0]								R	
02h	TD_STATUS				Number of touch points[3:0]						R
03h	TOUCH1_XH	1 <sup>st</sup> Event Flag					1 <sup>st</sup> Touch X Position[11:8]				R
04h	TOUCH1_XL	1 <sup>st</sup> Touch X Position[7:0]								R	
05h	TOUCH1_YH	1 <sup>st</sup> Touch ID[3:0]			1 <sup>st</sup> Touch X Position[11:8]						R
06h	TOUCH1_YL	1 <sup>st</sup> Touch Y Position[7:0]								R	
07h										R	
08h										R	
09h	TOUCH2_XH	2 <sup>nd</sup> Event Flag					2 <sup>nd</sup> Touch X Position[11:8]				R
0Ah	TOUCH2_XL	2 <sup>nd</sup> Touch X Position[7:0]								R	
0Bh	TOUCH2_YH	2 <sup>nd</sup> Touch ID[3:0]			2 <sup>nd</sup> Touch X Position[11:8]						R
0Ch	TOUCH2_YL	2 <sup>nd</sup> Touch Y Position[7:0]								R	
0Dh										R	
0Eh										R	
0Fh	TOUCH3_XH	3 <sup>rd</sup> Event Flag					3 <sup>rd</sup> Touch X Position[11:8]				R
10h	TOUCH3_XL	3 <sup>rd</sup> Touch X Position[7:0]								R	
11h	TOUCH3_YH	3 <sup>rd</sup> Touch ID[3:0]			3 <sup>rd</sup> Touch X Position[11:8]						R
12h	TOUCH3_YL	3 <sup>rd</sup> Touch Y Position[7:0]								R	
13h										R	
14h										R	
15h	TOUCH4_XH	4 <sup>th</sup> Event Flag					4 <sup>th</sup> Touch X Position[11:8]				R
16h	TOUCH4_XL	4 <sup>th</sup> Touch X Position[7:0]								R	
17h	TOUCH4_YH	4 <sup>th</sup> Touch ID[3:0]			4 <sup>th</sup> Touch X Position[11:8]						R
18h	TOUCH4_YL	4 <sup>th</sup> Touch Y Position[7:0]								R	
19h										R	
1Ah										R	
1Bh	TOUCH5_XH	5 <sup>th</sup> Event Flag					5 <sup>th</sup> Touch X Position[11:8]				R
1Ch	TOUCH5_XL	5 <sup>th</sup> Touch X Position[7:0]								R	
1Dh	TOUCH5_YH	5 <sup>th</sup> Touch ID[3:0]			5 <sup>th</sup> Touch X Position[11:8]						R
1Eh	TOUCH5_YL	5 <sup>th</sup> Touch Y Position[7:0]								R	

## 9.8 Data description.

### DEVICE\_MODE

This register is the device mode register, configure it to determine the current mode of the chip.

ADDRESS	BIT ADDRESS	REGISTER NAME	DESCRIPTION
00h	6:4	Device Mode [2:0]	000b Work Mode 100b Factory Mode – Read Raw Data

### GEST\_ID

This register describes the gesture of a valid touch.

ADDRESS	BIT ADDRESS	REGISTER NAME	DESCRIPTION
01h	7:0	Gesture ID [7:0]	Gesture ID 0x10 Move Up 0x14 Move Down 0x18 Move Right 0x48 Zoom In 0x49 Zoom Out 0x00 No Gesture

### TD\_STATUS

This register is the Touch Data status register.

ADDRESS	BIT ADDRESS	REGISTER NAME	DESCRIPTION
02h	3:0	Number of Touch Points [2:0]	How Many Points Detected 1-5 is Valid
	7:4		

### TOUCHn\_XH(n:1-10)

This register describes MSB of the X coordinate of the nth touch point and the corresponding event flag.

ADDRESS	BIT ADDRESS	REGISTER NAME	DESCRIPTION
03h ~ 39h	7:6	Event Flag	00b: Put Down 01b: Put Up 10b: Contact 11b: Reserved
	5:4		Reserved
	3:0	Touch X Position [11:8]	MSB of Touch X Position in Pixels

### TOUCHn\_XL(n:1-10)

This register describes LSB of the X coordinate of the nth touch point.

ADDRESS	BIT ADDRESS	REGISTER NAME	DESCRIPTION
04h ~ 3Ah	7:0	Touch X Position [7:0]	LSB of the Touch X Position in Pixels

### TOUCHn\_YH(n:1-10)

This register describes MSB of the Y coordinate of the nth touch point and corresponding touch ID.

ADDRESS	BIT ADDRESS	REGISTER NAME	DESCRIPTION
05h ~ 3Bh	7:4 3:0	Touch ID[3:0] Touch X Position [11:8]	Touch ID of Touch Point MSB of Touch Y Position in Pixels

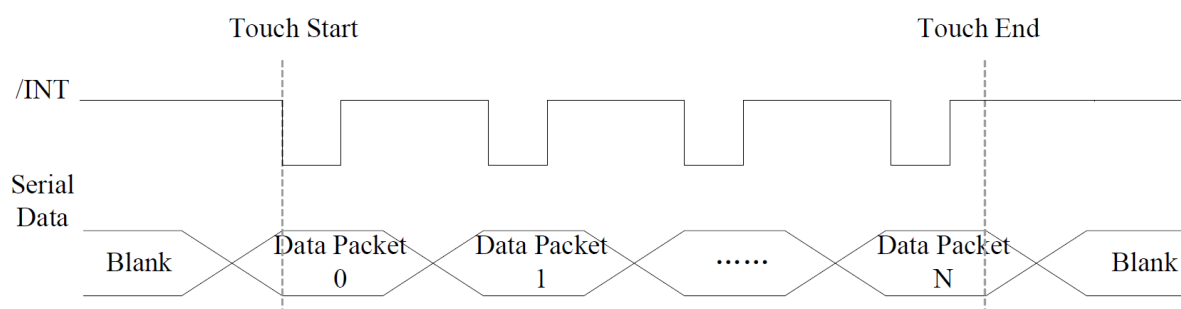
### TOUCHn\_YL(n:1-10)

This register describes LSB of the Y coordinate of the nth touch point.

ADDRESS	BIT ADDRESS	REGISTER NAME	DESCRIPTION
05h ~ 3Bh	7:0	Touch X Position [7:0]	LSB of the Touch Y Position in Pixels

## 9.9 Interrupt Trigger Mode

Figure 11. Interrupt trigger mode timing



## 10 RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION
1	High Temperature Storage	80±2°C/240hours
2	Low Temperature Storage	-30±2°C/240hours
3	High Temperature Operating	70±2°C/240hours
4	Low Temperature Operating	-20±2°C/240hours
5	Temperature Cycle	-30±2°C~25~80±2°C × 20 cycles (30min.) (5min.) (30min.)
6	Damp Proof Test	60°C ±5°C × 90%RH/240hours
7	Vibration Test	Frequency 10Hz~55Hz Amplitude of vibration : 1.5mm Sweep: 10Hz~55Hz~10Hz X, Y, Z 2 hours for each direction.
8	Package Vibration Test	Random vibration :0.15G*G/HZ from 5-200HZ,-6dB/Octave from 200-500HZ of each direction of X.Y. Z (6 hours for total)
9	Package Drop Test	Height:60 cm 1 corner,3 edges,6 surfaces
10	ESD Test	± 2KV, Human body mode,100pF/1500Ω
11	Mechanical Shock	100G 6ms, X, Y, Z 3 times for each direction



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