

TFT MODULE SPECIFICATION



RVT70AQLFWR00 V2

TN LVDS 7.0" LCD TFT DATASHEET

Rev.2.1

2021-11-23

ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally white	/
Size	7.0	Inch
Viewing Direction	12:00	O'clock
Gray Scale Inversion Direction	6:00	O'clock
Outside Dimensions (W x H x D)	181.60 x 100.60 x 11.70	mm
Active Area (W x H)	154.08 x 85.92	mm
Pixel Pitch (W x H)	0.1790 x 0.1926	mm
Resolution	800 (RGB) x 480	/
Brightness	400	cd/m ²
LCD Interface Type	LVDS	/
Driver On Board	GM8284DD	/
Color Depth	16.7 M	/
Pixel Arrangement	RGB Vertical Stripe	/
With/Without Touch	With Resistive Touch Panel	/
Weight	TBD	g

Note 1: RoHS compliant

Note 2: LCM weight tolerance: ± 5%.



1. REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
2.0	2021-03-24	Initial Release	
2.1	2021-11-23	Update the new template	



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3. MODULE CLASSIFICATION INFORMATION

RV	T	70	A	Q	L	F	W	R	00	V2
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.	11.

NO.	PARAMETER	SYMBOL
1.	BRAND	RV – Riverdi
2.	PRODUCT TYPE	T – TFT Standard
3.	DISPLAY SIZE	70 – 7.0"
4.	MODEL SERIAL NO.	A – (A - Z)
5.	RESOLUTION	Q – 800 x 480 px
6.	INTERFACE	L – TFT LCD, LVDS
7.	FRAME	F – With Mounting Metal Frame
8.	BACKLIGHT TYPE	W – LED White
9.	TOUCH PANEL	R – With Resistive Touch Panel
10.	OPTION	00 – (00 - 99)
11.	VERSION	V2

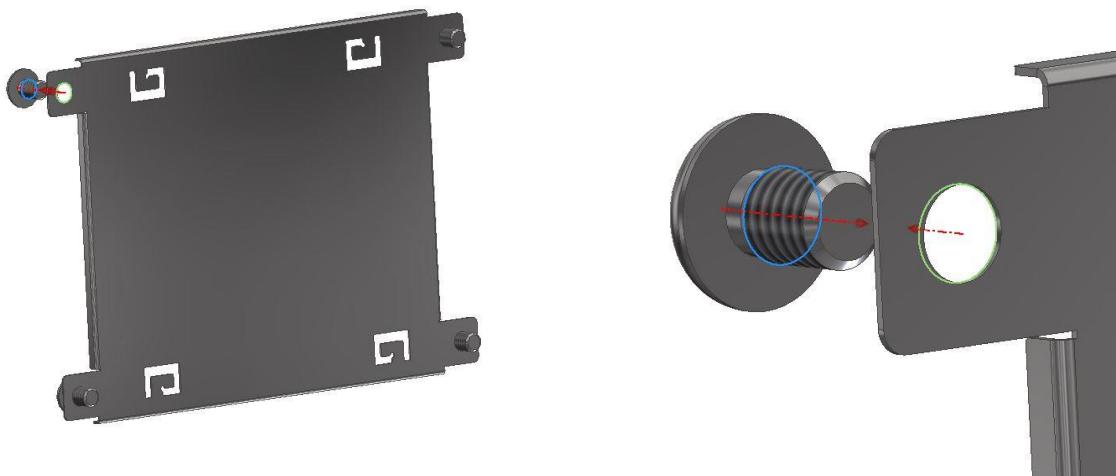


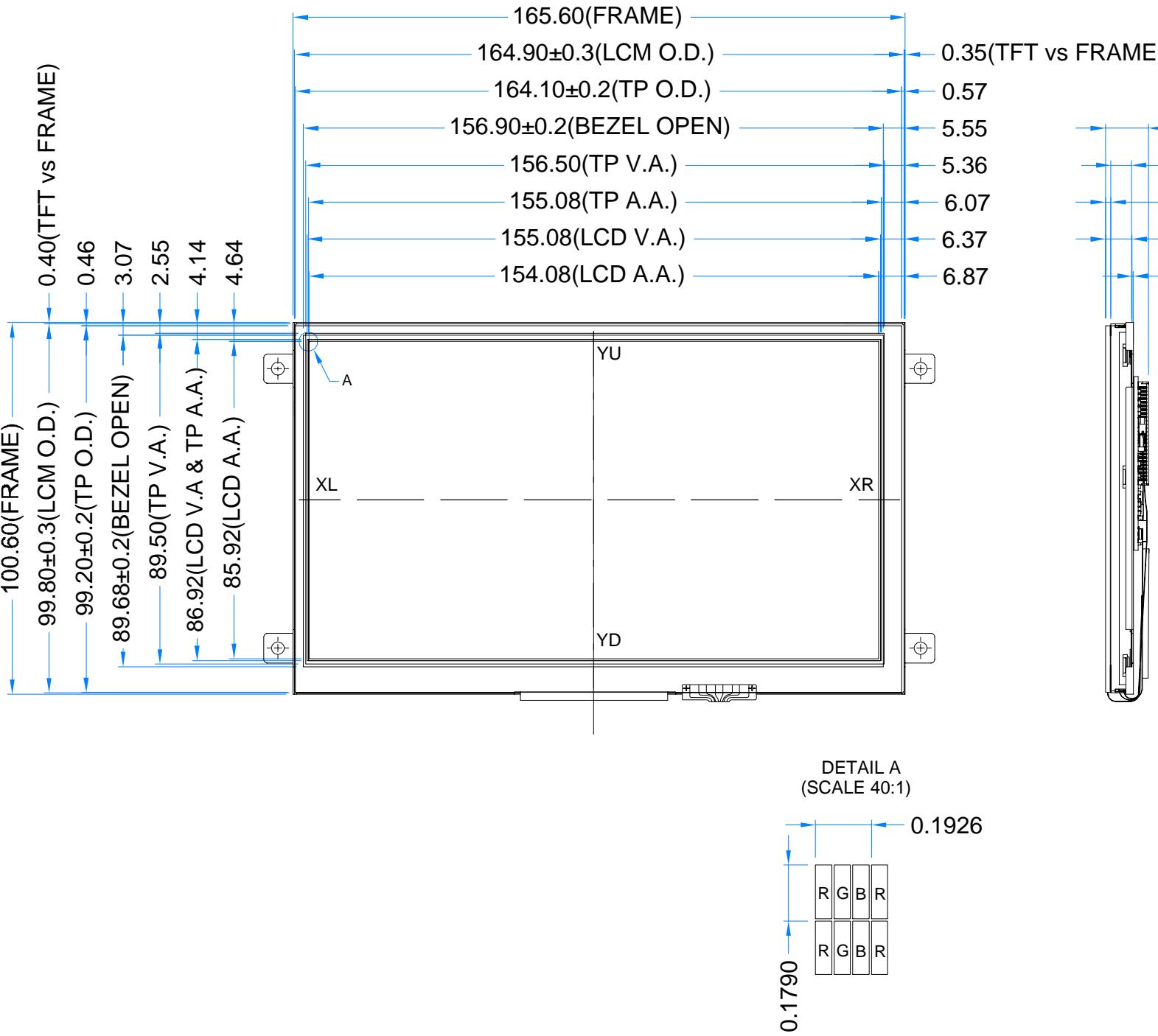
4. ASSEMBLY GUIDE

4.1 Mounting frame

For dimensions 3.5", 4.3", 5.0", 7.0" and 10.1", the product with mounting frame version is available. Thanks to the four catches attached to the side, frame provides strong assembly to the surface by mounting element (like the screw, see Figure 1). The frames are specially designed to fit Riverdi products perfectly. The diameter of the mounting hole is 3.5mm.

Figure 1. Mounting frame





LCM NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE
2. OPERATING VOLTAGE: VCC=3.3V
3. RESOLUTION: 800x480
4. VIEWING DIRECTION: 12 O'CLOCK
5. LED BACKLIGHT: 21-LED WHITE, BUILT-IN INVERTER
6. IC DRIVER: GM8284DD
7. SURFACE LUMINANCE: 400 cd/m²

TP NOTES:

1. TP STRUCTURE: FILM TO GLASS, 4 ELECTRODE RESISTIVE TOUCH PANEL

GENERAL NOTES:

1. OPERATING
2. STORAGE
3. WITHOUT IN
4. RoHS COMP



6. ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT
Supply Voltage for Logic	VDD	-0.3	5.0	V
Input Voltage for Logic	VIN	-0.3	VDD	V
Input Voltage for LED Inverter	BLVDD	-0.3	7.0	V
Operating Temperature	T _{OP}	-20	70	°C
Storage Temperature	T _{ST}	-30	80	°C
Humidity	RH	-	90% (Max 60°C)	RH

7. ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Supply Voltage for Module	VDD	-	3.3	-	V
Input Voltage for LED Inverter	BLVDD	2.8	5.0	5.5	V
LED Backlight Current	IDD _{backlight} (@ 5V)	-	450	540	mA
Input Voltage ' H ' level	V _{IH}	0.7VDD	-	VDD	V
Input Voltage ' L ' level	V _{IL}	0	-	0.2VDD	V
LED Lifetime	-	30,000	50,000	-	Hrs

Note: The LED lifetime is defined as the module brightness decrease to 50% original brightness at Ta = 25°C.



8. ELECTRO-OPTICAL CHARACTERISTICS

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 500mm from the LCD surface at a viewing angle of Φ and θ equal to 0°.

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	RMK	NOTE
Response Time	Tr+Tf	$\theta=0^\circ$ $\phi=0^\circ$ Ta=25 °C	-	20	35	ms	FIG 2.	4
Contrast Ratio	Cr		400	500	-	---	FIG 3.	1
Luminance Uniformity	δ WHITE		70	75	-	%	FIG 3.	3
Surface Luminance	Lv		-	400	-	cd/m ²	FIG 3.	2
Viewing Angle Range	θ	$\phi = 90^\circ$	40	50	-	deg	FIG 4.	6
		$\phi = 270^\circ$	60	70	-	deg	FIG 4.	
		$\phi = 0^\circ$	60	70	-	deg	FIG 4.	
		$\phi = 180^\circ$	60	70	-	deg	FIG 4.	
CIE (x, y) Chromaticity	Rx	$\theta=0^\circ$ $\phi=0^\circ$ Ta=25 °C	0.522	0.572	0.622	-	FIG 3.	5
	Ry		0.300	0.350	0.400	-		
	Gx		0.311	0.361	0.411	-		
	Gy		0.526	0.576	0.626	-		
	Bx		0.097	0.147	0.197	-		
	By		0.038	0.088	0.138	-		
	Wx		0.266	0.316	0.366	-		
	Wy		0.266	0.316	0.366	-		

Note 1. Contrast Ratio (CR) is defined mathematically as below, for more information see Figure 3.

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 3.

$$Lv = \text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}$$

Note 3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the minimum luminance of 5 points luminance by maximum luminance of 5 points luminance. For more information see Figure 3.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see Figure 2. The test equipment is Autronic-Melchers's ConoScope series.

Note 5. CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.



Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to LCD surface. For more information see Figure 4.

Note 7. For viewing angle and response time testing, the testing data is based on Autronic-Melchers's ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, CIE the test data is based on TOPCON's BM-5 photo detector.

Figure 2. The definition of response time

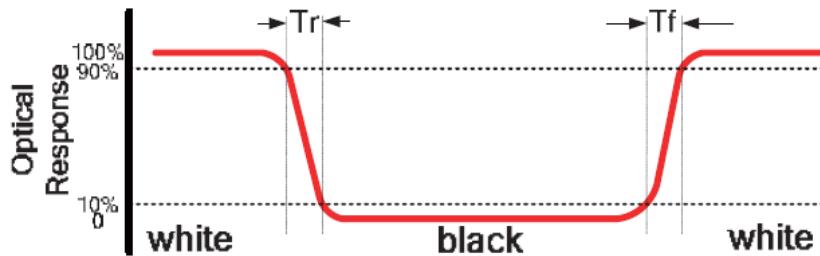


Figure 3. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

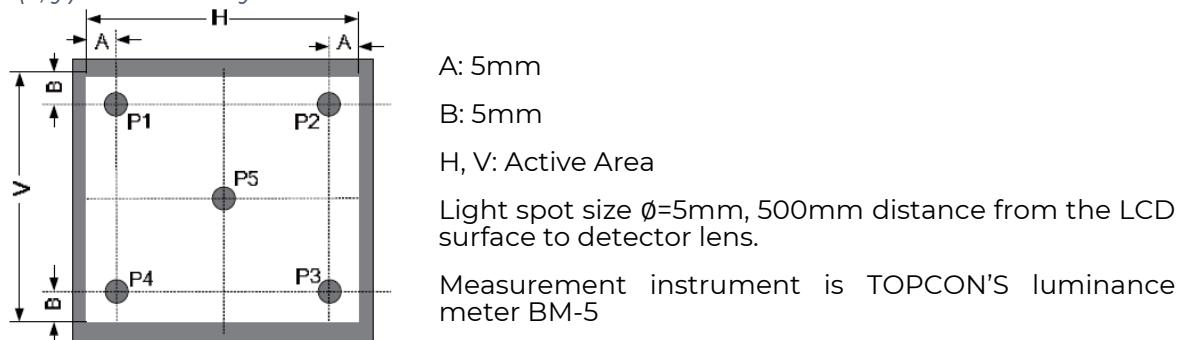
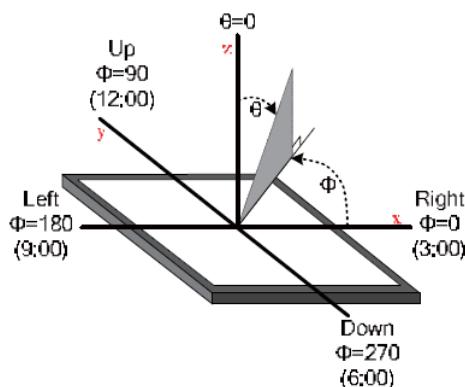


Figure 4. The definition of viewing angle





9. INTERFACES DESCRIPTION

9.1 CN1 assignment

PIN NO.	SYMBOL	DESCRIPTION
1	GND	Power Ground
2	RXCLK+	LVDS Clock Output Signal +
3	RXCLK-	LVDS Clock Output Signal -
4	GND	Power Ground
5	RXC0+	LVDS Channel 0 Input Signal +
6	RXC0-	LVDS Channel 0 Input Signal -
7	GND	Power Ground
8	RXC1+	Channel 1 Input Signal +
9	RXC1-	Channel 1 Input Signal -
10	GND	Power Ground
11	RXC2+	LVDS Channel 2 Input Signal +
12	RXC2-	LVDS Channel 2 Input Signal -
13	VDD	Power Supply: +3.3V
14	RXC3+	LVDS Channel 3 Input Signal +
15	RXC3-	LVDS Channel 3 Input Signal -
16	VDD	Power Supply: +3.3V
17	RESET	Global Reset Pin
18	BLGND	Backlight Ground
19	BLGND	Backlight Ground
20	BLVDD	Backlight Supply Voltage
21	BLVDD	Backlight Supply Voltage
22	BELENA	Enable Signal Control System
23	XL	X - Left
24	YD	Y - Bottom
25	XR	X - Right
26	YU	Y - Up

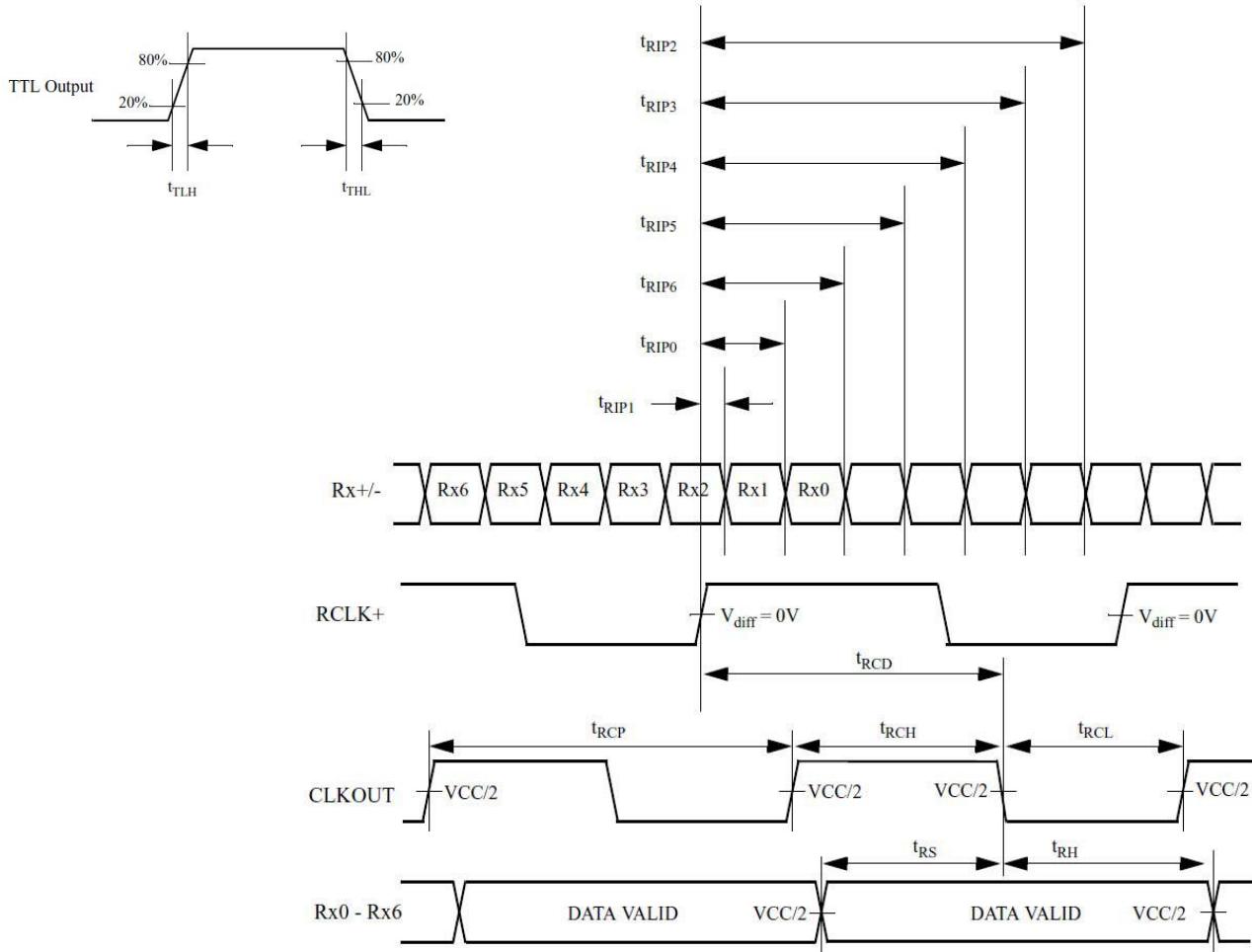


10. TIMING CHARACTERISTICS

10.1 LVDS timing condition

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
CLK OUT Period	tRCP	11.76	30.06(T)	50.0	ns
CLK OUT High Time	tRCH	-	4T/7	-	ns
CLK OUT Low Time	tRCL	-	3T/7	-	ns
RCLK+/- to CLK OUT Delay	tRCD	-	5T/7	-	ns
TTL Data Setup to CLK OUT	tRS	0.35T-0.3	-	-	ns
TTL Data Hold from CLK OUT	tRH	0.45T-1.6	-	-	ns
TTL Low to High Transition Time	tTLH	-	2.0	3.0	ns
TTL Low to Low Transition Time	tTHL	-	1.8	3.0	ns
Input Data Position0 (T=11.76ns)	tRIP1	-0.4	0.0	0.4	ns
Input Data Position0 (T=11.76ns)	tRIP0	T/7-0.4	T/7	T/7+0.4	ns
Input Data Position0 (T=11.76ns)	tRIP6	2T/7-0.4	2T/7	2T/7+0.4	ns
Input Data Position0 (T=11.76ns)	tRIP5	3T/7-0.4	3T/7	3T/7+0.4	ns
Input Data Position0 (T=11.76ns)	tRIP4	4T/7-0.4	4T/7	4T/7+0.4	ns
Input Data Position0 (T=11.76ns)	tRIP3	5T/7-0.4	5T/7	5T/7+0.4	ns
Input Data Position0 (T=11.76ns)	tRIP2	6T/7-0.4	6T/7	6T/7+0.4	ns
Phase Lock Loop Set	tRPLL	-	-	10	ms

10.2 AC characteristics



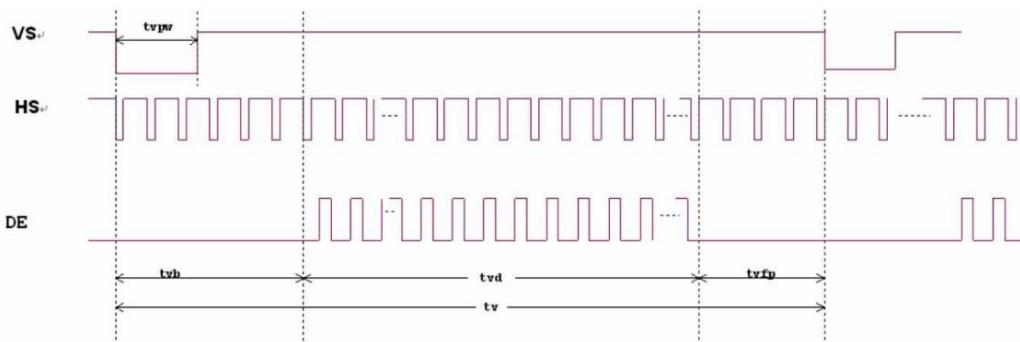


10.3 Clock and data input time diagram

Horizontal input timing diagram



Vertical input timing diagram



10.4 Parallel RGB input timing table

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
DCLK Frequency	Fclk	26.0	30.0	36.0	MHz
VSD Period Time	Tv	515	525	535	
VSD Display Area	Tvd		480		
VSD Blanking	Tvb		10		
VSD Front Porch	Tvfp	12	22	32	
VSD Pulse Width	Tpw	-	13	-	
HSD Pulse Width	Thpw	-	30	-	DCLK
HSD Period Time	Th	1026	1056	1086	
HSD Display Area	Thd		800		
HSD Blanking	Thb		16		
HSD Front Porch	Thfp	180	210	240	



11. RESISTIVE TOUCH SCREEN PANEL SPECIFICATIONS

11.1 Electrical characteristics

ITEM	VALUE			UNIT	REMARK
	Min.	Typ.	Max.		
Linearity	3.0	-	-	%	Analog X and Y directions
Terminal Resistance	440	-	1100	Ω	X (Film side)
	100	-	420	Ω	Y (Glass side)
Insulation Resistance	25	-	-	MΩ	DC 25V
Voltage	-	-	10	V	DC
Chattering	-	-	10	ms	100kΩ pull-up
Transparency	78	-	-	%	

Note. Avoid operating with hard or sharp material such as a ball point pen or a mechanical pencil except a polyacetal pen (tip R 0.8mm or less) or a finger.

11.2 Mechanical characteristics

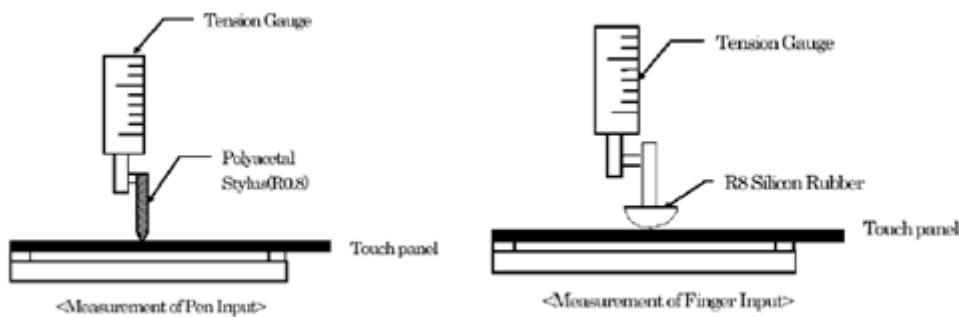
ITEM	VALUE			UNIT	REMARK
	Min.	Typ.	Max.		
Activation Force	20	-	100	gf	Note 1
Durability-Surface Scratching	Write 100,000	-	-	characters	Note 2
Durability-Surface Pitting	1,000 000	-	-	touches	Note 3
Surface Hardness	3	-	-	H	JIS K5400

Note 1: Activation force test condition

(1) Input DC 5V on X direction, drop off Polyacetal Stylus (R0.8), until output voltage stabilizes, then get the activation force.

(2) R8.0mm Silicon rubber for finger Activation force test.

(3) Test point: 9 points.



Note 2: Measurement for surface area.

(1) Scratch 100,000 times straight line on the film with a stylus change every 20,000 times.

(2) Force: 100gf.

(3) Speed: 60mm/sec.

(4) Stylus: R0.8 polyacetal tip.



Note 3: Pit 1,000,000 times on the film with a R0.8 silicon rubber.

(1) Force: 100gf.

(2) Speed: 2times/sec.

12. INSPECTION

Standard acceptance/rejection criteria for TFT module

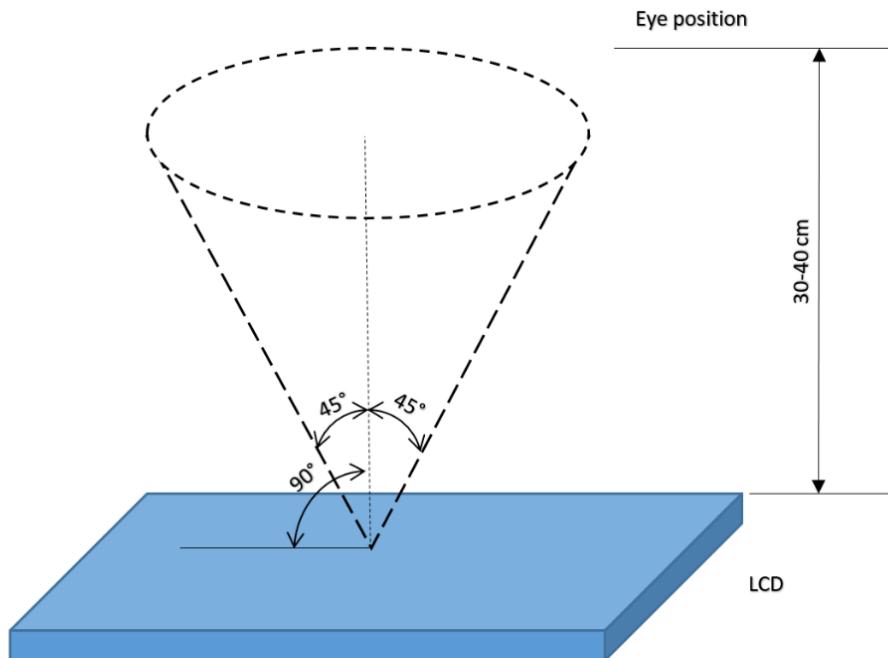
12.1 Inspection condition

Ambient conditions:

- Temperature: $25 \pm 2^\circ\text{C}$
- Humidity: $(60 \pm 10) \% \text{RH}$
- Illumination: Single fluorescent lamp non-directive (300 to 700 lux)

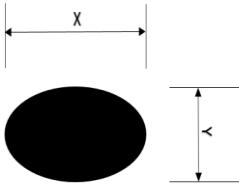
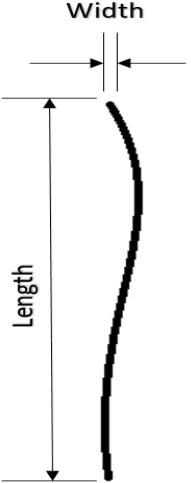
Viewing distance: $35 \pm 5\text{cm}$ between inspector bare eye and LCD.

Viewing Angle: U/D: $45^\circ/45^\circ$, L/R: $45^\circ/45^\circ$





12.2 Inspection standard

Item	Criterion																																		
Black spots, white spots, light leakage, Foreign Particle (round Type)	 $D = \frac{(x + y)}{2}$ <p>*Spots density: 10 mm</p> <table border="1"> <thead> <tr> <th colspan="2">Size < 5"</th> </tr> </thead> <tbody> <tr> <td>Average Diameter</td> <td>Qualified Qty</td> </tr> <tr> <td>D < 0.2 mm</td> <td>Ignored</td> </tr> <tr> <td>0.2 mm < D < 0.3 mm</td> <td>3</td> </tr> <tr> <td>0.3 mm < D < 0.5 mm</td> <td>2</td> </tr> <tr> <td>0.5 mm < D</td> <td>0</td> </tr> </tbody> </table> <table border="1"> <thead> <tr> <th colspan="2">Size >= 5"</th> </tr> </thead> <tbody> <tr> <td>Average Diameter</td> <td>Qualified Qty</td> </tr> <tr> <td>D < 0.2 mm</td> <td>Ignored</td> </tr> <tr> <td>0.2 mm < D < 0.3 mm</td> <td>4</td> </tr> <tr> <td>0.3 mm < D < 0.5 mm</td> <td>2</td> </tr> <tr> <td>0.5 mm < D</td> <td>0</td> </tr> </tbody> </table>	Size < 5"		Average Diameter	Qualified Qty	D < 0.2 mm	Ignored	0.2 mm < D < 0.3 mm	3	0.3 mm < D < 0.5 mm	2	0.5 mm < D	0	Size >= 5"		Average Diameter	Qualified Qty	D < 0.2 mm	Ignored	0.2 mm < D < 0.3 mm	4	0.3 mm < D < 0.5 mm	2	0.5 mm < D	0										
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Size >= 5"																									
Average Diameter	Qualified Qty																								
D < 0.25 mm	Ignored																								
0.25 mm < D < 0.5 mm	3																								
0.5 mm < D	0																								
Electrical Dot Defect	<table border="1"> <thead> <tr> <th colspan="2">Size < 5"</th> </tr> </thead> <tbody> <tr> <td>item</td><td>Qualified Qty</td></tr> <tr> <td>Black do defect</td><td>4</td></tr> <tr> <td>Bright dot defect</td><td>2</td></tr> <tr> <td>Total Dot</td><td>5</td></tr> </tbody> </table> <table border="1"> <thead> <tr> <th colspan="2">Size >= 5"</th> </tr> </thead> <tbody> <tr> <td>item</td><td>Qualified Qty</td></tr> <tr> <td>Black do defect</td><td>5</td></tr> <tr> <td>Bright dot defect</td><td>2</td></tr> <tr> <td>Total Dot</td><td>5</td></tr> </tbody> </table>	Size < 5"		item	Qualified Qty	Black do defect	4	Bright dot defect	2	Total Dot	5	Size >= 5"		item	Qualified Qty	Black do defect	5	Bright dot defect	2	Total Dot	5				
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Touch panel spot	Size < 5"		
	Average Diameter	Qualified Qty	
Touch panel White line Scratch	D < 0.2 mm	Ignored	
	0.2 mm < D < 0.4 mm	5	
Touch panel White line Scratch	0.4 mm < D < 0.5 mm	2	
	0.5 mm < D	0	
Touch panel White line Scratch	Size >= 5"		
	Average Diameter	Qualified Qty	
Touch panel White line Scratch	D<0.25 mm	Ignored	
	0.25 mm < D < 0.5 mm	4	
Touch panel White line Scratch	0.5 mm < D	0	
	Size < 5"		
Touch panel White line Scratch	Length	Width	Qualified Qty
	-	W< 0.02	Ignored
Touch panel White line Scratch	L < 3.0	0.02 < W <0.05	2
	L < 2.5	0.05 < W <0.08	
Touch panel White line Scratch	-	0.08 < W	0
	Size >= 5"		
Touch panel White line Scratch	Length	Width	Qualified Qty
	-	W< 0.03	Ignored
Touch panel White line Scratch	L < 5.0	0.03 < W <0.05	2
	-	0.05 < W	



13. RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION
1	High Temperature Storage	80±2°C/240hours
2	Low Temperature Storage	-30±2°C/240hours
3	High Temperature Operating	70±2°C/240hours
4	Low Temperature Operating	-20±2°C/240hours
5	Temperature Cycle	-30±2°C ~ 25 ~ 80±2°C x 20 cycles (30min.) (5min.) (30min.)
6	Damp Proof Test	60°C ± 5°C × 90%RH/240hours
7	Vibration Test	Frequency 10Hz~55Hz Amplitude of vibration: 1.5mm Sweep time: 11Mins Test period: Cycles For Each Direction of X, Y, Z (Packing Condition)
8	Shortage Test (Non-operating)	Random vibration :0.15G*G/HZ from 5-200HZ, -6dB/Octave from 200-500HZ of each direction of X.Y. Z (6 hours for total)
9	Package Drop Test	100G, 6Ms Direction: ±X, ±Y, ±Z Cycle: 3 Times

Note 1: Without water condensation.

Note 2: The function test shall be conducted after 2 hours storage at the room temperature (25°C±2°C) and room humidity (65%±5%) after removed from the test chamber. In the standard conditions, there shall be no functional defects occurred.



14. LEGAL INFORMATION

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