Low Vce(sat) transistor (strobe flash)

2SD2098 / 2SD2118

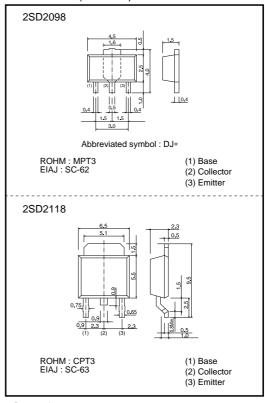
Features

- 1) Low VCE(sat). $V_{CE(sat)} = 0.25V (Typ.)$ (Ic/IB = 4A/0.1A)
- 2) Excellent DC current gain characteristics.
- 3) Complements the 2SB1386 / 2SB1412.

Structure

Epitaxial planar type NPN silicon transistor

●Dimensions (Unit: mm)



* Denotes hre

● Absolute maximum ratings (Ta=25°C)

=					
Parameter		Symbol	Limits	Unit	
Collector-base voltage		Vсво	50	V	
Collector-emitter voltage		Vceo	20	V	
Emitter-base voltage		Vево	6	V	
Calla stan assessat		Ic	5	A(DC)	
Collector current		Іср	10	A(Pulse) *1	
	2SD2098		0.5		
Collector power		Pc	2	W *2	
dissipation	2SD2118	PC	1		
			10	W(Tc=25°C)	
Junction temperature		Tj	150	°C	
Storage temperature		Tsta	-55 to +150	°C	

^{*1} Single pulse Pw=10ms *2 When mounted on a 40×40×0.7 mm ceramic board.

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	50	_	_	V	Ic=50μA
Collector-emitter breakdown voltage	BVceo	20	_	-	V	Ic=1mA
Emitter-base breakdown voltage	ВУЕВО	6	_	_	V	Iε=50μA
Collector cutoff current	Ісво	_	_	0.5	μΑ	Vcb=40V
Emitter cutoff current	Ієво	_	_	0.5	μΑ	V _{EB} =5V
Collector-emitter saturation voltage	VCE(sat)	_	0.3	1.0	V	Ic/I _B =4A/0.1A *
DC current transfer ratio	hfe	120	_	390	_	Vce=2V, Ic=0.5A *
Transition frequency	f⊤	_	150	-	MHz	Vce=6V, Ie=-50mA, f=100MHz
Output capacitance	Cob	_	35	_	pF	Vce=20V, Ie=0A, f=1MHz

 $[\]ast$ Measured using pulse current.

●Packaging specifications and hfe

	Package		Taping		
		Code	T100	TL	
Type	hfe	Basic ordering unit (pieces)	1000	2500	
2SD2098	QR	_	0	_	
2SD2118	QR		_	0	

hre values are classified as follows:

Item	Q	R
hfe	120 to 270	180 to 390

Electrical characteristic curves

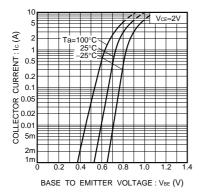


Fig.1 Grounded emitter propagation characteristics

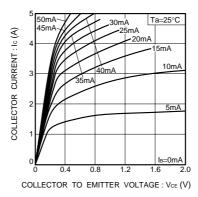


Fig.2 Grounded emitter output characteristics

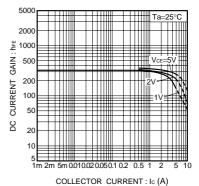


Fig.3 DC current gain vs. collector current (I)

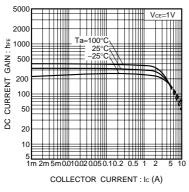


Fig.4 DC current gain vs. collector current (II)

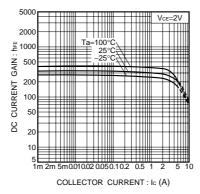


Fig.5 DC current gain vs. collector current (III)

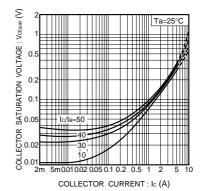


Fig.6 Collector-emitter saturation voltage vs. collector current (I)

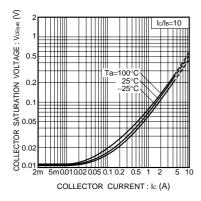


Fig.7 Collector-emitter saturation voltage vs. collector current (II)

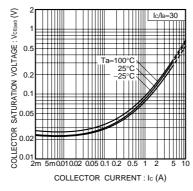


Fig.8 Collector-emitter saturation voltage vs. collector current (III)

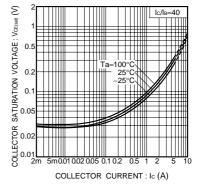


Fig.9 Collector-emitter saturation voltage vs. collector current (IV)

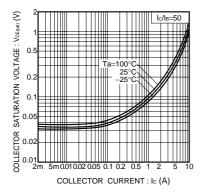


Fig.10 Collector-emitter saturation voltage vs. collector current (V)

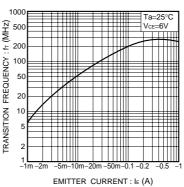


Fig.11 Gain bandwidth product vs. emitter current

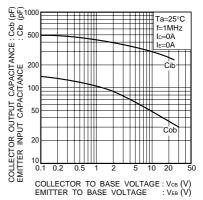


Fig.12 Collector output capacitance vs. collector-base voltage Emitter input capacitance vs. emitter-base voltage

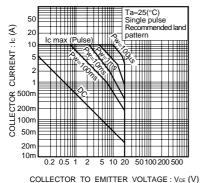
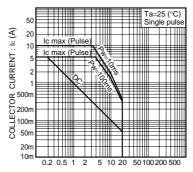


Fig.13 Safe operating area

(2SD2098)



COLLECTOR TO EMITTER VOLTAGE: VCE (V)

Fig.14 Safe operating area (2SD2118)

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