High performance video signal switcher
Five inputs Dual Circuits

## - Description

The BA7626F/FS is a 5 -input video signal switching circuit with a broadband 6 dB amplifier that was developed for AV amplifier input switching. Just by devising a transistor buffer in the output, player switching of two VCR or other videotape players and three DVD players or other playback devices is possible. Moreover, input switching and switching of recording to a VCR or other device also can be performed independently. Since the input circuit of the BA7626F/FS is terminated by $20 \mathrm{k} \Omega$ impedance, it is suited to not only video signal but also chroma signal or audio signal switching.

## - Features

1) 5 input line, 3 output line switching
2) Built-in 6 dB amplifier
3) 5 V operating voltage
4) $20 \mathrm{k} \Omega$ input impedance

## -Applications

AV amplifiers, Video selectors, etc.

- Line up matrix

| Part No. | BA7626F | BA7626FS |
| :--- | :---: | :---: |
| Package | SOP16 | SSOP-A16 |
| Input type | Bias (R=20k $\Omega)$ |  |

- Absolute Maximum Ratings $\left(\mathrm{Ta}=25^{\circ} \mathrm{C}\right)$

| Parameter |  | Symbol | Ratings | Unit |
| :--- | :--- | :---: | :---: | :---: |
| Supply voltage | Vcc | 9 | V |  |
| Power dissipation | BA7626F | Pd | $300 \times 1$ | mW |
|  | BA7626FS |  | $600 \times 2$ |  |
| Operating temperature | Topr | $-25 \sim+70$ | ${ }^{\circ} \mathrm{C}$ |  |
| Storage temperature | Tstg | $-55 \sim+125$ |  |  |

※1 Derating is done at $3.0 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $\mathrm{Ta}=25^{\circ} \mathrm{C}$.(BA7626F)
※2 Derating is done at $6.0 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $\mathrm{Ta}=25^{\circ} \mathrm{C}$.(BA7626FS)

- Operating Range ( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ )

| Parameter | Symbol | Ratings |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |
|  |  | Min. | Typ. | Max. |  |
| Power supply voltage | Vcc | 4.5 | 5.0 | 5.5 | V |

-Electrical characQteristics (Unless otherwise noted $\mathrm{Ta}=25^{\circ} \mathrm{C}, \mathrm{Vcc}=5.0 \mathrm{~V}$ )

| Parameter | Symbol | Limits |  |  | Unit | Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Min. | Typ. | Max. |  |  |
| Circuit current | ICC | - | 15.0 | 20.0 | mA | - |
| Maximum output level | Vom | 2.3 | 2.5 | - | Vp-p | $\mathrm{f}=1 \mathrm{kHz}, \mathrm{THD}=0.5 \%$ |
| Voltage gain | $\mathrm{G}_{V}$ | 5.7 | 6.2 | 6.7 | dB | $\mathrm{f}=1 \mathrm{MHz}, \mathrm{V}_{\mathrm{IN}}=1 \mathrm{Vp}-\mathrm{p}$ |
| Interchannel crosstalk | $\mathrm{G}_{\mathrm{T}}$ | - | -65 | -45 | dB | $\mathrm{f}=4.43 \mathrm{MHz}, \mathrm{V}_{\text {IN }}=1 \mathrm{Vp}-\mathrm{p}$ |
| Mute level | CTM | - | -35 | -25 | dB | $\mathrm{f}=4.43 \mathrm{MHz}, \mathrm{V}_{\text {IN }}=1 \mathrm{Vp}-\mathrm{p}$ |
| Frequency characteristic | $\mathrm{G}_{\mathrm{f}}$ | -3 | 0 | +3 | dB | $10 \mathrm{MHz} / 1 \mathrm{MHz}, \mathrm{V}_{\mathrm{IN}}=1 \mathrm{Vp}-\mathrm{p}$ |
| Input inpedance | $\mathrm{Z}_{\mathrm{IN}}$ | 16 | 20 | 24 | $\mathrm{k} \Omega$ | - |
| CTL pin switching level | $V_{\text {TH }}$ | 2.2 | - | 3.3 | V | - |

※ This product is not designed for protection against radioactive rays.

- Truth table

| Input |  | Output | Input |  |  | Output |  | Input |  | Output |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A | B | E | MONOUT | C | D | E | VOUT1 | C | D | E | VOUT2 |
| L | L | ※ | IN1 | L | L | $※$ | - | L | L | $※$ | IN1 |
| H | L | $※$ | IN2 | H | L | $※$ | IN2 | H | L | $※$ | - |
| L | H | $※$ | IN3 | L | H | $※$ | IN3 | L | H | $※$ | IN3 |
| H | H | L | IN4 | H | H | L | IN4 | H | H | L | IN4 |
| H | H | H | IN5 | H | H | H | IN5 | H | H | H | IN5 |

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## - Block diagram



Fig. 1 Block diagram

## - Application circuit



Fig. 2 Application circuit
-Pin descriptions $\left(\mathrm{Ta}=25^{\circ} \mathrm{C}, \mathrm{VCC}=5 \mathrm{~V}\right)$

| Pin No. | Pin name |
| :---: | :---: | :---: | :---: |

## - Notes for use

1) Numbers and data in entries are representative design values and are not guaranteed values of the items.
2) Although we are confident in recommending the sample application circuits, carefully check their characteristics further when using them. When modifying externally attached component constants before use, determine them so that they have sufficient margins by taking into account variations in externally attached components and the Rohm LSI, not only for static characteristics but also including transient characteristics.
3) Absolute maximum ratings

If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.
4) GND potential

Make the GND pin voltage such that it is the lowest voltage even when operating below it. Actually confirm that the voltage of each pin does not become a lower voltage than the GND pin, including transient phenomena.
5) Thermal design

Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation in actual states of use.
6) Shorts between pins and misinstallation

When mounting the LSI on a board, pay adequate attention to orientation and placement discrepancies of the LSI. If it is misinstalled and the power is turned on, the LSI may be damaged. It also may be damaged if it is shorted by a foreign substance coming between pins of the LSI or between a pin and a power supply or a pin and a GND.
7) Operation in strong magnetic fields

Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.

## -Ordering part number



SOP16


## SSOP-A16



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[^0]:    ※ Indicates "don't care"(H or L)

