| Structure | $:$ Silicon Monolithic Integrated Circuit |
| :--- | :--- |
| Product name | $:$ Single Circuit Wide Dynamic Range Video Signal Switchers |
| Type | $:$ BA7649AF |

## Features

1) Built-in mute function
2) Wide operating power supply voltage range
3) Good frequency characteristics (Typ. $10 \mathrm{MHz}, 0 \mathrm{~dB}$ )
4) Wide dynamic range (Typ. 3.5 Vp-p)
5) Sync-tip clamp input
6) Low interchannel crosstalk (Typ. $-65 \mathrm{~dB}, \mathrm{f}=4.43 \mathrm{MHz}$ )

OAbsolute Maximum Ratings ( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ )

| Parameter | Symbol | Limits | Unit |
| :---: | :---: | :---: | :---: |
| Supply voltage | Vcc | 13.5 | V |
| Power dissipation | Pd | $450 \quad * 1$ | mW |
| Operating temperature | Topr | $-25 \sim+75$ | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | Tstg | $-55 \sim+125$ | ${ }^{\circ} \mathrm{C}$ |

*1 Deratings is done at $4.5 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $\mathrm{Ta}=25^{\circ} \mathrm{C}$.

OOperating Range ( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ )

| Parameter | Symbol | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Power supply voltage | Vcc | 4.5 | 5.0 | 13.0 | V |

* This product is not designed for protection against radioactive rays.

The product described in this specification is designed to be used with ordinary electronic equipment or devices (such as audio-visual equipment, office-automation equipment, communications devices, electrical appliances, and electronic toys). Should you intend to use this product with equipment or devices which require an extremely high level or reliability and the malfunction of which would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), please be sure to consult with our sales representative in advance.

OElectrical characteristics (Unless otherwise noted, $\mathrm{Ta}=25^{\circ} \mathrm{C}, \mathrm{Vcc}=5.0 \mathrm{~V}$ )

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Circuit current | Icc | - | 9.5 | 14.5 | mA |  |
| Maximum output level | $\mathrm{V}_{\mathrm{OM}}$ | 3.0 | 3.5 | - | $\mathrm{V}_{\mathrm{p}-\mathrm{p}}$ | $\mathrm{f}=1 \mathrm{kHz}, \mathrm{THD}=0.5 \%$ |
| Voltage gain | GV | -0.5 | 0 | +0.5 | dB | $\mathrm{f}=1 \mathrm{MHz}, \mathrm{Vin}=1.0 \mathrm{~V}_{\mathrm{p}-\mathrm{p}}$ |
| Interchannel crosstalk | $\mathrm{C}_{\mathrm{T}}$ | - | -65 | - | dB | $\mathrm{f}=4.43 \mathrm{MHz}, \mathrm{Vin}=1.0 \mathrm{~V}_{\mathrm{p}-\mathrm{p}}$ |
| Frequency characteristic | $\mathrm{GF}_{\mathrm{F}}$ | -3.0 | 0.0 | +1.0 | dB | $10 \mathrm{MHz} / 1 \mathrm{MHz}, \mathrm{Vin}=1.0 \mathrm{~V}_{\mathrm{p}-\mathrm{p}}$ |
| CTL pin switching level A | $\mathrm{V}_{\mathrm{TH}-\mathrm{A}}$ | 1.0 | 2.0 | 3.0 | V |  |
| CTL pin switching level B | $\mathrm{V}_{\mathrm{TH}-\mathrm{B}}$ | 1.0 | 2.0 | 3.0 | V |  |
| CTL pin switching level C | $\mathrm{V}_{\mathrm{TH}-\mathrm{C}}$ | 1.0 | 2.0 | 3.0 | V |  |

OBlock diagram


| Pin <br> No. | Pin name |
| :---: | :---: |
| 1 | IN1 |
| 2 | GND |
| 3 | CTLA |
| 4 | CTLB |
| 5 | CTLC |
| 6 | IN2 |
| 7 | GND |
| 8 | IN3 |
| 9 | GND |
| 10 | IN4 |
| 11 | VCC |
| 12 | IN5 |
| 13 | GND |
| 14 | Vout |

OOuter dimensions


OCautions on use

1) Absolute maximum ratings

If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.
2) GND potential

Make the GND pin voltage such that it is the lowest voltage even when operating below it.
Actually confirm that the voltage of each pin does not become a lower voltage than the GND pin, including transient phenomena.
3) Thermal design

Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation in actual states of use.
4) Shorts between pins and miss-installation When mounting the LSI on a board, pay adequate attention to orientation and placement discrepancies of the LSI. If it is miss-installed and the power is turned on, the LSI may be damaged. It also may be damaged if it is shorted by a foreign substance coming between pins of the LSI or between a pin and a power supply or a pin and a GND.
5) Operation in strong magnetic fields Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.

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