## Analog Audio Processors series

## Sound Processors <br> for Car Audio

## BD37033FV-M

## General Description

BD37033FV-M is sound processors for car audio. The functions are stereo input selector with single end 3 and ground isolation input 2, volume, 3-band parametric equalizer, loudness, 6 ch fader volume, LPF for subwoofer, anti-aliasing-filter, Mixing volume. Moreover, "Advanced switch circuit", that is ROHM original technology, can reduce various switching noise (ex. No-signal, low frequency likes 20 Hz \& large signal inputs). "Advanced switch" makes control of microcomputer easier, and can construct high quality car audio system.

## Features

- Reduce switching noise of volume, mute, fader volume, bass, middle, treble, loudness by using advanced switch circuit [Possible to control all steps].
- Built-in input selector (single 3 / diff 2).
- Decrease the number of external components by built-in 3-band equalizer filter, LPF for subwoofer, loudness filter. And, possible to control Q, Gv, fo of 3 -band equalizer and fc of LPF, Gv, fo of loudness by $I^{2} \mathrm{C}$ BUS control freely.
- Built-in mixing volume and anti-aliasing-filter.
- Built-in anti-GSM-noise-filter.
- Package is SSOP-B28. Putting input-terminals together and output-terminals together can make PCB layout easier and can makes area of PCB smaller.
- It is possible to control by $3.3 \mathrm{~V} / 5 \mathrm{~V}$ for $\mathrm{I}^{2} \mathrm{C}$ BUS.
- AEC-Q100 Qualified.


## Applications

- It is the optimal for the car audio. Besides, it is possible to use for the audio equipment of mini Compo, micro Compo, TV etc with all kinds.


## Key Specifications

■ Current upon no signal:
31 mA (Typ.)

- Total harmonic distortion: 0.002\%(Typ.)
- Maximum input voltage:
2.1 Vrms(Typ.)
- Cross-talk between selectors: 100 dB (Typ.)
- Ripple rejection
- Output noise voltage:
-65dB(Typ.)
- Residual output noise voltage: $5.5 \mu \mathrm{Vrms}$ (Typ.)
- Operating Range of Temperature: $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$
package(s)
SSOP-B28

W(Typ.) x D(Typ.) x H(Max.) $10.00 \mathrm{~mm} \times 7.60 \mathrm{~mm} \times 1.35 \mathrm{~mm}$


SSOP-B28

## Typical Application Circuit



Figure 1. Application Circuit Diagram

## Pin Configuration

SSOP-B28
(TOP VIEW)


Figure 2. Pin configuration

Pin Descriptions

| Terminal <br> Number | Terminal <br> Name | Description | Terminal <br> Number | Terminal <br> Name | Description |
| :---: | :---: | :--- | :---: | :---: | :--- |
| 1 | A1 | A input terminal of 1ch | 15 | MUTE | External compulsory mute terminal |
| 2 | A2 | A input terminal of 2ch | 16 | LRST | Level meter reset terminal |
| 3 | B1 | B input terminal of 1ch | 17 | LOUT | Output terminal for Level meter |
| 4 | B2 | B input terminal of 2ch | 18 | OUTS2 | SW output terminal of 2ch |
| 5 | C1 | C input terminal of 1ch | 19 | OUTS1 | SW output terminal of 1ch |
| 6 | C2 | C input terminal of 2ch | 20 | OUTR2 | Rear output terminal of 2ch |
| 7 | DP1 | D positive input terminal of 1ch | 21 | OUTR1 | Rear output terminal of 1ch |
| 8 | DN | D negative input terminal | 22 | OUTF2 | Front output terminal of 2ch |
| 9 | DP2 | D positive input terminal of 2ch | 23 | OUTF1 | Front output terminal of 1ch |
| 10 | EP1 | E positive input terminal of 1ch | 24 | VCC | VCC terminal for power supply |
| 11 | EN1 | E negative input terminal of 1ch | 25 | SCL | I $^{2}$ C Communication clock terminal |
| 12 | EN2 | E negative input terminal of 2ch | 26 | SDA | I $^{2}$ C Communication data terminal |
| 13 | EP2 | E positive input terminal of 2ch | 27 | GND | GND terminal |
| 14 | MIN | Mixing input terminal | 28 | VREF | VREF terminal |

## Block Diagram



Figure 3. Block Diagram
Absolute Maximum Ratings

| Parameter | Symbol | Limits | Unit |
| :---: | :---: | :---: | :---: |
| Power supply Voltage | VCC $\ldots 1$ | 10.0 | V |
| Input Voltage | Vin $\ldots 1$ | VCC+0.3 to GND-0.3 <br> Only SCL,SDA 7 to GND-0.3 | V |
| Power Dissipation | Pd | $1.06 ※ 2$ | W |
| Storage Temperature | Tastg | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

※Maximum voltage which can be impressed referencing GND. Operation using batteries which is used in automobiles directly cannot be guaranteed.
$※ 2$ This value decreases $8.5 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ for $\mathrm{Ta}=25^{\circ} \mathrm{C}$ or more.
ROHM standard board shall be mounted
Thermal resistance $\theta \mathrm{ja}=117.6\left({ }^{\circ} \mathrm{C} / \mathrm{W}\right)$
ROHM Standard board Size: $70 \times 70 \times 1.6\left(\mathrm{~mm}^{3}\right)$
Material:A FR4 grass epoxy board(3\% or less of copper foil area)

## Operating Range

| Parameter | Symbol | Limits | Unit |
| :---: | :---: | :---: | :---: |
| Power supply voltage | VCC | 7.0 to 9.5 | V |
| Temperature | Topr | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |

## Electrical Characteristic

Unless specified particularly $\mathrm{Ta}=25^{\circ} \mathrm{C}, \mathrm{VCC}=8.5 \mathrm{~V}, \mathrm{f}=1 \mathrm{kHz}$, Vin $=1 \mathrm{Vrms}, \mathrm{Rg}=600 \Omega$, $\mathrm{RL}=10 \mathrm{k} \Omega$, A input, Volume 0 dB , Tone control OdB, Loudness OdB, LPF OFF, Fader OdB, Mix OFF, anti-aliasing-filter OFF

| $\begin{aligned} & \text { y } \\ & \text { O} \\ & \text { O } \end{aligned}$ | Item | Symbol | Limit |  |  | Unit | Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min. | Typ. | Max. |  |  |
| $\begin{aligned} & \bar{\Pi} \\ & \frac{\mathbb{D}}{\mathbb{D}} \\ & \text { © } \end{aligned}$ | Current upon no signal | $\mathrm{I}_{\mathrm{Q}}$ | - | 31 | 43 | mA | No signal |
|  | Voltage gain | Gv | -1.5 | 0 | +1.5 | dB | $\mathrm{Gv}=20 \mathrm{log}(\mathrm{VOUT} / \mathrm{VIN})$ |
|  | Channel balance | CB | -1.5 | 0 | +1.5 | dB | CB = GV1-GV2 |
|  | Total harmonic distortion | THD+N | - | 0.002 | 0.05 | \% | $\begin{aligned} & \text { VOUT }=1 \mathrm{Vrms} \\ & \mathrm{BW}=400-30 \mathrm{KHz} \end{aligned}$ |
|  | Output noise voltage * | $\mathrm{V}_{\mathrm{NO}}$ | - | 5.5 | 15 | $\mu \mathrm{Vrms}$ | $\begin{aligned} & \mathrm{Rg}=0 \Omega \\ & \mathrm{BW}=I \mathrm{HF}-\mathrm{A} \end{aligned}$ |
|  | Residual output noise voltage | $\mathrm{V}_{\text {NOR }}$ | - | 3.5 | 10 | $\mu \mathrm{Vrms}$ | $\begin{aligned} & \text { Fader }=-\infty \mathrm{dB} \\ & \mathrm{Rg}=0 \Omega, \mathrm{BW}=\mathrm{IHF}-\mathrm{A} \end{aligned}$ |
|  | Cross-talk between channels | CTC | - | -100 | -85 | dB | $\begin{aligned} & \mathrm{Rg}=0 \Omega \\ & \mathrm{CTC}=20 \log (\mathrm{VOUT} / \mathrm{VIN}) \\ & \mathrm{BW}=1 \mathrm{HF}-\mathrm{A} \end{aligned}$ |
|  | Ripple rejection | RR | - | -65 | -40 | dB | $\mathrm{f}=1 \mathrm{kHz}, \mathrm{VRR}=100 \mathrm{mVrms}$ RR=20log(VCC IN/VOUT) |
|  | Input impedance | RIN | 70 | 100 | 130 | k $\Omega$ |  |
|  | Maximum input voltage | $\mathrm{V}_{\text {IM }}$ | 2.0 | 2.1 | - | Vrms | $\begin{aligned} & \text { VIM at THD+N(VOUT })=1 \% \\ & \text { BW }=400-30 \mathrm{KHz} \end{aligned}$ |
|  | Cross-talk between selectors | CTS | - | -100 | -85 | dB | $\begin{aligned} & \mathrm{Rg}=0 \Omega \\ & \mathrm{CTS}=2010 \mathrm{~g}(\mathrm{VOUT} / \mathrm{VIN}) \\ & \mathrm{BW}=\mathrm{IHF}-\mathrm{A} \end{aligned}$ |
|  | Common mode rejection ratio | CMRR | 46 | 60 | - | dB | XP1 and XN input XP2 and XN input CMRR=20log(VIN/VOUT) $\begin{aligned} & \mathrm{BW}=\mathrm{IHF}-\mathrm{A} \\ & {[※ \mathrm{X} \cdot \cdot \cdot \mathrm{D} / \mathrm{E}]} \end{aligned}$ |
|  | Maximum input gain | $\mathrm{G}_{\mathrm{v} \text { max }}$ | +14 | +16 | +18 | dB | InputGain +16dB <br> VIN $=100 \mathrm{mVrms}$ <br> Gin=20log(VOUT/VIN) |
|  | Minimum input gain | $\mathrm{G}_{\mathrm{v} \text { MIN }}$ | -2 | 0 | +2 | dB | InputGain OdB <br> VIN=1Vrms <br> Gin=20log(VOUT/VIN) |
|  | Gain set error | Gverri | -2 | 0 | +2 | dB | GAIN $=+16$ to +1 dB |
| $\begin{aligned} & 0 \\ & \stackrel{0}{5} \\ & \stackrel{O}{0} \end{aligned}$ | Maximum boost gain | $\mathrm{G}_{\mathrm{v} \text { max }}$ | +13 | +15 | +17 | dB | Volume +15dB <br> $\mathrm{VIN}=100 \mathrm{mVrms}$ <br> Gin=20log(VOUT/VIN) |
|  | Maximum attenuation * | $\mathrm{G}_{\mathrm{v} \text { MIN }}$ | -83 | -79 | -75 | dB | Volume -79dB <br> VIN=2Vrms <br> Gin=20log(VOUT/VIN) |
|  | Gain set error | Gverr1 | -2 | 0 | +2 | dB | GAIN $=+15$ to +1 dB |
|  | Attenuation set error | GverR2 | -2 | 0 | +2 | dB | ATT $=0 \mathrm{~dB}$ to -79 dB |
| $\stackrel{\Phi}{\Sigma}$ | Mute attenuation * | $G_{\text {mute }}$ | - | -100 | -85 | dB | Mute ON <br> Gmute=20log(VOUT/VIN) $\mathrm{BW}=\mathrm{IHF}-\mathrm{A}$ |
| $\begin{aligned} & \mathscr{0} \\ & \tilde{\sim} \end{aligned}$ | Maximum boost gain | $\mathrm{G}_{\mathrm{B} \text { bSt }}$ | +13 | +15 | +17 | dB | $\begin{aligned} & \text { Gain }=+15 \mathrm{~dB} \mathrm{f}=100 \mathrm{~Hz} \\ & \mathrm{VIN}=100 \mathrm{mV} \mathrm{rms} \\ & \mathrm{~GB}=20 \log (\mathrm{VOUT} / \mathrm{VIN}) \end{aligned}$ |
|  | Maximum cut gain | $\mathrm{G}_{\mathrm{BCUT}}$ | -17 | -15 | -13 | dB | $\begin{aligned} & \text { Gain }=-15 \mathrm{~dB} \mathrm{f}=100 \mathrm{~Hz} \\ & \mathrm{VIN}=2 \mathrm{Vrms} \\ & \mathrm{~GB}=20 \log \text { (VOUT/VIN) } \end{aligned}$ |
|  | Gain set error | $\mathrm{G}_{\mathrm{B} \text { ERR }}$ | -2 | 0 | +2 | dB | Gain $=+15$ to $-15 \mathrm{~dB} \mathrm{f}=100 \mathrm{~Hz}$ |


| $\begin{aligned} & \text { Y } \\ & \text { O } \\ & \text { © } \end{aligned}$ | Item | Symbol | Limit |  |  | Unit | Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min. | Typ. | Max. |  |  |
| $\frac{\frac{0}{0}}{\stackrel{0}{2}}$ | Maximum boost gain | $\mathrm{Gm}_{\mathrm{MSST}}$ | +13 | +15 | +17 | dB | $\begin{aligned} & \text { Gain }=+15 \mathrm{~dB} \mathrm{f}=1 \mathrm{kHz} \\ & \text { VIN }=100 \mathrm{mVrms} \\ & \mathrm{G}_{\mathrm{M}}=20 \log (\mathrm{VOUT} / \mathrm{VIN}) \end{aligned}$ |
|  | Maximum cut gain | $\mathrm{G}_{\mathrm{M} \text { Cut }}$ | -17 | -15 | -13 | dB | $\begin{aligned} & \text { Gain }=-15 \mathrm{~dB} \mathrm{f}=1 \mathrm{kHz} \\ & \text { VIN }=2 \mathrm{Vrms} \\ & \mathrm{G}_{\mathrm{M}}=20 \log (\text { (VOUT/VIN }) \end{aligned}$ |
|  | Gain set error | $\mathrm{Gm}_{\text {ERR }}$ | -2 | 0 | +2 | dB | Gain $=+15$ to $-15 \mathrm{~dB} \mathrm{f}=1 \mathrm{kHz}$ |
| $\begin{aligned} & \frac{0}{O} \\ & \stackrel{0}{0} \\ & \stackrel{0}{2} \end{aligned}$ | Maximum boost gain | $\mathrm{G}_{\text {t bSt }}$ | +13 | +15 | +17 | dB | $\begin{aligned} & \text { Gain }=+15 \mathrm{~dB} \mathrm{f}=10 \mathrm{kHz} \\ & \text { VIN }=100 \mathrm{mVrms} \\ & \mathrm{G}_{\mathrm{T}}=20 \log \text { (VOUT/VIN) } \end{aligned}$ |
|  | Maximum cut gain | $\mathrm{G}_{\text {t Cut }}$ | -17 | -15 | -13 | dB | $\begin{aligned} & \text { Gain }=-15 \mathrm{~dB} \mathrm{f}=10 \mathrm{kHz} \\ & \text { VIN }=2 \mathrm{Vrms} \\ & \mathrm{G}_{\mathrm{T}}=20 \log (\mathrm{VOUT} / \mathrm{VIN}) \end{aligned}$ |
|  | Gain set error | $\mathrm{G}_{\text {terR }}$ | -2 | 0 | +2 | dB | Gain=+15 to $-15 \mathrm{~dB} \mathrm{f}=10 \mathrm{kHz}$ |
| $$ | Maximum gain | $\mathrm{G}_{\text {Lmax }}$ | -17 | -15 | -13 | dB | Gain $-15 \mathrm{~dB} \mathrm{f}=800 \mathrm{~Hz}$ VIN=1Vrms $\mathrm{G}_{\mathrm{L}}=20 \log (\mathrm{VOUT} / \mathrm{VIN})$ |
|  | Gain set error | GLERR | -2 | 0 | +2 | dB | Gain=-15 to -1dB |
| $\stackrel{x}{\sum}$ | Maximum boost gain | $\mathrm{GF}_{\text {FST }}$ | +13 | +15 | +17 | dB | $\begin{aligned} & \hline \text { Fader }=+15 \mathrm{~dB} \\ & \mathrm{~V}_{\text {IN }}=100 \mathrm{mVrms} \\ & \mathrm{G}_{\mathrm{F}}=20 \log (\mathrm{VOUT} / \mathrm{VIN}) \end{aligned}$ |
|  | Maximum attenuation * | $\mathrm{G}_{\mathrm{Fmin}}$ | - | -100 | -85 | dB | $\begin{aligned} & \text { Fader }=-\infty \mathrm{dB} \\ & \mathrm{G}_{\mathrm{F}}=20 \log (\mathrm{VOUT} / \mathrm{VIN}) \\ & \mathrm{BW}=\mathrm{IHF}-\mathrm{A} \end{aligned}$ |
|  | Input impedance | RIN_M | 70 | 100 | 130 | k $\Omega$ |  |
|  | Gain set error | $\mathrm{GF}_{\text {ERR }}$ | -2 | 0 | 2 | dB | Gain=+15 to 1dB |
|  | Attenuation set error 1 | $\mathrm{GF}_{\text {ERR1 }}$ | -2 | 0 | 2 | dB | ATT $=-1$ to -15 dB |
|  | Attenuation set error 2 | $\mathrm{GF}_{\text {ERR2 }}$ | -3 | 0 | 3 | dB | ATT $=-16$ to -47 dB |
|  | Attenuation set error 3 | $\mathrm{GF}_{\text {ERR } 3}$ | -4 | 0 | 4 | dB | ATT $=-48$ to-79dB |
|  | Maximum boost gain | $\mathrm{G}_{\mathrm{FSST}}$ | +13 | +15 | +17 | dB | $\begin{aligned} & \text { Fader }=+15 \mathrm{~dB} \\ & \mathrm{~V}_{\text {IN }}=100 \mathrm{mVrms} \\ & \mathrm{G}_{\mathrm{F}}=20 \log (\mathrm{VOUT} / \mathrm{VIN}) \end{aligned}$ |
|  | Maximum attenuation * | $\mathrm{GFmin}^{\text {min }}$ | - | -100 | -85 | dB | $\begin{aligned} & \text { Fader }=-\infty \mathrm{dB} \\ & \mathrm{G}_{\mathrm{F}}=20 \log (\text { VOUT } / \mathrm{VIN}) \\ & \mathrm{BW}=1 \mathrm{HF}-\mathrm{A} \end{aligned}$ |
|  | Gain set error | $\mathrm{GF}_{\text {ERR }}$ | -2 | 0 | 2 | dB | Gain $=+15$ to +1 dB |
|  | Attenuation set error 1 | $\mathrm{GF}_{\text {ERR1 }}$ | -2 | 0 | 2 | dB | ATT $=-1$ to -15 dB |
|  | Attenuation set error 2 | $\mathrm{G}_{\text {FERR2 }}$ | -3 | 0 | 3 | dB | ATT $=-16$ to -47 dB |
|  | Attenuation set error 3 | GFERR 3 | -4 | 0 | 4 | dB | ATT $=-48$ to -79 dB |
|  | Output impedance | Rout | - | - | 50 | $\Omega$ | $\mathrm{VIN}=100 \mathrm{mV} / \mathrm{ms}$ |
|  | Maximum output voltage | V ом | 2.0 | 2.2 | - | Vrms | $\begin{aligned} & \text { THD+N=1\% } \\ & \text { BW=400-30KHz } \end{aligned}$ |
|  | Maximum output voltage | $V_{\text {LMAX }}$ | 2.8 | 3.1 | 3.5 | V |  |
|  | Maximum offset voltage | $V_{\text {L OfF }}$ | - | 15 | 100 | mV |  |

[^0]Typical Performance Curve(s)


Figure 4. Vcc vs lq


Figure 6. THD vs Vin / Vo


Figure 5. Gain vs frequency


Figure 7. CMRR


Figure 8. PSRR


Figure 10. Loudness


Figure 9. CTC


Figure 11. Anti aliasing Filter


Figure 12. Bass gain vs frequency


Figure 14. Treble gain vs frequency


Figure 13. Middle gain vs frequency


Figure 15. LPF

## $I^{2} \mathrm{C}$ BUS CONTROL SIGNAL SPECIFICATION

(1) Electrical specifications and timing for bus lines and I/O stages


Figure 16. Definition of timing on the $I^{2} \mathrm{C}$-bus
Table 1 Characteristics of the SDA and SCL bus lines for $\mathrm{I}^{2} \mathrm{C}$-bus devices $\left(\mathrm{Ta}=25^{\circ} \mathrm{C}, \mathrm{VCC}=8.5 \mathrm{~V}\right.$ )

| Parameter |  | Symbol | Fast-mode ${ }^{2} \mathrm{C}$-bus |  | Unit |
| :---: | :--- | :---: | :---: | :---: | :---: |
|  | Min. |  |  |  |  |
| 1 | SCL clock frequency | fSCL | 0 | 400 | kHz |
| 2 | Bus free time between a STOP and START condition | tBUF | 1.3 | - | $\mu \mathrm{S}$ |
| 3 | Hold time (repeated) START condition. After this period, the <br> first clock pulse is generated | tHD;STA | 0.6 | - | $\mu \mathrm{S}$ |
| 4 | LOW period of the SCL clock | tLOW | 1.3 | - | $\mu \mathrm{S}$ |
| 5 | HIGH period of the SCL clock | tHIGH | 0.6 | - | $\mu \mathrm{S}$ |
| 6 | Set-up time for a repeated START condition | tSU;STA | 0.6 | - | $\mu \mathrm{S}$ |
| 7 | Data hold time | tHD;DAT | 0 | - | $\mu \mathrm{S}$ |
| 8 | Data set-up time | tSU;DAT | 100 | - | ns |
| 9 | Set-up time for STOP condition | tSU;STO | 0.6 | - | $\mu \mathrm{S}$ |

All values referred to VIH min. and VIL max. Levels (see Table 2).
Table 2 Characteristics of the SDA and SCL I/O stages for I ${ }^{2} \mathrm{C}$-bus devices

| Parameter |  | Symbol | Fast-mode devices |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Min. | Max. |  |
| 10 | LOW level input voltage |  | VIL | -0.3 | 1 | V |
| 11 | HIGH level input voltage | VIH | 2.3 | 5 | V |
| 12 | Pulse width of spikes which must be suppressed by the input filter. | tSP | 0 | 50 | ns |
| 13 | LOW level output voltage: at 3mA sink current | VOL1 | 0 | 0.4 | V |
| 14 | Input current each I/O pin with an input voltage between 0.4 V and 4.5 V | li | -10 | 10 | $\mu \mathrm{A}$ |



Figure 17
Command timing example in the I2C data transmission
(2) $I^{2} \mathrm{C}$ BUS FORMAT

|  MSB <br> S Slave Address |  | MSB |  | MSB |  | LSB |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | A | Select Address | A | Data | A | P |
| 1bit | 8bit | ```1bit 8bit 1bit 8bit 1bit 1b = Start conditions (Recognition of start bit) = Recognition of slave address. }7\mathrm{ bits in upper order are voluntary. The least significant bit is "L" due to writing.``` |  |  |  |  |  |
|  | S |  |  |  |  |  |  |
|  | Slave Address |  |  |  |  |  |  |
|  | A | = ACKNOWLEDGE bit (Recognition of acknowledgement) |  |  |  |  |  |
|  | Select Address | = Select every of volume, bass and treble. |  |  |  |  |  |
|  | Data | = Data on every volume and tone. |  |  |  |  |  |
|  | P | = Stop condition (Recognition of stop bit) |  |  |  |  |  |

(3) $\left.\right|^{2} \mathrm{C}$ BUS Interface Protocol

1) Basic form

| S | Slave Address | A | Select Address | A | Data | A | P |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| MSB |  |  | LSB | MSB | LSB | MSB | LSB |
|  |  |  |  |  |  |  |  |

2)Automatic increment(Select Address increases (+1) according to the number of data)

| S | Slave Address | A | Select Address | A | Data1 | A | Data2 | A | $\cdots$ | DataN | A | P |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |

(Example)(1)Data 1 is set as data of Select Address (20h).
(2)Data 2 is set as data of Select Address +1 (28h).
(3)Data N is set as data of Select Address $+\mathrm{N}-1$.
3)Configuration unavailable for transmission (In this case, only Select Address 1 is set.)

(Note)If any data is transmitted as Select Address 2 next to data, It is recognized as data, not as Select Address 2.
(4)Slave Address
MSB

| A6 | A5 | A4 | A3 | A2 | A1 | A0 | R $/ W$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |

(5)Select Address and Data

| Items | Select Address (hex) | MSB |  | Data |  |  |  | LSB |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 |
| Initial Setup | 01 | Advanced Switch ON/OFF | Anti Alias Filter ON/OFF | Advanced Switch Time of Volume/Fader /Tone/Loudness |  | 0 | 1 | Advanced Switch Time of Mute |  |
| LPF Setup | 02 | $\begin{gathered} \text { LPF } \\ \text { Phase } \\ 0 \% 180^{\circ} \end{gathered}$ | Level <br> Meter <br> Reset | Subwoofer Output Selector |  | Subwoofer Input Selector | Subwoofer LPF fc |  |  |
| Mixing Setup | 03 | Mixing Input Selector |  | 0 | Loudness f0 |  | $\begin{gathered} \mathrm{MIX} \text { _2CH } \\ \mathrm{ON} / \mathrm{OFF} \end{gathered}$ | $\begin{aligned} & \text { MIX_1CH } \\ & \text { ON/OFF } \end{aligned}$ | 1 |
| Input Selector | 05 | Full-diff Type | 0 | 0 | Input Selector |  |  |  |  |
| Input Gain | 06 | MUTE ON/OFF | 0 | 0 | Input Gain |  |  |  |  |
| Volume Gain | 20 | Volume Gain / Attenuation |  |  |  |  |  |  |  |
| Fader 1ch Front | 28 | Fader Gain / Attenuation |  |  |  |  |  |  |  |
| Fader 2ch Front | 29 | Fader Gain / Attenuation |  |  |  |  |  |  |  |
| Fader 1ch Rear | 2A | Fader Gain / Attenuation |  |  |  |  |  |  |  |
| Fader 2ch Rear | 2B | Fader Gain / Attenuation |  |  |  |  |  |  |  |
| Fader 1ch Sub | 2 C | Fader Gain / Attenuation |  |  |  |  |  |  |  |
| Mixing (2ch Sub) | 30 | Mixing Gain / Attenuation |  |  |  |  |  |  |  |
| Bass setup | 41 | 0 | 0 | Bass f0 |  | 0 | 0 | Bass Q |  |
| Middle setup | 44 | 0 | 0 | Middle f0 |  | 0 | 0 | Middle Q |  |
| Treble setup | 47 | 0 | 0 | Treble f0 |  | 0 | 0 | 0 | Treble Q |
| Bass Gain | 51 | Bass Boost/Cut | 0 | 0 | Bass Gain |  |  |  |  |
| Middle Gain | 54 | Middle Boost/Cut | 0 | 0 | Middle Gain |  |  |  |  |
| Treble Gain | 57 | Treble Boost/Cut | 0 | 0 | Treble Gain |  |  |  |  |
| Loudness Gain | 75 | 0 | Loudness Hi_Cut |  | Loudness Gain |  |  |  |  |
| System Reset | FE | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |

$\square$ : Advanced Switch
※(Set up bit (It is written with " 0 " by the above table) which hasn't been used in " 0 ".

## Note

1. In function changing of the hatching part, it works Advanced switch..
2. Upon continuous data transfer, the Select Address is circulated by the automatic increment function, as shown below.

3. For the function of input selector and subwoofer input select etc, it is not corresponded for advanced switch.

Therefore, please apply mute on the side of a set when changes these setting.
4. When using mute function of this IC at the time of changing input selector, please switch mute ON/OFF for waiting advanced-mute time.


Select address 02（hex）
Default：8＇h00

（＊1）：If Subwoofer Output Selector is set as 「Subwoofer」（＇b11），Mixing volume can be used as Subwoofer 2ch． Mixing volume and Subwoofer 2ch volume cannot be used simultaneously．
（＊2）：If＂Level Meter Reset＂is set as Reset（＂b1），a reset pulse will be outputted only once to a level meter block．
Also about this register，after a reset pulse output returns to a Hold（＂b0）state，without holding a Reset（＂b1）state． Therefore，in order to change into a Hold state，it is not necessary to carry out a register setup again．
（＊3）：If Subwoofer LPF fc is set as 「PASS」（＇b000），LPF PHASE is compulsorily fixed to $0{ }^{\prime}{ }^{\prime}$＇b0）．

Select address 03(hex)
Default:8'h00

| Function Name | Mode | Pin |  |  |  | MSB |  |  | HPF Setup |  |  | LSB |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | 1p | 1n | 2 n | 2 p | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 |
| Mixing 1ch ON/OFF | ON |  |  |  |  |  |  |  |  |  |  | 0 |  |
|  | OFF |  |  |  |  |  |  |  |  |  |  | 1 |  |
| Mixing 2ch ON/OFF | ON |  |  |  |  |  |  |  |  |  | 0 |  |  |
|  | OFF |  |  |  |  |  |  |  |  |  | 1 |  |  |
| Loudness f0 | 400 Hz |  |  |  |  |  |  |  | 0 | 0 |  |  |  |
|  | 800 Hz |  |  |  |  |  |  |  | 0 | 1 |  |  |  |
|  | 2400 Hz |  |  |  |  |  |  |  | 1 | 0 |  |  |  |
|  | Prohibition |  |  |  |  |  |  |  | 1 | 1 |  |  |  |
| Mixing Input Selector | Mix | MIN | - |  | MIN | 0 | 0 |  |  |  |  |  |  |
|  | A_Single | A1 | - |  | A2 | 0 | 1 |  |  |  |  |  |  |
|  | B_Single | B1 | - |  | B2 | 1 | 0 |  |  |  |  |  |  |
|  | Prohibition |  |  |  |  | 1 | 1 |  |  |  |  |  |  |

Select address 05(hex)

| Function Name | Mode | Pin |  |  |  | MSB |  |  | Input Selector |  |  | LSB |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | 1p | 1n | 2 n | 2p | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 |
| Input Selector | A_Single | A1 | - | - | A2 |  |  |  | 0 | 0 | 0 | 0 | 0 |
|  | B_Single | B1 | - | - | B2 |  |  |  | 0 | 0 | 0 | 0 | 1 |
|  | C_Single | C1 | - | - | C2 |  |  |  | 0 | 0 | 0 | 1 | 0 |
|  | D_Single | DP1 | - | - | DP2 |  |  |  | 0 | 0 | 0 | 1 | 1 |
|  | E1_Single | EP1 | - | - | EN1 |  |  |  | 0 | 1 | 0 | 1 | 0 |
|  | E2_Single | EN2 | - | - | EP2 |  |  |  | 0 | 1 | 0 | 1 | 1 |
|  | D_Diff | D_Diff |  |  | DP2 |  |  |  | 0 | 0 | 1 | 1 | 0 |
|  | E_Full_Diff | EP1 | EN1 | EN2 | EP2 |  |  |  | 0 | 1 | 0 | 0 | 0 |
|  | Prohibition |  |  |  |  |  |  |  | Other setting |  |  |  |  |
|  |  | Input | hort |  |  |  |  |  | 0 | 1 | 0 | 0 | 1 |
| Full-diff Type | Negative inputBias |  |  |  |  | 0 |  |  |  |  |  |  |  |
|  |  |  |  |  |  | 1 |  |  |  |  |  |  |  |



Select address 20 (hex) Default:8'h00

| Function Name | Mode | MSB |  |  | Volume Gain |  |  | LSB |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 |
| Volume Gain | Prohibition | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
|  |  | ! | : | ! | ! | ! | : | ! | : |
|  |  | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
|  | +15dB | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 1 |
|  | +14dB | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 |
|  | +13dB | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 |
|  | +12dB | 0 | 1 | 1 | 1 | 0 | 1 | 0 | 0 |
|  | +11dB | 0 | 1 | 1 | 1 | 0 | 1 | 0 | 1 |
|  | +10dB | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 0 |
|  | +9dB | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 1 |
|  | +8dB | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 |
|  | +7dB | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 1 |
|  | +6dB | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 |
|  | +5dB | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 |
|  | +4dB | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 0 |
|  | +3dB | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 1 |
|  | +2dB | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 |
|  | +1dB | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
|  | -0dB | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
|  | -1dB | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |
|  | -2dB | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 |
|  | -3dB | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 1 |
|  | -4dB | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 |
|  | -5dB | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 1 |
|  | -6dB | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 0 |
|  | -7dB | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 1 |
|  | -8dB | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 |
|  | -9dB | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 1 |
|  | -10dB | 1 | 0 | 0 | 0 | 1 | 0 | 1 | 0 |
|  | -11dB | 1 | 0 | 0 | 0 | 1 | 0 | 1 | 1 |
|  | -12dB | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 |
|  | -13dB | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 1 |
|  | -14dB | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 0 |
|  | -15dB | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 1 |
|  | -16dB | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
|  | -17dB | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 1 |
|  | -18dB | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 |
|  | -19dB | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 |
|  | -20dB | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 |


| Function Name | Mode | MSB |  |  | Volume Gain |  |  | LSB |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 |
| Volume Gain | -21dB | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 1 |
|  | -22dB | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 0 |
|  | -23dB | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 |
|  | -24dB | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 |
|  | -25dB | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 1 |
|  | -26dB | 1 | 0 | 0 | 1 | 1 | 0 | 1 | 0 |
|  | -27dB | 1 | 0 | 0 | 1 | 1 | 0 | 1 | 1 |
|  | -28dB | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 |
|  | -29dB | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 1 |
|  | -30dB | 1 | 0 | 0 | 1 | 1 | 1 | 1 | 0 |
|  | -31dB | 1 | 0 | 0 | 1 | 1 | 1 | 1 | 1 |
|  | -32dB | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 |
|  | -33dB | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 1 |
|  | -34dB | 1 | 0 | 1 | 0 | 0 | 0 | 1 | 0 |
|  | -35dB | 1 | 0 | 1 | 0 | 0 | 0 | 1 | 1 |
|  | -36dB | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 |
|  | -37dB | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 1 |
|  | -38dB | 1 | 0 | 1 | 0 | 0 | 1 | 1 | 0 |
|  | -39dB | 1 | 0 | 1 | 0 | 0 | 1 | 1 | 1 |
|  | -40dB | 1 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
|  | -41dB | 1 | 0 | 1 | 0 | 1 | 0 | 0 | 1 |
|  | -42dB | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 |
|  | -43dB | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 1 |
|  | -44dB | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 |
|  | -45dB | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 1 |
|  | -46dB | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 |
|  | -47dB | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 |
|  | -48dB | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
|  | -49dB | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 1 |
|  | -50dB | 1 | 0 | 1 | 1 | 0 | 0 | 1 | 0 |
|  | -51dB | 1 | 0 | 1 | 1 | 0 | 0 | 1 | 1 |
|  | -52dB | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 0 |
|  | -53dB | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 1 |
|  | -54dB | 1 | 0 | 1 | 1 | 0 | 1 | 1 | 0 |
|  | -55dB | 1 | 0 | 1 | 1 | 0 | 1 | 1 | 1 |
|  | -56dB | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 |
|  | -57dB | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 |
|  | -58dB | 1 | 0 | 1 | 1 | 1 | 0 | 1 | 0 |
|  | -59dB | 1 | 0 | 1 | 1 | 1 | 0 | 1 | 1 |
|  | -60dB | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 0 |
|  | -61dB | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 |
|  | -62dB | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 0 |
|  | -63dB | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
|  | -64dB | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
|  | -65dB | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 1 |
|  | -66dB | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 0 |
|  | -67dB | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 1 |
|  | -68dB | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 |
|  | -69dB | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 1 |
|  | -70dB | 1 | 1 | 0 | 0 | 0 | 1 | 1 | 0 |


| Function Name | Mode | MSB |  |  | Volume Gain |  |  | LSB |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 |
| Volume Gain | -71dB | 1 | 1 | 0 | 0 | 0 | 1 | 1 | 1 |
|  | -72dB | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
|  | -73dB | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 |
|  | -74dB | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 |
|  | -75dB | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 1 |
|  | -76dB | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 |
|  | -77dB | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 1 |
|  | -78dB | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 |
|  | -79dB | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 1 |
|  | Prohibition | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
|  |  | $\vdots$ | $\vdots$ | ! | : | $\vdots$ | : | : | ! |
|  |  | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 |
|  | '-79dB | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |

Select address 28, 29, 2A, 2B, 2C, 30(hex) Default:8'hFF

| Function Name | Mode | MSB |  |  | Fader / Mixing Gain |  |  | LSB |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 |
| Fader/Mixing Gain(*1) | Prohibition | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
|  |  | ! | $\vdots$ | ! | ! | ; | ! | ! | $\vdots$ |
|  |  | 0 | 1 |  | 1 | 0 | 0 | 0 | 0 |
|  | +15dB | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 1 |
|  | +14dB | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 |
|  | +13dB | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 |
|  | +12dB | 0 | 1 | 1 | 1 | 0 | 1 | 0 | 0 |
|  | +11dB | 0 | 1 | 1 | 1 | 0 | 1 | 0 | 1 |
|  | +10dB | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 0 |
|  | $+9 \mathrm{~dB}$ | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 1 |
|  | +8dB | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 |
|  | +7dB | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 1 |
|  | +6dB | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 |
|  | +5dB | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 |
|  | +4dB | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 0 |
|  | +3dB | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 1 |
|  | +2dB | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 |
|  | +1dB | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |

(*1): When Subwoofer Output Selector is set as 「Subwoofer」('b11), Mixing Gain (Select Address 30) becomes a gain setup of Subwoofer 2ch.



| Select address 41(hex) |  | Default:8'h00 |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Function Name | Mode | MSB |  |  |  |  |  |  | LSB |
|  |  | D7 | D6 |  |  |  | D2 | D1 | D0 |
| Bass Q | 0.5 |  |  |  |  |  |  | 0 | 0 |
|  | 1.0 |  |  |  |  |  |  | 0 | 1 |
|  | 1.5 |  |  |  |  |  |  | 1 | 0 |
|  | 2.0 |  |  |  |  |  |  | 1 | 1 |
| Bass f0 | 60 Hz |  |  | 0 | 0 |  |  |  |  |
|  | 80 Hz |  |  | 0 | 1 |  |  |  |  |
|  | 100 Hz |  |  | 1 | 0 |  |  |  |  |
|  | 120 Hz |  |  | 1 | 1 |  |  |  |  |


| Select address 44(hex) |  | Default:8'h00 |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Function Name | Mode | MSB |  |  |  |  |  | LSB |  |
| Function Name |  | D7 | D6 |  |  |  | D2 | D1 | D0 |
| Middle Q | 0.75 |  |  |  |  |  |  | 0 | 0 |
|  | 1.00 |  |  |  |  |  |  | 0 | 1 |
|  | 1.25 |  |  |  |  |  |  | 1 | 0 |
|  | 1.50 |  |  |  |  |  |  | 1 | 1 |
| Middle f0 | 0.5 kHz |  |  | 0 | 0 |  |  |  |  |
|  | 1kHz |  |  | 0 | 1 |  |  |  |  |
|  | 1.5 kHz |  |  | 1 | 0 |  |  |  |  |
|  | 2.5 kHz |  |  | 1 | 1 |  |  |  |  |




Select address 75(hex)
Default:8'h00

: Initial condition

Recommendation of VOLUME DIAGRAM
The example of the SET VOLUME DIAGRAM by Volume(SelectAddress 20(hex)) and Fader(SelectAddress $28,29,2 \mathrm{~A}, 2 \mathrm{~B}, 2 \mathrm{C}, 30$ (hex)) is explained in the following.

Example 1) It is recommended when a signal level is made to attenuate, a decline by Volume is done by -24 dB . It is adjusted with Fader after -24 dB . $\mathrm{S} / \mathrm{N}$ ratio can improve in comparison with the case that it is made to attenuate only with Volume.

| Display <br> $(※)$ | Total Gain <br> [dB] | Volume <br> $[\mathrm{dB}]$ | Fader <br> $[\mathrm{dB}]$ |
| :---: | :---: | :---: | :---: |
| 50 | 6 | 6 | 0 |
| 49 | 5 | 5 | 0 |
| 48 | 4 | 4 | 0 |
| 47 | 3 | 3 | 0 |
| 46 | 2 | 2 | 0 |
| 45 | 1 | 1 | 0 |
| 44 | 0 | 0 | 0 |
| 43 | -1 | -1 | 0 |
| 42 | -2 | -2 | 0 |
| 41 | -3 | -3 | 0 |
| 40 | -4 | -4 | 0 |
| 39 | -5 | -5 | 0 |
| 38 | -6 | -6 | 0 |
| 37 | -7 | -7 | 0 |
| 36 | -8 | -8 | 0 |
| 35 | -9 | -9 | 0 |
| 34 | -10 | -10 | 0 |
| 33 | -11 | -11 | 0 |
| 32 | -12 | -12 | 0 |
| 31 | -13 | -13 | 0 |
| 30 | -14 | -14 | 0 |
| 29 | -15 | -15 | 0 |
| 28 | -16 | -16 | 0 |
| 27 | -17 | -17 | 0 |
| 26 | -18 | -18 | 0 |


| Display <br> $(※)$ | Total Gain <br> [dB] | Volume <br> $[\mathrm{dB}]$ | Fader <br> $[\mathrm{dB}]$ |
| :---: | :---: | :---: | :---: |
| 25 | -19 | -19 | 0 |
| 24 | -20 | -20 | 0 |
| 23 | -21 | -21 | 0 |
| 22 | -22 | -22 | 0 |
| 21 | -23 | -23 | 0 |
| 20 | -24 | -24 | 0 |
| 19 | -26 | -24 | -2 |
| 18 | -28 | -24 | -4 |
| 17 | -30 | -24 | -6 |
| 16 | -32 | -24 | -8 |
| 15 | -34 | -24 | -10 |
| 14 | -36 | -24 | -12 |
| 13 | -38 | -24 | -14 |
| 12 | -40 | -24 | -16 |
| 11 | -42 | -24 | -18 |
| 10 | -44 | -24 | -20 |
| 9 | -46 | -24 | -22 |
| 8 | -48 | -24 | -24 |
| 7 | -50 | -24 | -26 |
| 6 | -52 | -24 | -28 |
| 5 | -54 | -24 | -30 |
| 4 | -56 | -24 | -32 |
| 3 | -58 | -24 | -34 |
| 2 | -60 | -24 | -36 |
| 1 | -62 | -24 | -38 |
| 0 | $-\infty$ | Mute | Mute |

Table 1. A decline by Volume is done by -24 dB . It is adjusted with Fader after -24 dB .
(※Display=SET VOLUME)
※When a attenuate after -32dB is used with Volume, in case of use Subwoofer Input Selector = Input Selector
(SelectAddress 02(hex), D3 = 1), Output level of OUTS1/S2 is attenuated 「Volume - (-31dB) 」.


Figure 18. Subwoofer Input Selector $=$ Input Selector $($ SelectAddress 02(hex), D3 = 1)

| Volume [dB] | OUTS1/S2 [dB] |  | Volume [dB] | OUTS1/S2 [dB] |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Select Address 20(hex) | Subwoofer Input Selector=Loudness (Select Address 02(hex), D3=0) | Subwoofer Input Selector= Input Selector (Select Address 02(hex), D3=1) | Select Address 20(hex) | Subwoofer Input Selector=Loudness (Select Address 02(hex), D3=0) | Subwoofer Input Selector= Input Selector (Select Address 02(hex), D3=1) |
| 6 | 6 | 0 | -19 | -19 | 0 |
| 5 | 5 | 0 | -20 | -20 | 0 |
| 4 | 4 | 0 | -21 | -21 | 0 |
| 3 | 3 | 0 | -22 | -22 | 0 |
| 2 | 2 | 0 | -23 | -23 | 0 |
| 1 | 1 | 0 | -24 | -24 | 0 |
| 0 | 0 | 0 | -26 | -26 | 0 |
| -1 | -1 | 0 | -28 | -28 | 0 |
| -2 | -2 | 0 | -30 | -30 | 0 |
| -3 | -3 | 0 | -32 | -32 | -1 |
| -4 | -4 | 0 | -34 | -34 | -3 |
| -5 | -5 | 0 | -36 | -36 | -5 |
| -6 | -6 | 0 | -38 | -38 | -7 |
| -7 | -7 | 0 | -40 | -40 | -9 |
| -8 | -8 | 0 | -42 | -42 | -11 |
| -9 | -9 | 0 | -44 | -44 | -13 |
| -10 | -10 | 0 | -46 | -46 | -15 |
| -11 | -11 | 0 | -48 | -48 | -17 |
| -12 | -12 | 0 | -50 | -50 | -19 |
| -13 | -13 | 0 | -52 | -52 | -21 |
| -14 | -14 | 0 | -54 | -54 | -23 |
| -15 | -15 | 0 | -56 | -56 | -25 |
| -16 | -16 | 0 | -58 | -58 | -27 |
| -17 | -17 | 0 | -60 | -60 | -29 |
| -18 | -18 | 0 | -62 | -62 | -31 |

Table 2. Subwoofer Input Selector = Input Selector (SelectAddress 02(hex), D3 = 1)
Volume attenuation vs Output Level of OUTS1/S2

## About loudness

When Loudness is set up in on, signal level in fo (set up by (SelectAddress 03(hex), D3,D4))is attenuated) is made attenuated.
Therefore to make it put emphasis on the low and high band, use volume together


Figure 19. Loudness gain vs frequency ( $\mathrm{f} 0=800 \mathrm{~Hz}$ )

| Display (※) | Total Gain A [dB] | Total Gain B [dB] | Volume [dB] | Loudness <br> [dB] | Fader [dB] | Display | Total Gain | Total Gain | Volume | Loudness | Fader [dB] |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 50 | 6 | 6 | 6 | 0 | 0 |  | A [dB] | B [dB] |  |  |  |
| 49 | 5 | 5 | 5 | 0 | 0 | 25 | -10 | -19 | -10 | -9 | 0 |
| 48 | 4 | 4 | 4 | 0 | 0 | 24 | -10 | -20 | -10 | -10 | 0 |
| 47 | 3 | 3 | 3 | 0 | 0 | 23 | -10 | -21 | -10 | -11 | 0 |
| 46 | 2 | 2 | 2 | 0 | 0 | 22 | -10 | -22 | -10 | -12 | 0 |
| 45 | 1 | 1 | 1 | 0 | 0 | 21 | -10-10 | -23 | -10 | -13 | 0 |
| 44 | 0 | 0 | 0 | 0 | 0 | 20 | -10 | -24 | -10 | -14 | 0 |
| 43 | -1 | -1 | -1 | 0 | 0 | 19 | -12 | -26 | -10 | -14 | -2 |
| 42 | -2 | -2 | -2 | 0 | 0 | 18 | -14 | -28 | -10 | -14 | -4 |
| 41 | -3 | -3 | -3 | 0 | 0 | 17 | -16 | -30 | -10 | -14 | -6 |
| 40 | -4 | -4 | -4 | 0 | 0 | 16 | -18 | -32 | -10 | -14 | -8 |
| 39 | -5 | -5 | -5 | 0 | 0 | 15 | -20 | -34 | -10 | -14 | -10 |
| 38 | -6 | -6 | -6 | 0 | 0 | 14 | -22 | -36 | -10 | -14 | -12 |
| 37 | -7 | -7 | -7 | 0 | 0 | 13 | -24 | -38 | -10 | -14 | -14 |
| 36 | -8 | -8 | -8 | 0 | 0 | 12 | -26 | -40 | -10 | -14 | -16 |
| 35 | -9 | -9 | -9 | 0 | 0 | 11 | -28 | -42 | -10 | -14 | -18 |
| 34 | -10 | -10 | -10 | 0 | 0 | 10 | -30 | -44 | -10 | -14 | -20 |
| 33 | -10 | -10 | -10 | -1 | 0 | 9 | -32 | -46 | -10 | -14 | -22 |
| 32 | -10 | -10 | -10 | -2 | 0 | 8 | -34 | -48 | -10 | -14 | -24 |
| 31 | -10 | -10 | -10 | -3 | 0 | 7 | -36 | -50 | -10 | -14 | -26 |
| 30 | -10 | -10 | -10 | -4 | 0 | 6 | -38 | -52 | -10 | -14 | -28 |
| 29 | -10 | -10 | -10 | -5 | 0 | 5 | -40 | -54 | -10 | -14 | -30 |
| 28 | -10 | -10 | -10 | -6 | 0 | 4 | -42 | -56 | -10 | -14 | -32 |
| 27 | -10 | -10 | -10 | -7 | 0 | 3 | -44 | -58 | -10 | -14 | -34 |
| 26 | -10 | -10 | -10 | -8 | 0 | 2 | -46 | -60 | -10 | -14 | -36 |
|  |  |  |  |  |  | 1 | -48 | -62 | -10 | -14 | -38 |
|  |  |  |  |  |  | 0 | $-\infty$ | $-\infty$ | Mute | -14 | Mute |

Table 3. A decline by Volume is done by -24 dB . It is adjusted with Fader after -24 dB . Loudness=ON (※Display=SET VOLUME)


Figure 20. Gain vs frequency of Table. 3

## Attention about Loudness ON/OFF

To make it put emphasis on the low and high band, when it is made to boost with Volume so long as it was made to attenuate with Loudness.

- Loudness OFF $\rightarrow$ ON : Send data of loudness before volume
- Loudness ON $\rightarrow$ OFF : Send data of volume before loudness
※Transmit data in the above turn. A signal level declines first, and it is amplified after that. And so natural switching can be realized.

$$
\text { Display }=17, \text { Total Gain }=-30 \mathrm{~dB}
$$



Figure 21. example of data sending about Loudness ON/OFF
(6) About power on reset

At on of supply voltage circuit made initialization inside IC is built-in. Please send data to all address as initial data at supply voltage on. And please supply mute at set side until this initial data is sent.)

| Item | Symbol | Limit |  |  | Unit | Condition |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Typ. | Max. |  |  |  |
| Rise time of VCC | Trise | 33 | - | - | usec | VCC rise time from 0V to 5V) |
| VCC voltage of <br> release power on <br> reset | Vpor | - | 4.1 | - | V |  |

(7)About external compulsory mute terminal

Mute is possible forcibly than the outside after input again department, by the setting of the MUTE terminal.

| Mute Voltage Condition | Mode |
| :---: | :---: |
| GND to 1.0 V | MUTE ON |
| 2.3 V to 5.0 V | MUTE OFF |

Establish the voltage of MUTE in the condition to have been defined.)

## About VCC response of VREF terminal)

Output voltage of VREF terminal keep fixed voltage in operational range.


Figure 22. OUT(18 to 23pin)_DC-Bias $=4.15 \mathrm{~V}$ fixed.

## About Advanced switching circuit

【1】 About Advanced switch
1－1．Effect of Advanced switch
It is the ROHM original technology for prevention of switching noise．When gain switching such as volume and tone control is done momentarily，a music signal doesn＇t continue，and unpleasant shock noise is made．Advanced switch can reduce shock noise with the technology which signal wave shape is changed to gently so that a music signal may not continue drastically．


Advanced switch starts switching after the control data transmitted by a microcomputer are received．It takes one fixed time，and wave shape transits as the above figure．The data transmitted by a microcomputer are processed inside， and the most suitable movement is done inside the IC so that switching shock noise may not be made．）

But，it presumes by the transmitting timing when it doesn＇t become intended switching wave shape because it is the function which needs time．The example which relations with the switching time of the data transmitting timing and the reality were shown in is given to it in the following．It asks for design when it is confirmed well．

About a kind of transmission method
－A data setup（by the data format，the thing which isn＇t indicated by gray）except for the item for advanced switch There is no regulation in transmission specially．
－The data setup（by the data format，the thing which gray indication is）of the item for advanced switch
Though there is no regulation in data transmission，the switching order when data are transmitted to several blocks follows the

> next 2-3.

【2】About transmission DATA of advanced switching item

## 2－1．About switching time of advanced switch

advanced switch ON／OFF is set up in ON to make advanced switch function effective．
And，though it becomes the same completely，the movement time of advanced switch can set up only MUTE in the independence．
As for these，set it up referring to select address 01（hex）of the data format．
There are transition time to be equivalent to the setup value，and treatment time（effect－less time）inside the IC in advanced switch．Therefore，actual switching time（Tsoft and Tsoft，MUTE）is defined as follows．


Setup value 11.2 msec should be recommended in switch time of advanced switch．But，when a shock noise level during gain switching isn＇t sufficient，it has the possibility that it can be reduced by setting it up more this long on the actual use．But，be careful because an response in switching of around one time becomes slow when you lengthen time．

2-2. About the data transmitting timing in same block state and the switching movement.

- Transmitting example 1

A time chart to the switching start has become the next since the data transmission.
The interval of the same blocks is fully left, and the example when data are transmitted is shown first.
And, as for enough interval, it becomes the time when setup time was multiplied by the dispersion margin 1.4


- Transmitting example 2

Next, when a transmitting interval isn't sufficient, the example of (When it is shorter than the above interval.) is shown. The next switching movement is started in succession after that movement is finished when data are transmitted during the first switching movement.


■ Transmitting example 3
Next, the example of the switcr'ng movement when a transmitting interval was shortened more is shown. It has the buffer which memorizes data inside the IC, and a buffer always does transmitting data. But, data of +6 dB which transmitted to the second become invalid with this example to hold only the latest data.


## - Transmitting example 4

As for handling of refresh-data, advanced switch movement isn't started to judge for present setup data and a difference that to be inside the IC)


2-3. About the data transmitting timing in several block state and the switching movement.)
When data are transmitted to several blocks, treatment in the BS (block state) unit is carried out inside the IC.
The movement start order of adbanced switch is decided by BS in advance.)


Figure 23. The order of advanced switch start
※It is possible in the block in the same BS that switching is started at the same timing.
-Transmitting example 5
The timing of the switching start follows the figure of the former page though there is no restriction of the $\mathrm{I}^{2} \mathrm{C}$ BUS data transmitting timing as it explained in the former knot even if it is related to the transmission to several blocks.
Therefore, it isn't based on the data transmitting order, and an actual switching turn becomes the turn of the upper figure. (Transmitting example 6)
Each block data is being transmitted with the transmitting example 5 separately. But, it becomes the same result even if data are transmitted in bulk.)


Transmitting example 6）
Switching of the next BS is done after the present switching completion when an actual switching order is different from the transmitting order，and data except for the same BS are transmitted at the timing when advanced switch movement isn＇t finished．）


The case that the same BS3 and BS2 were transmitted during BS1 switching is shown with the next example． （Transmitting example 7）

■Transmitting example 7


## 2－4．About Gain switch of TONE（Bass／Middle／Treble）

It becomes two－step transition movement that it passed through OdB 【 Gain of Bass／Middle／Treble 】 to prevent the occurrence of the switching noise when Gain is changed from boost to the cut（or，from the cut，boost）．And，when boost／cut doesn＇t change，it is the same as 【2－2】【2－3】．But，it is in the same way as other switching as advanced switch switching time．）
－Transmitting example 8
When it is changed Bass＋15dB from Bass－15dB．（Initial ：Bass－15dB）


【3】 About advanced switch of MUTE
Advanced switch of MUTE is controllable independently of other advanced switch.
There is no regulation about the timing to which MUTE is applied, and the timing to cancel.

■Transmitting example 9 Normal MUTE ON/OFF


Transmitting example 10 The movement when it was canceled earlier than advanced switch time of MUTE


## Advanced switch transmitting timing list

（1）【 MUTE】

|  | Stand－by advanced switch |  | working advanced switch |
| :---: | :---: | :---: | :---: |
| Data transfer timing | No constraint |  | No constraint |
| Operation start timing | After send data |  | After send data |
| Advanced switch time | $\mathrm{T}_{\text {soft＿MUTE }}{ }^{* 1}$ |  | $\mathrm{~T}_{\text {soft＿MUTE }}$ |

（2）【 VOL／FAD（F1，F2，R1，R2，S1，MIX＿VOL（S2））／TONE（BAS，MID．TRE，LOUD）】

|  | Stand－by advanced switch |  | working advanced switch |
| :---: | :---: | :---: | :---: |
| Data transfer timing | No constraint |  |  |
| Operation start timing | After send data |  | No constraint |
| Advanced switch time | $\mathrm{T}_{\text {soft }} *^{2}$ |  | $\mathrm{~T}_{\text {soft }}$ |

（3）【 TONE BOOST $\Leftrightarrow$ CUT】

|  | Stand－by advanced switch |  | working advanced switch |
| :---: | :---: | :---: | :---: |
| Data transfer timing | No constraint |  | No constraint |
| Operation start timing | After send data |  | After finished current work |
| Advanced switch time | $\mathrm{T}_{\text {soft }}{ }^{* 3}$ |  | $\mathrm{T}_{\text {soft }}$ |

※1 As for $\mathrm{T}_{\text {soft，MUTE，}}$ mentioned advanced switch of MUTE movement time is expressed to the data format． It is equivalent by the explanation in the body in one block．
※2 As for Tsoft，the time of two times of mentioned advanced movement time is expressed to the data format． It changes to block it in the same way by the explanation in the body by the continuance twice．
※3 About $T_{\text {soft }}$ of TONE BOOST $\Leftrightarrow$ CUT，advanced switch treatment time is in the same way as the switching time of the above（ $(22$ ）though two times hang on the movement time because this movement comes to switch that it passes through 0dB automatically inside the IC．

## Application Circuit Diagram



## Notes on wiring

(1) Please connect the decoupling capacitor of a power supply in the shortest distance as much as possible to GND.
(2) Lines of GND shall be one-point connected.
(3) Wiring pattern of Digital shall be away from that of analog unit and cross-talk shall not be acceptable.
(4) Lines of SCL and SDA of ${ }^{2} \mathrm{C}$ BUS shall not be parallel if possible. The lines shall be shielded, if they are adjacent to each other.
(5) Lines of analog input shall not be parallel if possible. The lines shall be shielded, if they are adjacent to each other.

## Thermal Derating Curve

About the thermal design by the IC
Characteristics of an IC have a great deal to do with the temperature at which it is used, and exceeding absolute maximum ratings may degrade and destroy elements. Careful consideration must be given to the heat of the IC from the two standpoints of immediate damage and long-term reliability of operation.


Figure 25. Temperature Derating Curve
Note) Values are actual measurements and are not guaranteed.
Power dissipation values vary according to the board on which the IC is mounted.

Terminal Equivalent Circuit and Description

| Terminal No. | Terminal Name | Terminal Voltage | Equivalent Circuit | Terminal Description |
| :---: | :---: | :---: | :---: | :---: |
| $\begin{gathered} 1 \\ 2 \\ 3 \\ 4 \\ 5 \\ 6 \\ 7 \\ 8 \\ 8 \\ 9 \\ 10 \\ 11 \\ 12 \\ 13 \\ 14 \\ \hline \end{gathered}$ | $\begin{gathered} \mathrm{A} 1 \\ \mathrm{~A} 2 \\ \mathrm{~B} 1 \\ \mathrm{~B} 2 \\ \mathrm{C} 1 \\ \mathrm{C} 2 \\ \mathrm{DP} 1 \\ \mathrm{DN} \\ \text { DP2 } \\ \text { EP1 } \\ \text { EN1 } \\ \text { EN2 } \\ \text { EP2 } \\ \text { MIN } \end{gathered}$ | 4.15 |  | A terminal for signal input. <br> The input impedance of $A$ to $E$ is $100 \mathrm{k} \Omega$ (typ). |
| 15 | MUTE | - |  | A terminal for external compulsory mute. If terminal voltage is High level, the mute is off. And if the terminal voltage is Low level, the mute is on. |
| 17 | LOUT |  |  | A terminal for Level meter output. <br> The output impedance is $10 \mathrm{k} \Omega$ (typ). |
| $\begin{aligned} & 18 \\ & 19 \\ & 20 \\ & 21 \\ & 22 \\ & 23 \end{aligned}$ | OUTS2 <br> OUTS1 <br> OUTR2 <br> OUTR1 <br> OUTF2 <br> OUTF1 | 4.15 |  | A terminal for fader and Subwoofer output. |

[^1]| Terminal No. | Terminal <br> Name | Terminal Voltage | Equivalent Circuit | Terminal Description |
| :---: | :---: | :---: | :---: | :---: |
| 24 | VCC | 8.5 |  | Power supply terminal. |
| 25 | SCL | - |  | A terminal for clock input of $I^{2} \mathrm{C}$ BUS communication. |
| 26 | SDA | - |  | A terminal for data input of $I^{2} C$ BUS communication. |
| 16 | LRST | - |  | A terminal for level meter reset. If terminal voltage is High level, the reset is on. And if the terminal voltage is Low level, the reset is off. |
| 27 | GND | 0 |  | Ground terminal. |
| 28 | VREF | 4.15 |  | Voltage for reference bias of analog signal system. The simple precharge circuit and simple discharge circuit for an external capacitor are built in. |

[^2]Notes for use
1.Absolute maximum rating voltage

When it impressed the voltage on VCC more than the absolute maximum rating voltage, circuit currents increase rapidly, and there is absolutely a case to reach characteristic deterioration and destruction of a device. In particular in a serge examination of a set, when it is expected the impressing serge at VCC terminal (15pin), please do not impress the large and over the absolute maximum rating voltage (including a operating voltage + serge ingredient (around 14V)).

## 2.About a signal input part

1)In the signal input terminal, the constant setting of input coupling capacitor $C(F)$ be sufficient input impedance $\operatorname{RIN}(\Omega)$ inside IC and please decide. The first HPF characteristic of RC is composed.



$$
A(f)=\sqrt{\frac{(2 \pi \mathrm{fCRIN})^{2}}{1+(2 \pi \mathrm{fCRIN})^{2}}}
$$

2) SHORT mode is the command which makes switch $\mathrm{S}_{\mathrm{SH}}=\mathrm{ON}$ an input selector part and input impedance RIN of all terminals, and makes resistance small. Switch S $_{S H}$ is OFF when not choosing a SHORT command.
A constant time becomes small at the time of this command twisting to the resistance inside the capacitor connected outside and LSI. The charge time of a capacitor becomes short. Since SHORT mode turns ON the switch of $\mathrm{S}_{\mathrm{SH}}$ and makes it low impedance, please use it at the time of a non-signal.
3.About Mute terminal(15pin) when power supply is off

Any voltage shall not be supplied to Mute terminal (15pin) when power-supply is off.
Please insert a resistor (about $2.2 \mathrm{k} \Omega$ ) to Mute terminal in series, if voltage is supplied to mute terminal in case. (Please refer Application Circuit Diagram.)

## Ordering Information



## ME 2

Packaging and forming specification
E2: Embossed tape and reel
(SSOP-B28)

Physical Dimension Tape and Reel Information

## SSOP-B28



## Marking Diagram(s)(TOP VIEW)

SSOP-B28(TOP VIEW)


## Revision history

| Date | Revision |  | Changes |
| :---: | :---: | :--- | :---: |
| 3.APR.2013 | 001 | New Release |  |


| Date | Revision | Changes |
| :---: | :---: | :--- |
| 4. NOV.2013 | 002 | All page <br> delete |
| 4. NOV.2013 | 002 | Page 2 <br> < Sub Title> <br> Sound Processors for Car Audios $\Rightarrow$ Analog Audio Processors series change. <br> <Feature> <br> AEC-Q100 Qualified add. <br> <Logo> <br> PbFree, RoHS delete |
| 4. NOV.2013 | 002 | Page 3 <br> Power Dissipation mW $\Rightarrow \mathrm{W}$ change. |
| 4.NOV.2013 | 002 | Page 19 <br> SelectAddress 47hex, middle $\Rightarrow$ treble change. |
| 4.NOV.2013 | 002 | Page 37 <br> Ordering Information, E2 $\Rightarrow$ ME2 change. |

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| :---: | :---: | :---: | :---: |
| CLASSIII | CLASSIII | CLASS II b | CLASSIII |
|  |  | CLASSIII |  |

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[e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
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[h] Use of the Products in places subject to dew condensation
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For details, please refer to ROHM Mounting specification

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[^0]:    *VP-9690A(Average value detection, effective value display) filter by Matsushita Communication is used for * measurement. Phase between input / output is same.

[^1]:    The figure in the pin explanation and input/output equivalent circuit is reference value, it doesn't guarantee the value.

[^2]:    The figure in the pin explanation and input/output equivalent circuit is reference value, it doesn't guarantee the value.

