

Protective Compliance

Small-sized Class-D Speaker Amplifiers Analog Input Monourol Class D Sm

Monaural Class-D Speaker Amplifier

BD5460GUL

No.10101EAT01

Description

BD5460GUL is a low voltage drive class-D monaural speaker amplifier that was developed for cellular phones, mobile audio products and the others.LC-filter of speaker output is unnecessary, and the number of external components is three. It is suitable for the application of battery drive because of high efficiency and low power consumption.

Also, stand-by current is 0µA (typ.), and fast transitions from standby to active with little pop noise. It is suitable for applications that switch repeatedly between stand-by and active.

Features

- 1) No LC filter required
- 2) Only three external components
- 3) High power 2.5W/4 Ω /BTL (VDD=5V, RL=4 Ω , THD+N=10%, typ.)
- 4) High power 0.85W/8Ω/BTL (VDD=3.6V, RL=8Ω, THD+N=10%, typ.)
- 5) Gain 6dB
- 6) Analog differential input / PWM digital output
- 7) Pop noise suppression circuitry
- 8) Built-in standby function
- 9) Protection circuitry (Short protection [Auto recover without power cycling], Thermal shutdown, Under voltage lockout)
- 10) Very small package 9-Bump WL-CSP (1.6*1.6*0.55mmMAX)

Applications

Mobile phones, Mobile electronics applications

●Absolute Maximum Ratings(Ta=25°C)

| Item | Symbol | Ratings | Unit | | |
|-------------------------------|--------|--------------|------|--|--|
| Power Supply Voltage | VDD | 7.0 | V | | |
| Power Dissipation | Pd | 690 *1 | mW | | |
| Storage Temperature Range | Tstg | -55 ~ +150 | °C | | |
| STBY Terminal Input Range | Vstby | -0.1~VDD+0.1 | V | | |
| IN+, IN- Terminal Input Range | Vin | -0.1~VDD+0.1 | V | | |

*1 When mounted on a 50 mm × 58mm Rohm standard board, reduce by 5.52 mW/°C above Ta = +25 °C.

Operating Conditions

| Item | Symbol | Ratings | Unit |
|----------------------|--------|-------------|------|
| Power Supply Voltage | VDD | +2.5 ~ +5.5 | V |
| Temperature Range | Topr | -40 ~ +85 | °C |

X This product is not designed for protection against radioactive rays

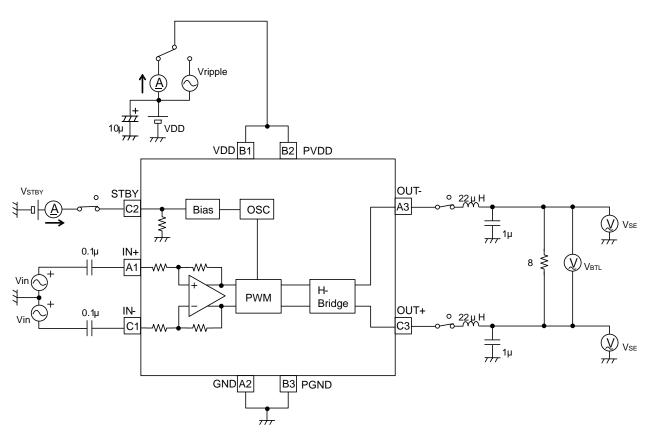
Electric Characteristics

(Unless otherwise specified, Ta=25°C, VDD=3.6V, f=1kHz, RL=8 Ω , AC item=LC Filter ; L=22 μ H, C=1 μ F)

| | , , | , | , | , | | | · · · · · · |
|------------------------------|------------|--------------------|--------|------|------|---------------|-------------------------------|
| Parameter | | Question | Limits | | 1.1 | O an ditian a | |
| | | Symbol | MIN. | TYP. | MAX. | Unit | Conditions |
| Circuit current (No signa | al) | I _{CC} | | 2.0 | 4.0 | mA | Active mode, No load |
| Circuit current (Standby | () | I _{STBY} | — | 0.1 | 2 | μA | Standby mode |
| Output power 1 | | P ₀₁ | 450 | 680 | _ | mW | BTL, f=1kHz, THD+N=1% *1 |
| Output power 2 | | P _{O2} | 550 | 850 | _ | mW | BTL, f=1kHz, THD+N=10% *1 |
| Voltage gain | | Gv | 5.4 | 5.9 | 6.4 | dB | BTL |
| Power Supply Rejection Ratio | | P _{SRR} | 45 | 53 | _ | dB | BTL,f=1kHz, Vripple=0.1Vpp *2 |
| Output offset voltage | | ΔVo | -25 | 0 | +25 | mV | Vin=0V, BTL |
| Switching Frequency | | fosc | 175 | 250 | 325 | kHz | |
| Start-up time | | Ton | 0.39 | 0.51 | 0.73 | msec | |
| Standby input Voltage | High-level | V _{STBYH} | 1.4 | — | VDD | V | Active mode |
| | Low-level | V _{STBYL} | 0 | — | 0.4 | V | Standby mode |
| Standby input current | High-level | I _{STBYH} | 6 | 12 | 18 | μA | V _{STBY} =3.6V |
| | Low-level | I _{STBYL} | -5 | 0 | 5 | μA | V _{STBY} =0V |

*BTL=Bridged Tied Load (Voltage between A3-C3.), *1;B.W.=400~30kHz,*2;DIN AUDIO

Measurement Circuit Diagram

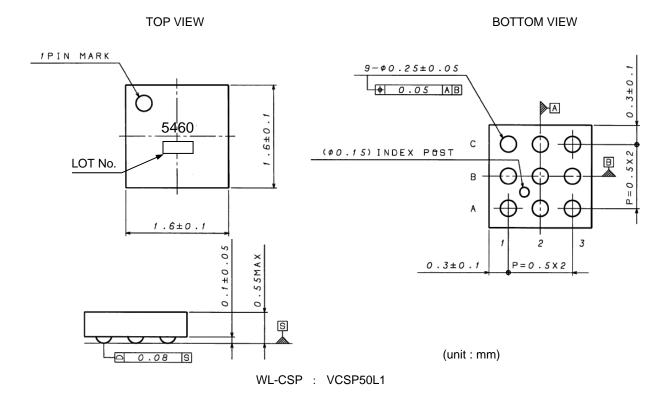


Active / Standby Control

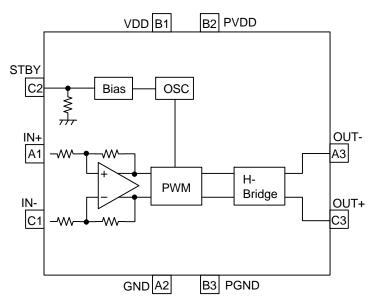
| STBY | Pin(| C2 | pin |) |
|------|------|----|-----|---|
| | | | | |

| Mode | Pin level | Conditions | |
|---------|-----------|-------------|--|
| Active | Н | IC active | |
| Standby | L | IC shutdown | |

Package Outlines



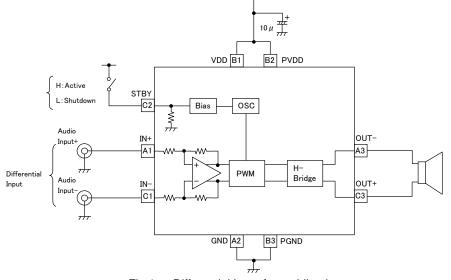
Block Diagram

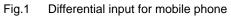


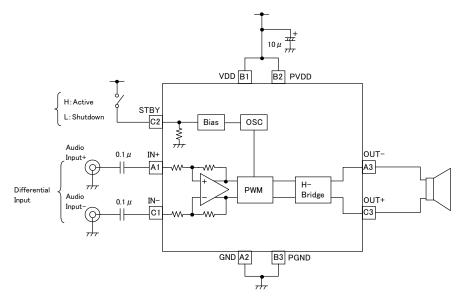
Pin Assignment Chart

| PIN No. | PIN 名 |
|---------|-------|
| A1 | IN+ |
| A2 | GND |
| A3 | OUT- |
| B1 | VDD |
| B2 | PVDD |
| B3 | PGND |
| C1 | IN- |
| C2 | STBY |
| C3 | OUT+ |

Application Circuit Example









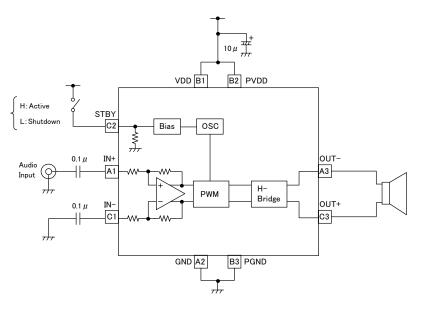
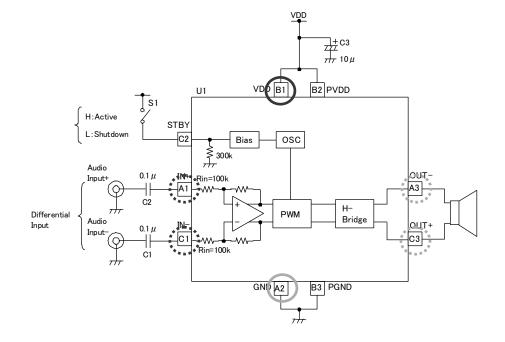
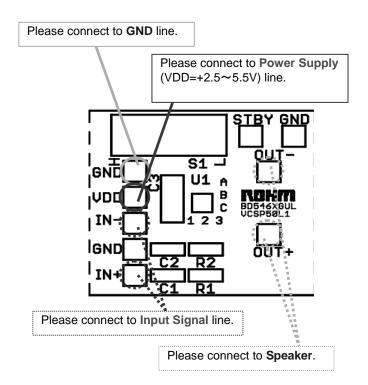


Fig.3 Single-Ended input

Evaluation board Circuit Diagram





•Evaluation board Parts List

| Qty. | Item | Description | SMD Size | Manufacturer/ Part Number |
|------|--------|--|--------------------------------|------------------------------|
| 2 | C1, C2 | Capacitor, 0.1µF | 0603 | Murata GRM188R71C104KA01D |
| 1 | C3 | Capacitor, 10µF | A (3216) | ROHM TCFGA1A106M8R |
| 1 | S1 | Slide Switch | 4mm X 10.2mm | NKK SS-12SDP2 |
| 1 | U1 | IC, BD5460GUL, Mono Class-D audio amplifier | 1.6mm X 1.6mm WLCSP Package | ROHM BD5460GUL |
| 1 | PCB1 | Printed-circuit board, BD5460GUL EVM | — | — |

Description of External components

 Input coupling capacitor (C1,C2) It makes a Input coupling capacitor 0.1μF. Input impedance is 100kΩ (Typ.).

It sets cut-off frequency fc by the following formula by input coupling capacitor C1 (=C2) and input impedance Ri.

$$fc = \frac{1}{2\pi \times Ri \times C1} [Hz]$$

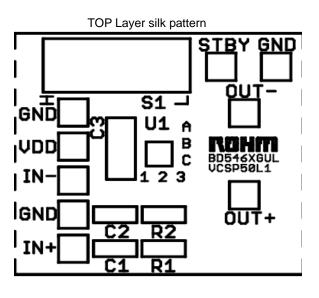
In case of Ri =100k Ω , C1 (=C2) =0.1 μ F, it becomes fc = about 16 Hz.

② Power decoupling capacitor (C3)

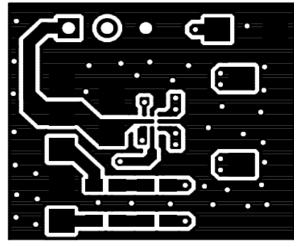
It makes a power decoupling capacitor 10 µF.

When making capacitance of the power decoupling capacitor, there is an influence in the Audio characteristic. When making small, careful for the Audio characteristic at the actual application.

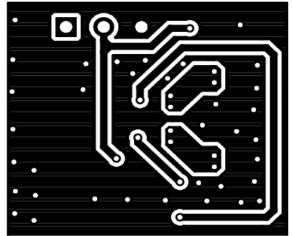
•Evaluation board PCB layer



TOP Layer

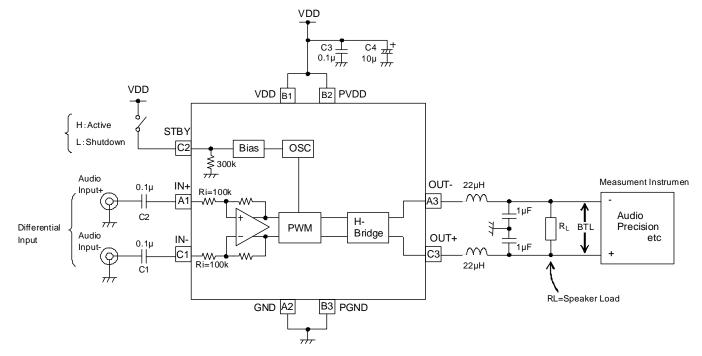


Bottom Layer



The way of evaluating audio characteristic

Evaluation Circuit Diagram



When measuring audio characteristics, insert LC filter during the output terminal of IC and the speaker load and measure it. Arrange LC filter as close as possible to the output terminal of IC. In case of L=22 μ H, C=1 μ F, the cut-off frequency becomes the following.

$$fc = \frac{1}{2\pi\sqrt{LC}} = \frac{1}{2\pi\sqrt{22\mu H \times 1\mu F}} \cong 34\,\text{kHz}$$

Use a big current type - Inductor L. (Reference)

TDK: SLF12575T-220M4R0

BD5460GUL

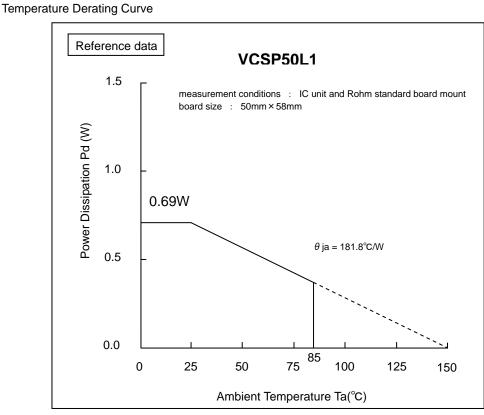
About the thermal design by the IC

Characteristics of an IC have a great deal to do with the temperature at which it is used, and exceeding absolute maximum ratings may degrade and destroy elements. Careful consideration must be given to the heat of the IC from the two standpoints of immediate damage and long-term reliability of operation. Pay attention to points such as the following.

Since an maximum junction temperature (Tjmax.) or operating temperature range (Topr) is shown in the absolute maximum ratings of the IC, to reference the value, find it using the Pd-Ta characteristic (temperature derating curve).

If an input signal is too great when there is insufficient radiation, TSD (thermal shutdown) may operate.

TSD, which operates at a chip temperature of approximately +180°C, is canceled when this goes below approximately +100°C.Since TSD operates persistently with the purpose of preventing chip damage, be aware that long-term use in the vicinity that TSD affects decrease IC reliability.



Note) Values are actual measurements and are not guaranteed.

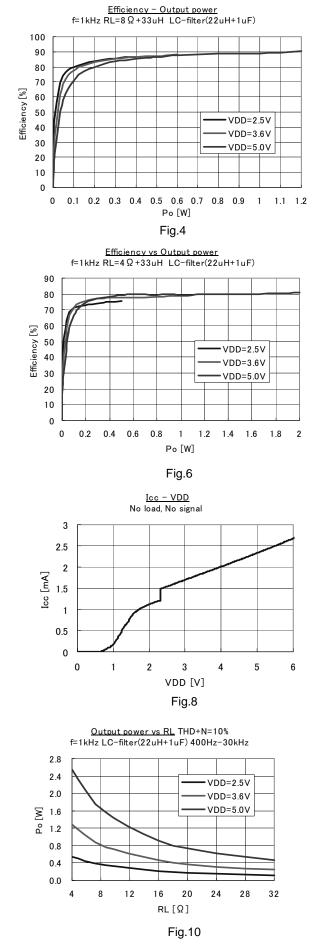
Power dissipation values vary according to the board on which the IC is mounted. The Power dissipation of this IC when mounted on a multilayer board designed to radiate is greater than the values in the graph above.

Typical Characteristics

Table of graphs

| Items | Parameter | Figure |
|--|------------------------------|----------------------------|
| Efficiency | vs Output power | 4, 6 |
| Supply surrent (les) | vs Output power | 5, 7 |
| Supply current (Icc) | vs Supply voltage | 8 |
| Shutdown current (Istby) | vs Supply voltage | 9 |
| Output power (Po) | vs Load resistance | 10, 11 |
| Output power (Po) | vs Supply voltage | 12 |
| | vs Output power | 13, 14 |
| Total harmonic distortion plus noise (THD+N) | vs Frequency | 15, 16, 17, 18, 25, 26, 27 |
| | vs Common-mode input voltage | 19 |
| Supply voltage rejection ratio (PSRR) | vs Frequency | 20, 21, 22, 23 |
| Common-mode rejection ratio (CMRR) | vs Frequency | 24 |
| Gain | vs Frequency | 28, 29, 30, 31 |

Reference data



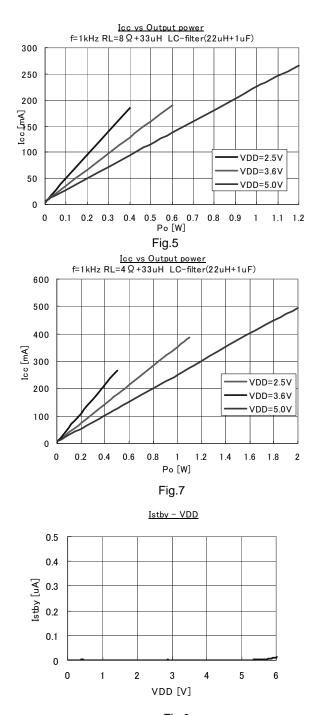
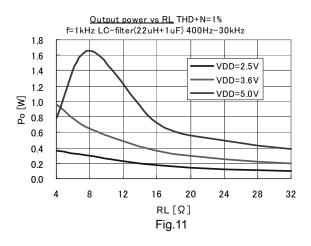
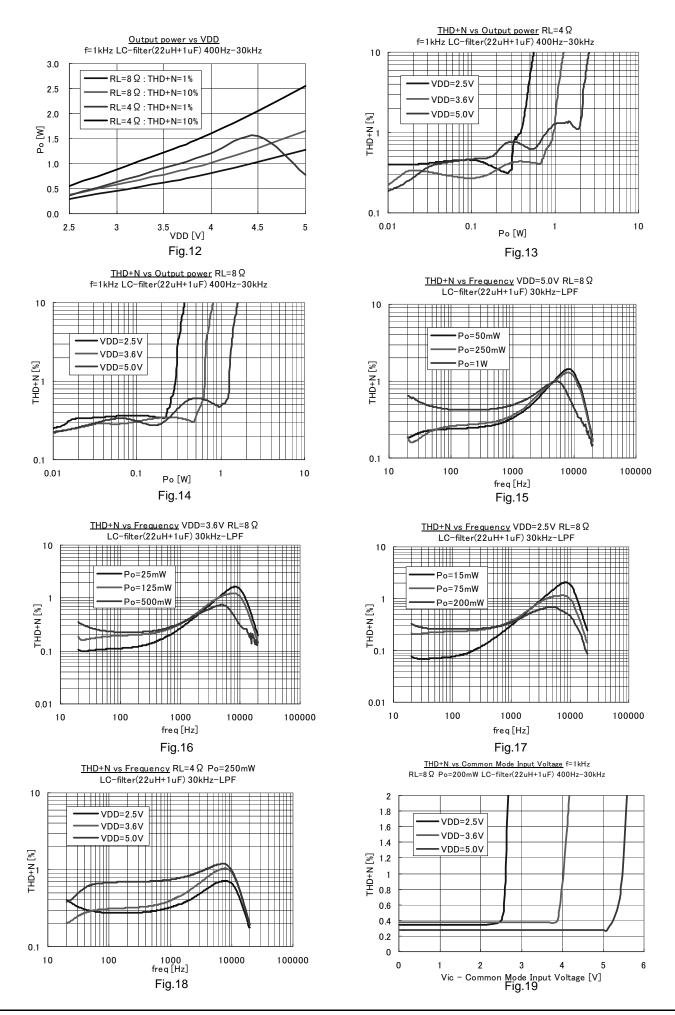
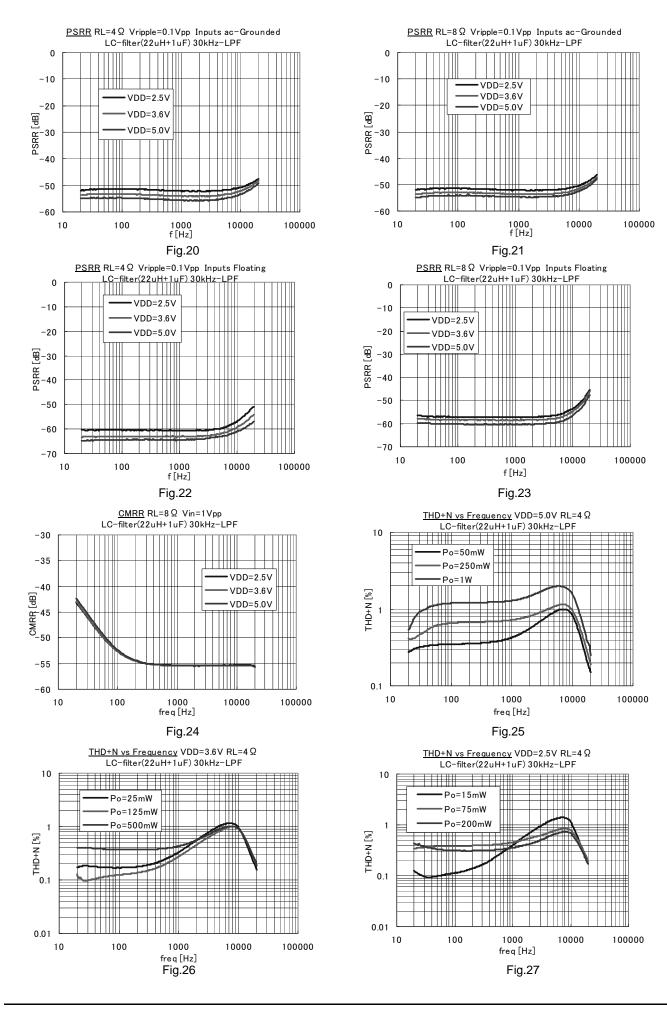


Fig.9







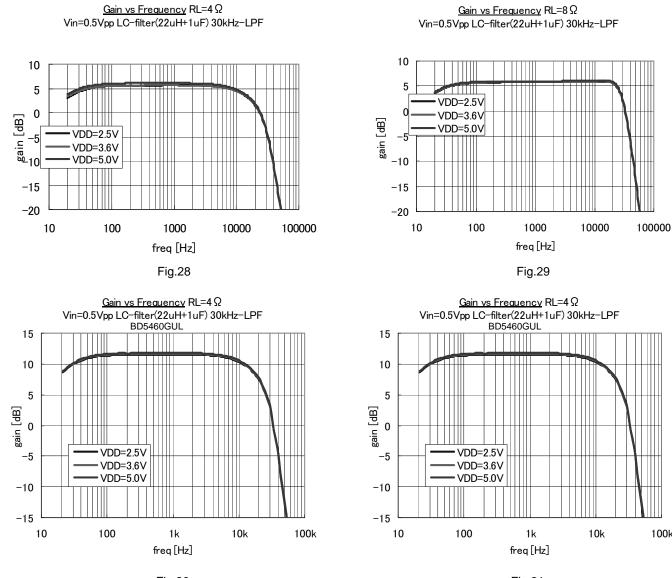


Fig.30

Fig.31

100k

Notes for use

- (1) Numbers and data in entries are representative design values and are not guaranteed values of the items.
- (2) Although we are confident recommending the sample application circuit, carefully check their characteristics further when using them. When modifying externally attached component constants before use, determine them so that They have sufficient margins by taking into account variations in externally attached components and the Rohm IC, not only for static characteristics but also including transient characteristics.
- (3) Absolute maximum ratings

This IC may be damaged if the absolute maximum ratings for the applied voltage, temperature range, or other parameters are exceeded. Therefore, avoid using a voltage or temperature that exceeds the absolute maximum ratings. If it is possible that absolute maximum ratings will be exceeded, use fuses or other physical safety measures and determine ways to avoid exceeding the IC's absolute maximum ratings.

(4) GND terminal's potential

Try to set the minimum voltage for GND terminal's potential, regardless of the operation mode.

(5) Shorting between pins and mounting errors

When mounting the IC chip on a board, be very careful to set the chip's orientation and position precisely. When the power is turned on, the IC may be damaged if it is not mounted correctly. The IC may also be damaged if a short occurs (due to a foreign object, etc.) between two pins, between a pin and the power supply, or between a pin and the GND.

(6) Operation in strong magnetic fields

Note with caution that operation faults may occur when this IC operates in a strong magnetic field.

(7) Thermal design

Ensure sufficient margins to the thermal design by taking in to account the allowable power dissipation during actual use modes, because this IC is power amp. When excessive signal inputs which the heat dissipation is insufficient condition, it is possible that thermal shutdown circuit is active.

(8) Thermal shutdown circuit

This product is provided with a built-in thermal shutdown circuit. When the thermal shutdown circuit operates, the output transistors are placed under open status. The thermal shutdown circuit is primarily intended to shut down the IC avoiding thermal runaway under abnormal conditions with a chip temperature exceeding Tjmax = $+150^{\circ}$ C, and is not intended to protect and secure an electrical appliance.

(9) Load of the output terminal

This IC corresponds to dynamic speaker load, and doesn't correspond to the load except for dynamic speakers.

(10) The short protection of the output terminal

The short-circuiting protection of this IC corresponds only to "VDD-short" (the short-circuiting with the power) of the output terminal and "GND-short" (the short-circuiting with GND) of the output terminal. It doesn't correspond to the short-circuiting among the output terminals.

Also, when the short-circuiting condition of the output terminal is canceled, it detects the high impedance of the output terminal and it is equipped with the auto recover without power cycling(the cancellation) function in the short-circuiting protection. Be careful of the output terminal, because, there is a fear not to return automatically when the short-circuiting condition occurs in pull-up or the pull-down at equal to or less than about $1M\Omega$ impedance,

(11) Operating ranges

The rated operating power supply voltage range (VDD=+ $2.5V \sim +5.5V$) and the rated operating temperature range (Ta=- $40^{\circ}C \sim +85^{\circ}C$) are the range by which basic circuit functions is operated. Characteristics and rated output power are not guaranteed in all power supply voltage ranges or temperature ranges.

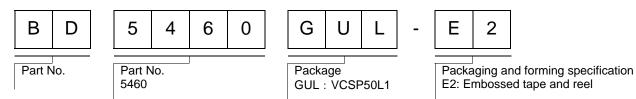
(12) Electrical characteristics

Electrical characteristics show the typical performance of device and depend on board layout, parts, power supply. The standard value is in mounting device and parts on surface of ROHM's board directly.

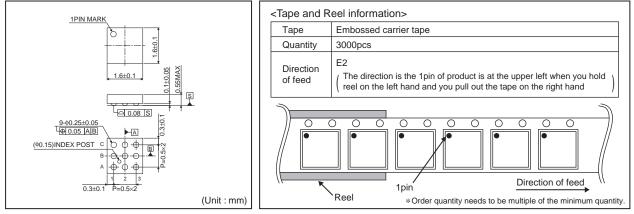
(13) Power decoupling capacitor

Because the big peak current flows through the power line, the class-D amplifier has an influence on the Audio characteristic by the capacitance value or the arrangement part of the power decoupling capacitor. Arrange a power decoupling capacitor as close as possible to the VDD terminal of IC.

Ordering part number



VCSP50L1(BD5460GUL)



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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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