

Temperature Sensor ICs Thermostat Output with Variable Detection Temperature

BDExxx0G Series

General Description

BDExxx0G series are low quiescent current (16 μ A), high accuracy thermostat (temperature switch) ICs. This IC has a built-in temperature sensor, reference voltage regulator, D/A converter, and comparator. The IC's OS terminal changes its logic state upon detection of temperature. An open-drain output (Active-L) is available in this series.

Features

- ±5°C-step Selectable Detection Temperature with Control
- ESD Rating of 8kV (HBM)
- Excellent Ripple Rejection Characteristic

Applications

Thermal Protection for Electrical Equipment (Notebook PC, Cellular phone, FPD-TV, etc.). Fan Control for Thermal Management.

Key Specifications

Power Supply Voltage Range: 2.9V to 5.5VSupply Current: 16.0µA (Typ)

■ Detection Temperature Range: +55°C to +115°C

■ Detection Temperature Accuracy:

±4.0°C (Max) @Ta=-20°C to +115°C

■ Hysteresis Temperature: 10°C (Typ)

■ High Accuracy Analog Output:

±3.5°C (Max) @Ta=30°C

Analog Output Temperature Sensitivity:

-10.68mV/°C (Typ)

■ Operating Temperature Range: -30°C to +130°C

Package

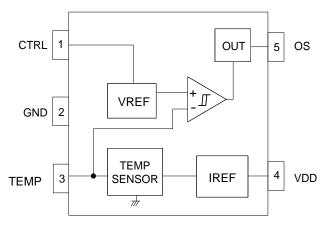




2.90mm x 2.80mm x 1.25mm

Block Diagram and Pin Configuration

TOP VIEW



Pin Descriptions

Pin No.	Pin Name	Function	Comment
1	CTRL	Detection Temperature Setting	Refer to page 9/11 for the temperature settings. (Temperature / Output Format Table)
2	GND	Ground	
3	TEMP	Output voltage in inverse proportion to the temperature (Typ -10.68mV/°C)	Set to Open state or connect to a high input impedance node (over $10M\Omega$).
4	VDD	Power Supply Voltage	
5	os	Digital Thermostat Output	Open-Drain type. Use pull-up resistor of over 10kΩ.

OProduct structure: Silicon monolithic integrated circuit OThis product has no designed protection against radioactive rays

Absolute Maximum Ratings (Ta = 25°C)

Parameters	Symbol	Limit	Unit
Power Supply Voltage	V_{DD}	-0.3 to +7.0 (Note 1)	V
Input Voltage (CTRL)	V _{IN}	-0.3 to $+V_{DD}+0.3$	V
Input Current (CTRL)	I _{IN}	-1.0, +0.1	mA
OS Terminal Voltage	Vos	-0.3 to +7.0	V
OS Terminal Current	los	5.0	mA
Power Dissipation	Pd	0.54 (Note 2)	W
Storage Temperature Range	Tstg	-55 to +150	°C

(Note 1) However, not exceeding Pd.

(Note 2) When mounted on ROHM standard board, derate by 5.40mW/°C for Ta higher than 25 °C.

Caution: Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

Recommended Operating Conditions

Parameters	Symbol	Min	Тур	Max	Unit
Power Supply Voltage	V_{DD}	2.9	3.0	5.5	V
Operating Temperature Range	Topr	-30	-	+130	°C

Electrical Characteristics (Unless otherwise specified, V_{DD} = 3.0V, Ta = 25°C)

Doromator	Coursels al	Limits			1.1:4	Conditions
Parameter	Symbol	Min	Тур	Max	Unit	Conditions
Supply Current	I _{DD}	-	16.0	20.0	μA	V _{CTRL} = 3.0V
Analog Output						
TEMP Output Voltage	VTEMP	1.716	1.753	1.790	V	Ta = 30°C
TEMP Temperature Sensitivi	ty V _{SE}	-10.28	-10.68	-11.08	mV/°C	Ta = -30°C to +100°C
TEMP Load Regulation	ΔV_TEMPRL	-	-	1	mV	difference of I _{OUT} : 0µA/2µA
OS Output Open Drain	OS Output Open Drain					
OS Leakage Current	IL	-	-	1.0	μA	Vos = 5.0V
OS Output Voltage	Vol	-	-	0.4	V	los = 1.2mA
CTRL						
Input L Voltage	VIL	GND	-	0.6	V	
Input H Voltage	V _{IH}	2.4	-	V_{DD}	V	

(Note) Radiation hardiness is not designed.

Temperature Accuracy (Unless otherwise specified, V_{DD} = 3.0V)

Parameters		Symbol	Limit			Unit	Conditions
			Min	Тур	Max	Offic	Conditions
Thermostat (Temperature Switch)							
Detection Temperature Accuracy		Tacc	-	-	±4.0	°C	Ta = -20°C to +115°C
Detection Temperature Hysteresis		Thys	7.5	10.0	12.5	°C	
Analog Output							
TEMP Temperatur	e Accuracy	T _{TEMP}	-	-	±3.5	°C	Ta = 30°C

Typical Performance Curves

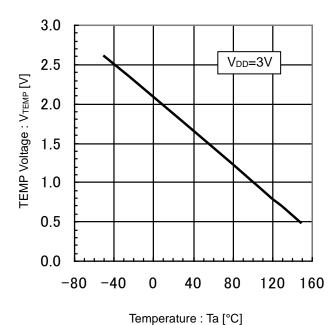


Figure 1. TEMP Voltage vs Temperature (Temperature Sensitivity)

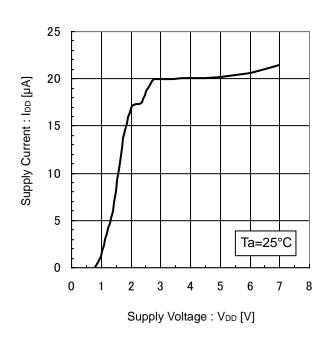


Figure 2. Supply Current vs Supply Voltage

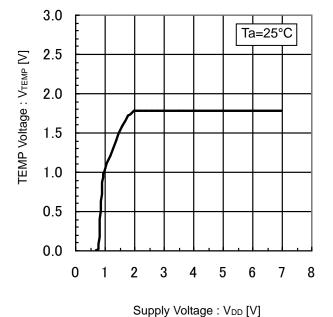


Figure 3. TEMP Voltage vs Supply Voltage

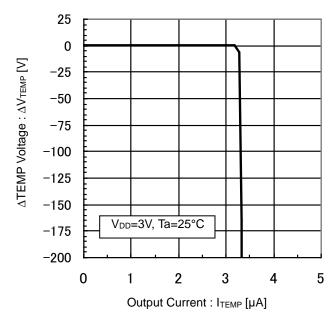


Figure 4. TEMP Voltage vs Output Current

Typical Performance Curves - continued

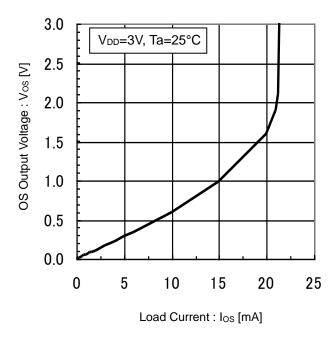
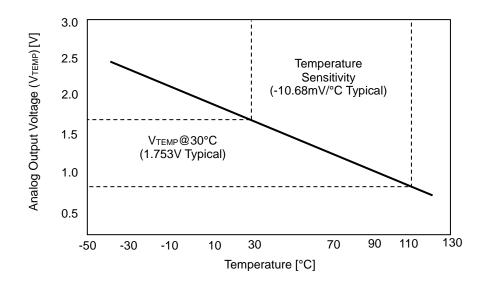
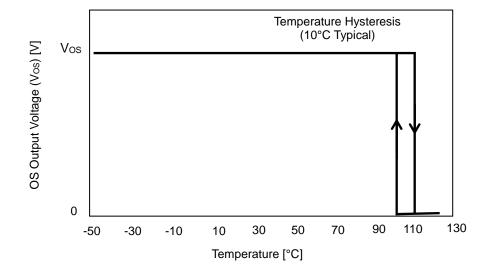


Figure 5. OS Output Voltage vs Load Current

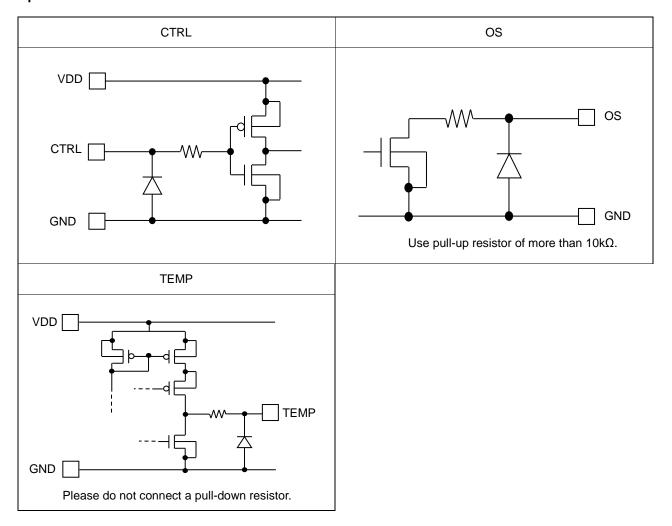
Application Information

1. Functional Diagram (ex. Detection Temperature 110°C)





I/O Equivalent Circuits



Operational Notes

1. Reverse Connection of Power Supply

Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply pins.

2. Power Supply Lines

Design the PCB layout pattern to provide low impedance supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

3. Ground Voltage

Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

4. Ground Wiring Pattern

When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

5. Thermal Consideration

Should by any chance the power dissipation rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. In case of exceeding this absolute maximum rating, increase the board size and copper area to prevent exceeding the Pd rating.

6. Recommended Operating Conditions

These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

7. Inrush Current

When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

8. Operation Under Strong Electromagnetic Field

Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

9. Testing on Application Boards

When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

10. Inter-pin Short and Mounting Errors

Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

Operational Notes - continued

11. Unused Input Pins

Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

12. Regarding the Input Pin of the IC

This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When GND > Pin A and GND > Pin B, the P-N junction operates as a parasitic diode. When GND > Pin B, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

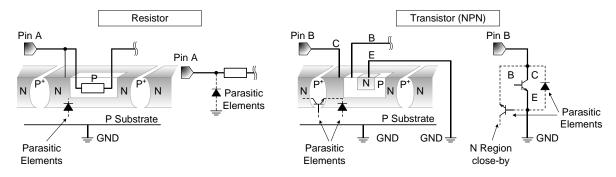
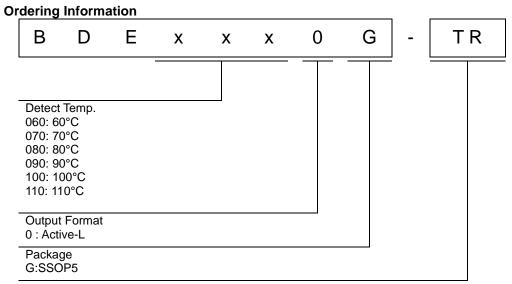


Figure 6. Example of monolithic IC structure



Packaging and forming specification

TR: Embossed tape and reel

(SSOP5)

Lineup

Temperature / Output Format Table CTRL status description (L:Low, O:Open, H:High)

75

65

55

Detection Temperature (°C) Product **CTRL OS Output Format** Name L Н 0 BDE1100G 105 110 115 Open-Drain Active-L BDE1000G 100 105 Active-L 95 Open-Drain BDE0900G 85 90 95 Active-L Open-Drain

85

75

65

Open-Drain

Open-Drain

Open-Drain

Active-L

Active-L

Active-L

80

70

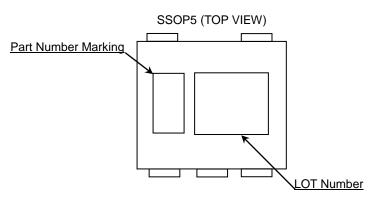
60

Marking Diagram

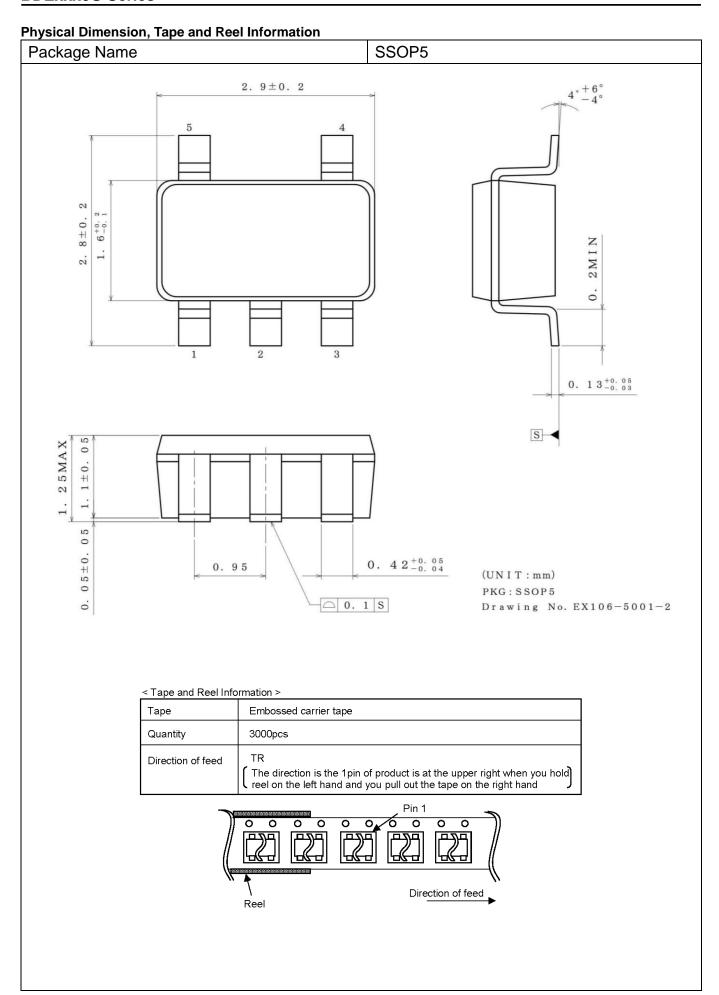
BDE0800G

BDE0700G

BDE0600G



Orderable Part Number	Part Number Marking		
BDE1100G-TR	eB		
BDE1000G-TR	eC		
BDE0900G-TR	eD		
BDE0800G-TR	eE		
BDE0700G-TR	eF		
BDE0600G-TR	eG		



Revision History

Date	Revision	Changes
06.Nov.2015	001	New Release

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(Note1) Medical Equipment Classification of the Specific Applications

Ī	JÁPAN	USA	EU	CHINA	
Ī	CLASSⅢ	CLACCIII	CLASS II b	CL A C C TT	
Ī	CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ	

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For details, please refer to ROHM Mounting specification

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 REEL
 ADM1032ARMZ-IRL
 AT30TS74-U1FMBB-T
 AT30TS74-U1FMAB-T
 AT30TS74-U1FMCB-T
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