

Ambient Light Sensor ICs

Digital 16bit Serial Output Type Ambient Light Sensor IC



No.11046ECT06

Descriptions

BH1715FVC

BH1 $\dot{7}$ 15FVC is an digital Ambient Light Sensor IC for I 2 C bus interface. BH1715FVC is the most suitable to obtain the ambient light data for adjusting LCD and Keypad backlight power of Mobile phone. It is possible to detect wide range at High resolution. (1 - 65535 lx).

Features

- 1) I²C bus Interface (f / s Mode Support)
- 2) Spectral responsibility is approximately human eye response
- 3) Illuminance to Digital Converter
- 4) Wide range and High resolution. (1 65535 lx)
- 5) Low Current by power down function
- 6) 50Hz / 60Hz Light noise reject-function
- 7) 1.8V Logic input interface
- 8) No need any external parts
- Light source dependency is little.
 (ex. Incandescent Lamp. Fluorescent Lamp. Halogen Lamp. White LED. Sun Light)
- 10) It is possible to select 2 type of I²C slave-address.
- 11) Adjustable measurement result for influence of optical window (It is possible to detect min. 0.23 lx, max. 100000 lx by using this function.)
- 12) Small measurement variation (+/- 15%)

Applications

Mobile phone, LCD TV, NOTE PC, Portable game machine, Digital camera, Digital video camera, PDA, LCD display

● Absolute Maximum Ratings

Parameter	Symbol	Ratings	Units
Supply Voltage	Vmax	4.5	٧
Operating Temperature	Topr	-40~85	°C
Storage Temperature	Tstg	-40~100	°C
SDA Sink Current	Imax	7	mA
Power Dissipation	Pd	260*	mW

 $[\]times$ 70mm \times 70mm \times 1.6mm glass epoxy board. Derating in done at 3.47mW/°C for operating above Ta=25°C.

Operating Conditions

Deremeter	Cymhol		Linita			
Parameter	Symbol	Min.	Тур.	Max.	Units	
V _{CC} Voltage	Vcc	2.4	3.0	3.6	V	
I ² C Reference Voltage	V _{DVI}	1.65	_	V _{CC}	V	

●Electrical Characteristics (V_{CC} = 3.0V, DVI = 3.0V, Ta = 25°C, unless otherwise noted)

ectrical Characteristics (VCC - 3.0V, DVI	- 5.0 V, 1a -	25 0, 01110	Limits	isc riolcu)			
Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions	
Supply Current	lcc1	_	120	190	μA	Ev = 100 lx **1	
Powerdown Current	Icc2	_	0.01	1.0	μΑ	No input Light	
Peak Wave Length	λр	_	560	_	nm		
Measurement Accuracy	S/A	1.02	1.2	1.38	times	Sensor out / Actual Ix EV = 1000 Ix **1, **2	
Dark (0 lx) Sensor out	S0	0	0	2	count	H-Resolution Mode **3	
H-Resolution Mode Resolution	rHR	_	1	_	lx		
L-Resolution Mode Resolution	rLR	_	4	_	lx		
H-Resolution Mode Measurement Time	tHR	_	120	180	ms		
L-Resolution Mode Measurement Time	tLR	_	16	24	ms		
Incandescent / Fluorescent Sensor out ratio	rIF	_	1	_	times	EV = 1000 lx	
ADDR Input 'H' Voltage	VAH	0.7 * V _{CC}	_	_	V		
ADDR Input 'L' Voltage	VAL	_	_	0.3 * V _{CC}	V		
DVI Input 'L' Voltage	VDVL	_	_	0.4	V		
SCL, SDA Input 'H' Voltage 1	VIH1	0.7 * DVI	_	_	V	DVI ≧ 1.8V	
SCL, SDA Input 'H' Voltage 2	VIH2	1.26	_	_	V	1.65V ≦ DVI < 1.8V	
SCL, SDA Input 'L' Voltage 1	VIL1	_	_	0.3 * DVI	V	DVI ≧ 1.8V	
SCL, SDA Input 'L' Voltage 2	VIL2	_	_	DVI-1.26	V	1.65V ≦ DVI < 1.8V	
SCL, SDA, ADDR Input 'H' Current	IIH	_	_	10	μΑ		
SCL, SDA, ADDR Input 'L' Current	IIL	_	_	10	μA		
I ² C SCL Clock Frequency	fSCL	_	_	400	kHz		
I ² C Bus Free Time	tBUF	1.3	_	_	μs		
I ² C Hold Time (repeated) START Condition	tHDSTA	0.6	_	_	μs		
I ² C Set up time for a Repeated START Condition	tSUSTA	0.6	_	_	μs		
I ² C Set up time for a Repeated STOP Condition	tSUSTD	0.6	_	_	μs		
I ² C Data Hold Time	tHDDAT	0	_	0.9	μs		
I ² C Data Setup Time	tSUDAT	100	_	_	ns		
I ² C 'L' Period of the SCL Clock	tLOW	1.3	_	_	μs		
I ² C 'H' Period of the SCL Clock	tHIGH	0.6	_	_	μs		
I ² C SDA Output 'L' Voltage	VOL	0	_	0.4	V	IOL = 3 mA	

^{%1} White LED is used as optical source.

 $[\]label{lem:condition} \begin{tabular}{ll} \mathbb{X} 2 Measurement Accuracy typical value is possible to change '1' by "Measurement result adjustment function". \end{tabular}$

³ Use H-Resolution Mode if dark data (less than 10 lx) is need.

● Reference Data

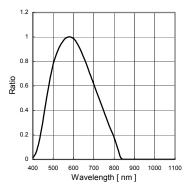


Fig.1 Spectral Response

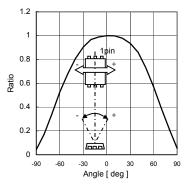


Fig.4 Directional Characteristics 1

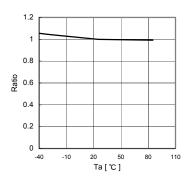


Fig.7 Measurement Result Temperature Dependency

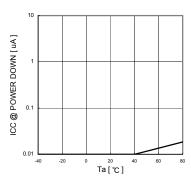


Fig.10 V_{CC} – I_{CC} @0 Lx (POWER DOWN)

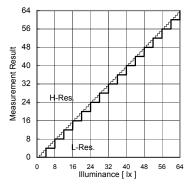


Fig.2 Illuminance – Measurement Result 1

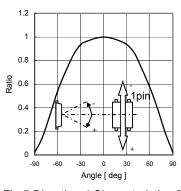


Fig.5 Directional Characteristics 2

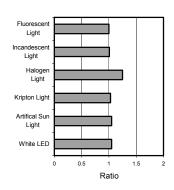


Fig.8 Light Source Dependency (Fluorescent Light is set to '1')

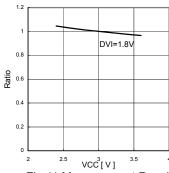


Fig.11 Measurement Result VCC Dependency

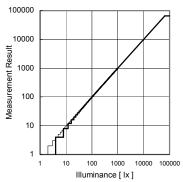


Fig.3 Illuminance – Measurement Result 2

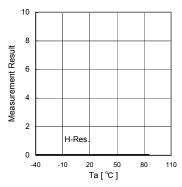
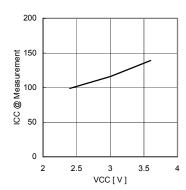


Fig.6 Dark Response



 $\label{eq:fig.9} \begin{aligned} &\text{Fig.9 V}_{\text{CC}} - \text{I}_{\text{CC}} \\ &\text{(During measurement)} \end{aligned}$

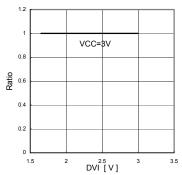
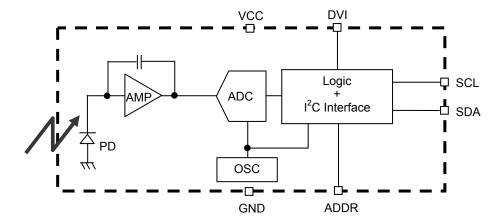


Fig.12 Measurement Result DVI Dependency

Block Diagram

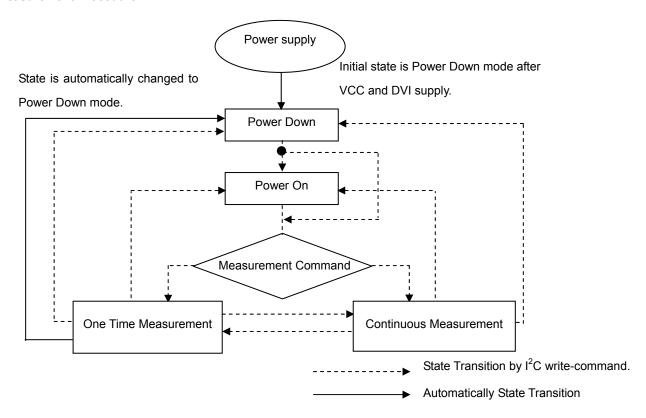


Block Diagram Descriptions

- PD
 - Photo diode with approximately human eye response.
- AMP
 - Integration-OPAMP for converting from PD current to Voltage.
- ADC
- AD converter for obtainment Digital 16bit data.
- Logic + I²C Interface
 Ambient Light Calculation and I²C BUS Interface. It is including below register.
 Data Register → This is for registration of Ambient Light Data. Initial Value is "0000_0000_0000_0000".

 Measurement Time Register → This is for registration of measurement time. Initial Value is "0100_0101".
- OSC
 Internal Oscillator (typ. 320kHz). It is CLK for internal logic.

Measurement Procedure



^{* &}quot;Power On" Command is possible to omit.

●Instruction Set Architecture

Instruction	Opecode	Comments
Power Down	0000_0000	No active state.
Power On	0000_0001	Waiting for measurement command.
Reset 0000_0111		Reset Data register value. Reset command is not acceptable in Power Down mode.
Continuously H-Resolution Mode	0001_0000	Start measurement at 1lx resolution. Measurement Time is typically 120ms.
Continuously L-Resolution Mode	0001_0011	Start measurement at 4lx resolution. Measurement Time is typically 16ms.
One Time H-Resolution Mode	0010_0000	Start measurement at 1lx resolution. Measurement Time is typically 120ms. It is automatically set to Power Down mode after measurement.
One Time L-Resolution Mode	0010_0011	Start measurement at 4lx resolution. Measurement Time is typically 16ms. It is automatically set to Power Down mode after measurement.
Change Measurement time (High bit)	01000_MT[7,6,5]	Change measurement time. X Please refer "adjust measurement result for influence of optical window."
Change Measurement time (Low bit)	011_MT[4,3,2,1, 0]	Change measurement time. X Please refer "adjust measurement result for influence of optical window."

X Don't input the other opecode.

■Measurement mode explanation

Measurement Mode	Measurement Time.	Resolution
H-Resolution Mode	Typ. 120ms.	1 Lx.
L-Resolution Mode	Typ. 16ms.	4 Lx.

We recommend to use H-Resolution Mode.

Measurement time (integration time) of H-Resolution Mode is so long that some kind of noise(including in 50Hz / 60Hz noise) is rejected. And H-Resolution Mode is 1 Ix resolution so that it is suitable for darkness (less than 10 Ix)

● Explanation of Asynchronous reset and Reset command "0000_0111"

1) Asynchronous reset

All registers are reset. It is necessary on power supply sequence. Please refer "Timing chart for VCC and DVI power supply sequence" in this page. It is power down mode during DVI = 'L'.

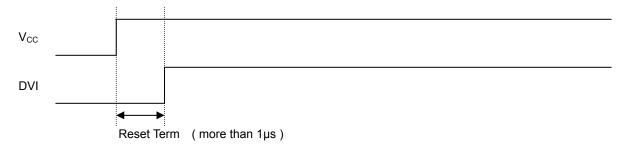
2) Reset command

Reset command is for only reset Illuminance data register. (reset value is '0') It is not necessary even power supply sequence. It is used for removing previous measurement result. This command is not working in power down mode, so that please set the power on mode before input this command.

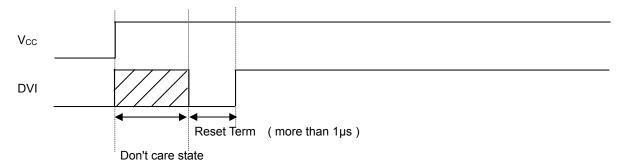
Timing chart for VCC and DVI power supply sequence

DVI is I^2C bus reference voltage terminal. And it is also asynchrono reset terminal. It is necessary to set to 'L' after VCC is supplied. In DVI 'L' term, internal state is set to Power Down mode.

1) Recommended Timing chart1 for VCC and DVI supply.



2) Timing chart2 for VCC and DVI supply.
(If DVI rises within 1us after VCC supply)



ADDR, SDA, SCL is not stable if DVI 'L' term (1µs) is not given by systems. In this case, please connect the resisters (approximately 100kOhm) to ADDR without directly

connecting to VCC or GND, because it is 3 state buffer for Internal testing.

• Measurement sequence example from "Write instruction" to "Read measurement result"

ex1) Continuously H-resolution mode (ADDR = 'L')

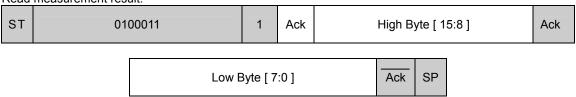
from Master to Slave from Slave to Master

① Send "Continuously H-resolution mode " instruction



2 Wait to complete 1st H-resolution mode measurement.(max. 180ms.)

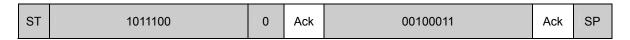
3 Read measurement result.



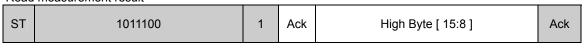
How to calculate when the data High Byte is "10000011" and Low Byte is "10010000" $(2^{15} + 2^9 + 2^8 + 2^7 + 2^4) / 1.2 = 28067$ [Ix]

The result of continuously measurement mode is updated.(120ms.typ at H-resolution mode, 16ms.typ at L-resolution mode)

- ex2) One time L-resolution mode (ADDR = 'H')
 - 1) Send "One time L-resolution mode" instruction



- 2 Wait to complete L-resolution mode measurement.(max. 24ms.)
- 3 Read measurement result



Low Byte [7:0] Ack SP

How to calculate when the data High Byte is "00000001" and Low Byte is "00010000" $(2^8 + 2^4) / 1.2 = 227 [Ix]$

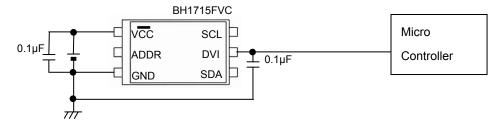
In one time measurement, Statement moves to power down mode after measurement completion. If updated result is need then please resend measurement instruction.

Application circuit example of DVI terminal

The DVI terminal is an asynchronous reset terminal. Please note that there is a possibility that IC doesn't operate normally if the reset section is not installed after the start-up of VCC. (Please refer to the paragraph of "Timing chart for VCC and DVI power supply sequence")

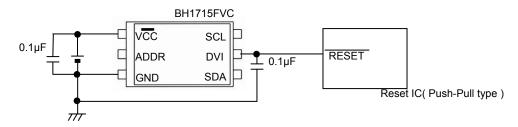
The description concerning SDA and the terminal SCL is omitted in this application circuit example. Please design the application the standard of the I²C bus as it finishes being satisfactory. Moreover, the description concerning the terminal ADDR is omitted. Please refer to the paragraph of "Timing chart for VCC and DVI power supply sequence" about the terminal ADDR design.

ex 1) The control signal line such as CPU is connected.

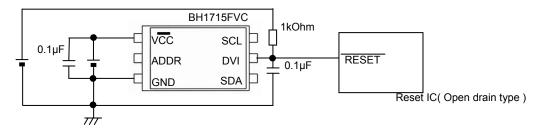


ex 2) Reset IC is used.

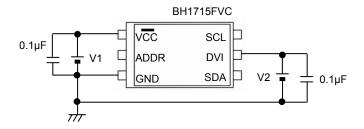
1, For Reset IC of the Push-Pull type



2, For Reset IC of the Open drain output



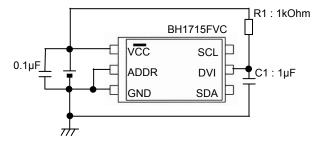
ex 3) A different power supply is used.



※ Power supply of DVI must stand up later than power supply of VCC stand up, because it is necessary to secure reset section (1µs or more).

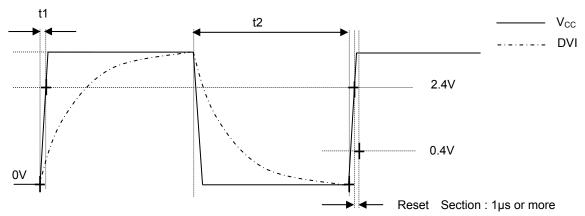
ex 4) LPF using CR is inserted between VCC and DVI.

This method has the possibility that the Reset section of turning on the power supply can not satisfied. cannot be satisfied. Please design the set considering the characteristic of the power supply enough.



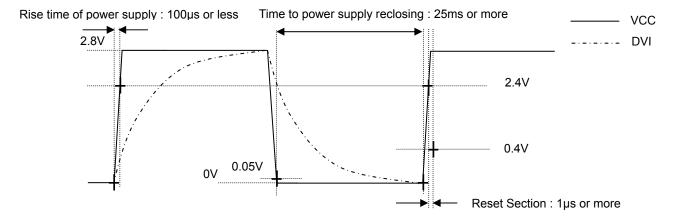
- ♦ Notes when CR is inserted between VCC and DVI
- Please note that there is a possibility that reset section (1µs) can not be satisfied because the power supply is turned on when the rise time of VCC is slow
- When VCC is turned off, the DVI voltage becomes higher than VCC voltage but IC destruction is not occurred if recommended constant (R1 = 1kOhm, C1 = 1μF) is used.
- Please note that there is a possibility that Reset section (1μsec) cannot be satisfied if wait time is not enough long after turning off VCC.

 (It is necessary to consider DVI voltage level after turning off VCC.)



*Please do the application design to secure Reset section 1µs or more after the reclosing of the power supply.

- lacktriangle Example of designing set when CR (C = 1μF, R = 1kΩ) is inserted between VCC and DVI with VCC=2.8V
- ① The rise time to $0\rightarrow 2.4V$ of VCC must use the power supply of $100\mu s$ or less.
- ② Please wait 25ms or more after VCC turn off (VCC <= 0.05V), because it is necessary to secure reset section (1μs or more).

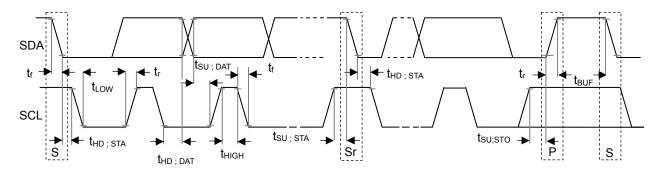


^{*} Please do the application design to secure Reset section 1µs or more after the reclosing of the power supply.

●I²C Bus Access

1) I²C Bus Interface Timing chart

Write measurement command and Read measurement result are done by I²C Bus interface. Please refer the formally specification of I²C Bus interface, and follow the formally timing chart.



2) Slave Address

Slave Address is 2 types, it is determined by ADDR Terminal

ADDR = 'H' (ADDR $\ge 0.7V_{CC}$) \rightarrow "1011100" ADDR = 'L' (ADDR $\le 0.3V_{CC}$) \rightarrow "0100011"

3)Write Format

BH1715FVC is not able to accept plural command without stop condition. Please insert SP every 1 Opecode.

-								
	ST	Slave Address	R/W 0	Ack	Opecode	Ack	SP	

4)Read Format

ST	Slave Address	R/W 1	Ack	High Byte [15:8] 2 ¹⁵ 2 ¹⁴ 2 ¹³ 2 ¹² 2 ¹¹ 2 ¹⁰ 2 ⁹ 2 ⁸	Ack	
----	---------------	----------	-----	---	-----	--

from Master to Slave	from Slave to Maste

ex)
High Byte = "1000_0011"
Low Byte = "1001_0000" $(2^{15} + 2^9 + 2^8 + 2^7 + 2^4) / 1.2 = 28067 [lx]$

^{*} Please refer formality I²C bus specification of NXP semiconductors.

Adjust measurement result for influence of optical window. (sensor sensitivity adjusting)

BH1715FVC is possible to change sensor sensitivity. And it is possible to cancel the optical window influence (difference with / without optical window) by using this function. Adjust is done by changing measurement time. For example, when transmission rate of optical window is 50% (measurement result becomes 0.5 times if optical window is set), influence of optical window is ignored by changing sensor sensitivity from default to 2 times

Sensor sensitivity is shift by changing the value of MTreg (measurement time regisiter). MTreg value has to set 2 times if target of sensor sensitivity is 2 times. Measurement time is also set 2 times when MTreg value is changed from default to 2 times.

ex) Procedure for changing target sensor sensitivity to 2 times. Please change Mtreg from "0100_0101" (default) to "1000_1010" (default * 2).

1) Changing High bit of Mtreg

ST	Slave Address	R/W 0	Ack	01000_100	Ack	SP
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2) Changing Low bit of Mtreg

ST	Slave Address	R/W 0 Ack	011_01010	Ack	SP

3) Input Measurement Command.

-	- /							
	ST	Slave Address	R/W 0	Ack	0001_0000	Ack	SP	

^{*}This example is High Resolution mode, but it accepts the other measurement.

4) After about 240ms, measurement result is registered to Data Register. (High Resolution mode is typically 120ms, but measurement time is set twice.)

The below table is seeing the changable range of MTreg.

		Min.	Тур.	Max.
	binary	0001_1111	0100_0101	1111_1110
changable	Diriary	(sensitivity : default * 0.45)	default	(sensitivity : default * 3.68)
range of MTreg	-1:1	31	69	254
	decimal	(sensitivity : default * 0.45)	default	(sensitivity : default * 3.68)

It is possilbe to detect 0.23lx by using this function at H-resolution mode.

The below formula is to calculate illuminance per 1 count.

Illuminance per 1 count (lx/count) = 1 / 1.2 *(69 / X)

1.2 : Measurement accuracy

69: Default value of MTreg (dec)

X: MTreg value

The below table is seeing the detail of resolution.

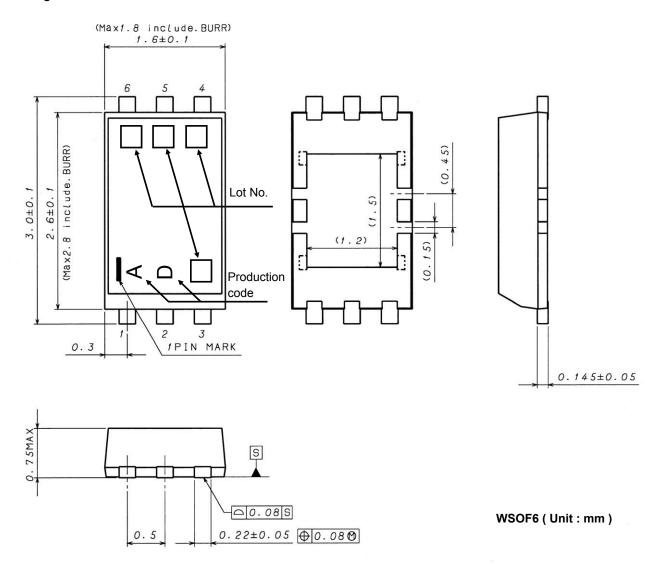
MTreg value	lx / count	
0001_1111	1.85	
0100_0101	0.83	
1111_1110	0.23	

●Terminal Description

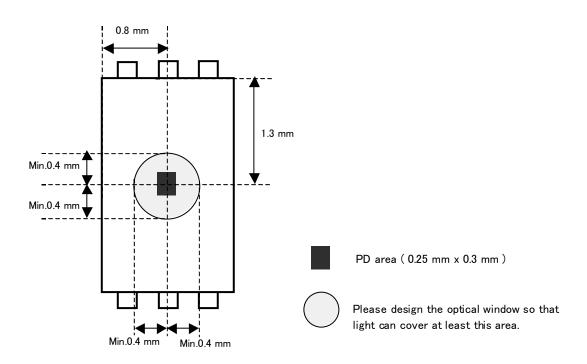
Terminal	erminal Description							
Pin No.	Terminal Name	Equivalent Circuit	Function					
1	VCC		Power Supply Terminal					
2	ADDR	V _{cc}	I ² C Slave-address Terminal ADDR = 'H' (ADDR \geq 0.7V _{CC}) "1011100" ADDR = 'L' (ADDR \leq 0.3V _{CC}) "0100011" ADDR Terminal is designed as 3 state buffer for internal test. So that please take care of V _{CC} and DVI supply procedure.Please see P6.					
3	GND		GND Terminal					
4	SDA		I ² C bus Interface SDA Terminal					
5	DVI	150kOhm	SDA, SCL Reference Voltage Terminal And DVI Terminal is also asynchronous Reset for internal registers. So that please set to 'L' (at least 1 μ s, DVI <= 0.4V) after V _{CC} is supplied. BH1715FVC is pulled down by 150kOhm while DVI = 'L'.					
6	SCL		I ² C bus Interface SCL Terminal					

X These values are design-value, not guaranteed.

● Package Outlines



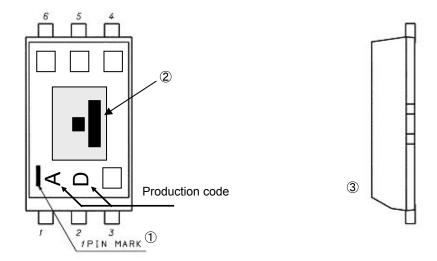
●About an optical design on the device



●The method of distinguishing 1pin

There is some method of distinguishing 1pin.

- Distinguishing by 1Pin wide-lead
 Distinguishing by die pattern
 Distinguishing by taper part of 1-3pin side
 - ② (by die pattern) is the easiest method to distinguish by naked eye.



Cautions on use

1) Absolute Maximum Ratings

An excess in the absolute maximum ratings, such as supply voltage (Vmax), temperature range of operating conditions (Topr), etc., can break down devices, thus making impossible to identify breaking mode such as a short circuit or an open circuit. If any special mode exceeding the absolute maximum ratings is assumed, consideration should be given to take physical safety measures including the use of fuses, etc.

2) GND voltage

Make setting of the potential of the GND terminal so that it will be maintained at the minimum in any operating state. Furthermore, check to be sure no terminals are at a potential lower than the GND voltage including an actual electric transient.

3) Short circuit between terminals and erroneous mounting

In order to mount ICs on a set PCB, pay thorough attention to the direction and offset of the ICs. Erroneous mounting can break down the ICs. Furthermore, if a short circuit occurs due to foreign matters entering between terminals or between the terminal and the power supply or the GND terminal, the ICs can break down.

4) Operation in strong electromagnetic field

Be noted that using ICs in the strong electromagnetic field can malfunction them.

5) Inspection with set PCB

On the inspection with the set PCB, if a capacitor is connected to a low-impedance IC terminal, the IC can suffer stress. Therefore, be sure to discharge from the set PCB by each process. Furthermore, in order to mount or dismount the set PCB to/from the jig for the inspection process, be sure to turn OFF the power supply and then mount the set PCB to the jig. After the completion of the inspection, be sure to turn OFF the power supply and then dismount it from the jig. In addition, for protection against static electricity, establish a ground for the assembly process and pay thorough attention to the transportation and the storage of the set PCB.

6) Input terminals

In terms of the construction of IC, parasitic elements are inevitably formed in relation to potential. The operation of the parasitic element can cause interference with circuit operation, thus resulting in a malfunction and then breakdown of the input terminal. Therefore, pay thorough attention not to handle the input terminals; such as to apply to the input terminals a voltage lower than the GND respectively, so that any parasitic element will operate. Furthermore, do not apply a voltage to the input terminals when no power supply voltage is applied to the IC. In addition, even if the power supply voltage is applied, apply to the input terminals a voltage lower than the power supply voltage or within the guaranteed value of electrical characteristics.

7) Thermal design

Perform thermal design in which there are adequate margins by taking into account the power dissipation (Pd) in actual states of use.

8) Treatment of package

Dusts or scratch on the photo detector may affect the optical characteristics. Please handle it with care.

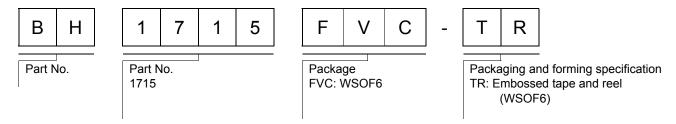
9) Rush current

When power is first supplied to the CMOS IC, it is possible that the internal logic may be unstable and rush current may flow instantaneously. Therefore, give special consideration to power coupling capacitance, power wiring, width of GND wiring, and routing of connections.

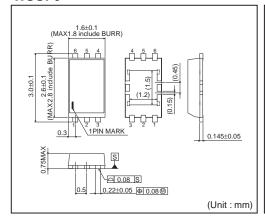
10) The exposed central pad on the back side of the package

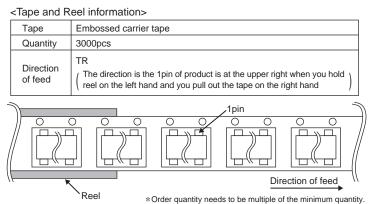
There is an exposed central pad on the back side of the package. But please do it non connection. (Don't solder, and don't do electrical connection) Please mount by Footprint dimensions described in the Jisso Information for WSOF6. This pad is GND level, therefore there is a possibility that LSI malfunctions and heavy-current is generated.

Ordering part number



WSOF6





Notice

Precaution on using ROHM Products

Our Products are designed and manufactured for application in ordinary electronic equipments (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA	
CLASSⅢ	CLASSⅢ	CLASS II b	CLASSIII	
CLASSIV	CLASSIII	CLASSⅢ		

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
 - [a] Installation of protection circuits or other protective devices to improve system safety
 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
- 3. Our Products are designed and manufactured for use under standard conditions and not under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc, prior to use, must be necessary:
 - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

- If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

Precaution for Product Label

QR code printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

Precaution for Foreign Exchange and Foreign Trade act

Since our Products might fall under controlled goods prescribed by the applicable foreign exchange and foreign trade act, please consult with ROHM representative in case of export.

Precaution Regarding Intellectual Property Rights

- 1. All information and data including but not limited to application example contained in this document is for reference only. ROHM does not warrant that foregoing information or data will not infringe any intellectual property rights or any other rights of any third party regarding such information or data. ROHM shall not be in any way responsible or liable for infringement of any intellectual property rights or other damages arising from use of such information or data.:
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- 2. All information contained in this docume nt is current as of the issuing date and subject to change without any prior notice. Before purchasing or using ROHM's Products, please confirm the latest information with a ROHM sale s representative.
- 3. The information contained in this doc ument is provided on an "as is" basis and ROHM does not warrant that all information contained in this document is accurate an d/or error-free. ROHM shall not be in an y way responsible or liable for any damages, expenses or losses incurred by you or third parties resulting from inaccuracy or errors of or concerning such information.

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