PNP -100mA -50V Digital Transistors (Bias Resistor Built-in Transistors)

### **Datasheet**

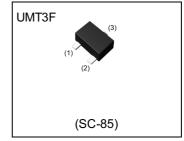
### **AEC-Q101 Qualified**

Parameter	Value
V <sub>CC</sub>	-50V
I <sub>C(MAX.)</sub>	-100mA
R <sub>1</sub>	10kΩ
R <sub>2</sub>	10kΩ

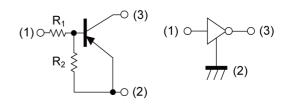
#### Features

- 1) Built-In Biasing Resistors,  $R_1 = R_2 = 10k\Omega$
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary NPN Types: DTC114EUB HZG.

#### Outline



#### •Inner circuit



- (1) IN (BASE)
- (2) GND (+) (EMITTER)
- (3) OUT (COLLECTOR)

#### Application

INVERTER, INTERFACE, DRIVER

#### Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTA114EUB HZG	UMT3F	2021	TL	180	8	3000	14

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

Parameter	Symbol	Values	Unit
Supply voltage	V <sub>CC</sub>	-50	V
Input voltage	V <sub>IN</sub>	-40 to 10	V
Output current	Io	-50	mA
Collector current	I <sub>C(MAX)</sub> *1	-100	mA
Power dissipation	P <sub>D</sub> *2	200	mW
Junction temperature	T <sub>j</sub>	150	°C
Range of storage temperature	T <sub>stg</sub>	-55 to +150	°C

# ●Electrical characteristics (T<sub>a</sub> = 25°C)

Dougnoston	C: resh al	Conditions	Values			11.2	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Innuit valtage	$V_{I(off)}$	$V_{CC} = -5V, I_{O} = -100\mu A$	-	-	-0.5	-	
Input voltage	V <sub>I(on)</sub>	$V_O = -0.3V$ , $I_O = -10$ mA	-3.0	-	-		
Output voltage	V <sub>O(on)</sub>	$I_0/I_1 = -10 \text{mA} / -0.5 \text{mA}$	-	-100	-300	mV	
Input current	I <sub>I</sub>	V <sub>I</sub> = -5V	-	-	-880	μA	
Output current	I <sub>O(off)</sub>	$V_{CC} = -50V, V_{I} = 0V$	-	-	-500	nA	
DC current gain	G <sub>I</sub>	$V_{O} = -5V, I_{O} = -5mA$	30	-	-	-	
Input resistance	R <sub>1</sub>	-	7	10	13	kΩ	
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	-	0.8	1.0	1.2	-	
Transition frequency	f <sub>T</sub> *1	V <sub>CE</sub> = -10V, I <sub>E</sub> = 5mA, f = 100MHz	-	250	-	MHz	

<sup>\*1</sup> Characteristics of built-in transistor

<sup>\*2</sup> Each terminal mounted on a reference land

### ● Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)

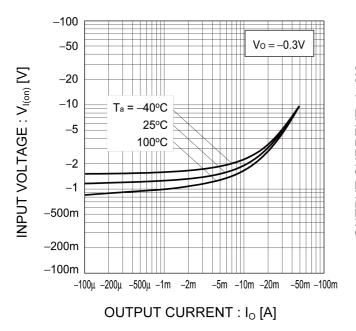


Fig.2 Output current vs. input voltage (OFF characteristics)

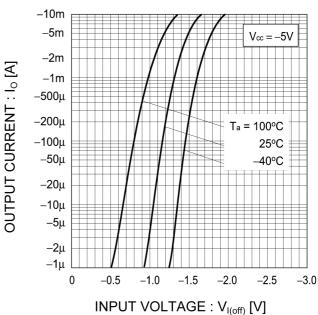


Fig.3 Output current vs. output voltage

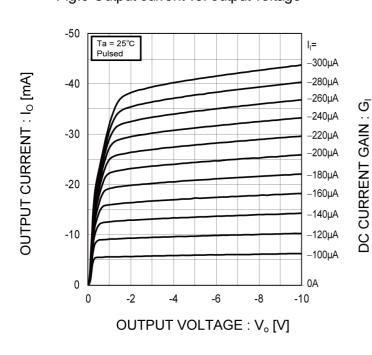
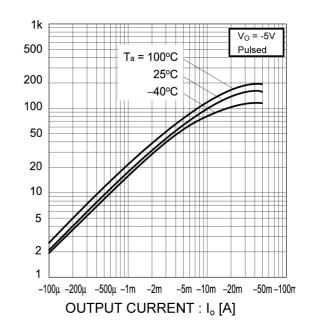
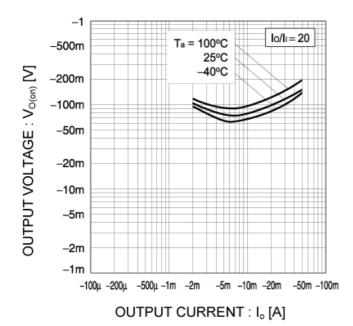


Fig.4 DC current gain vs. output current

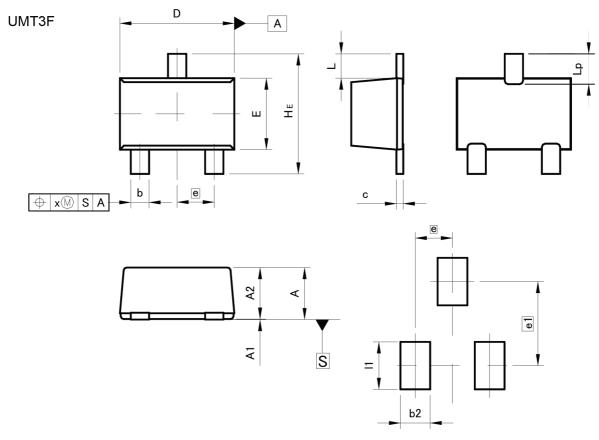


## ●Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.5 Output voltage vs. output current



### Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	0.85	1.05	0.033	0.041	
A1	0.00	0.10	0.000	0.004	
A2	0.80	1.00	0.031	0.039	
b	0.27	0.42	0.011	0.017	
С	0.08	0.18	0.003	0.007	
D	1.90	2.10	0.075	0.083	
E	1.15	1.35	0.045	0.053	
е	0.0	65	0.026		
HE	2.00	2.20	0.079	0.087	
L	0.4	43	0.0	17	
Lp	0.43	0.63	0.017	0.025	
х	_	0.10	_	0.004	

DIM MILIMETERS		ETERS	INCHES			
ואונט	MIN MAX		MIN	MAX		
b2	- 0.52		I	0.020		
e1	1.47		0.058			
11	- 0.83		_	0.033		

Dimension in mm/inches



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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
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