PNP -100mA -50V Digital Transistor (Bias Resistor Built-in Transistor)

# **AEC-Q101 Qualified**

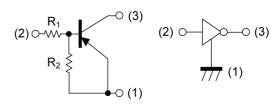
Parameter	Value
V <sub>CC</sub>	-50V
I <sub>C(MAX.)</sub>	-100mA
R <sub>1</sub>	4.7kΩ
R <sub>2</sub>	4.7kΩ

# ● Outline SOT-323 SC-70 (UMT3)

## Features

- 1) Built-In Biasing Resistors,  $R_1 = R_2 = 4.7k\Omega$
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary NPN Types: DTC143EU3 HZG

## •Inner circuit



- (1) GND (+) (EMITTER)
- (2) IN (BASE)
- (3) OUT (COLLECTOR)

# Application

INVERTER, INTERFACE, DRIVER

## Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTA143EU3 HZG	SOT-323 (UMT3)	2021	T106	180	8	3000	13

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

Parameter	Symbol	Values	Unit
Supply voltage	V <sub>CC</sub>	-50	V
Input voltage	V <sub>IN</sub>	-30 to 10	V
Output current	Io	-100	mA
Collector current	I <sub>C(MAX)</sub> *1	-100	mA
Power dissipation	P <sub>D</sub> *2	200	mW
Junction temperature	T <sub>j</sub>	150	°C
Range of storage temperature	T <sub>stg</sub>	-55 to +150	°C

# • Electrical characteristics $(T_a = 25^{\circ}C)$

Parameter	Symbol	Conditions	Values			Linit	
Parameter Sym		Conditions	Min.	Тур.	Max.	Unit	
lanut valtaga	$V_{l(off)}$	$V_{CC} = -5V, I_{O} = -100\mu A$	-	-	-0.5	- V	
Input voltage	V <sub>I(on)</sub>	$V_O = -0.3V$ , $I_O = -20$ mA	-3	-	-		
Output voltage	V <sub>O(on)</sub>	I <sub>O</sub> = -10mA, I <sub>I</sub> = -0.5mA	-	-100	-300	mV	
Input current	I <sub>I</sub>	V <sub>I</sub> = -5V	-	-	-1.8	mA	
Output current	I <sub>O(off)</sub>	V <sub>CC</sub> = -50V, V <sub>I</sub> = 0V	-	-	-500	nA	
DC current gain	G <sub>I</sub>	$V_O = -5V, I_O = -10mA$	30	-	1	-	
Input resistance	R <sub>1</sub>	-	3.29	4.7	6.11	kΩ	
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	-	0.8	1	1.2	-	
Transition frequency	f <sub>T</sub> *1	V <sub>CE</sub> = -10V, I <sub>E</sub> = 5mA, f = 100MHz	-	250	-	MHz	

<sup>\*1</sup> Characteristics of built-in transistor.

<sup>\*2</sup> Each terminal mounted on a reference land.

# ● Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)

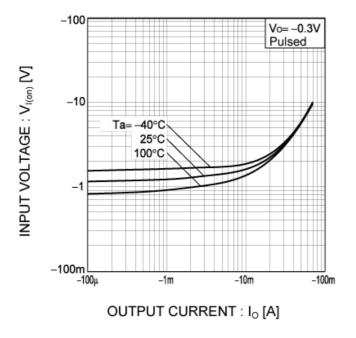


Fig.2 Output current vs. input voltage (OFF characteristics)

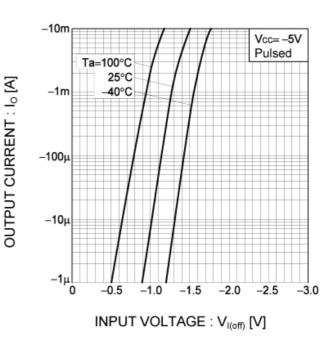


Fig.3 Output current vs. output voltage

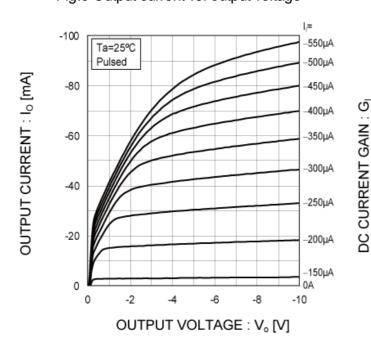
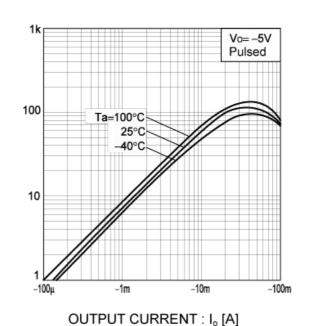
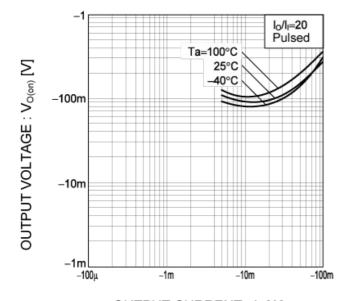


Fig.4 DC current gain vs. output current



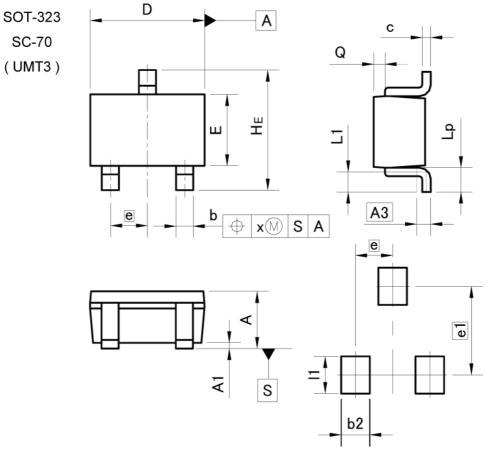
# ● Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.5 Output voltage vs. output current



OUTPUT CURRENT :  $I_{\circ}$  [A]

# Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
Α	0.80	1.00	0.031	0.039	
A1	0.00	0.10	0.000	0.004	
A3	0.5	25	0.0	10	
b	0.25	0.40	0.010	0.016	
С	0.10	0.20	0.004	0.008	
D	1.90	2.10	0.075	0.083	
E	1.15	1.35	0.045	0.053	
е	0.65		0.026		
HE	2.00	2.20	0.079	0.087	
L1	0.10	0.40	0.004	0.016	
Lp	0.25	0.55	0.010	0.022	
Q	0.10	0.30	0.004	0.012	
X	-	0.10	-	0.004	

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
b2	_	0.50	_	0.020	
e1	1.55		0.0	61	
- 11	-	0.65	-	0.026	

Dimension in mm/inches

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Ì	JÁPAN	USA	EU	CHINA
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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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## Precaution for Mounting / Circuit board design

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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