

PNP -100mA -50V Digital Transistor (Bias Resistor Built-in Transistor)

Parameter	Value	
V <sub>CEO</sub>	-50V	
I <sub>C</sub>	-100mA	
R <sub>1</sub>	4.7kΩ	

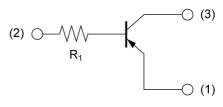
# Outline



## Features

- 1) Built-In Biasing Resistor
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary NPN Types: DTC143TCA

## •Inner circuit



- (1) EMITTER
- (2) BASE
- (3) COLLECTOR

# Application

INVERTER, INTERFACE, DRIVER

# Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTA143TCA	SOT-23 (SST3)	2924	T116	180	8	3000	93

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

Parameter	Symbol	Values	Unit
Collector-base voltage	$V_{CBO}$	-50	V
Collector-emitter voltage	V <sub>CEO</sub>	-50	V
Emitter-base voltage	V <sub>EBO</sub>	-5	V
Collector current	I <sub>C</sub>	-100	mA
Device discipation	P <sub>D</sub> *1	200	mW
Power dissipation	P <sub>D</sub> *2	350	mW
Junction temperature	Tj	150	°C
Range of storage temperature	T <sub>stg</sub>	-55 to +150	°C

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Unit
- Farameter	Symbol Conditions -		Min.	Тур.	Max.	Offic
Collector-base breakdown voltage	BV <sub>CBO</sub>	I <sub>C</sub> = -50μA	-50	-	-	V
Collector-emitter breakdown voltage	BV <sub>CEO</sub>	I <sub>C</sub> = -1mA	-50	1	-	V
Emitter-base breakdown voltage	BV <sub>EBO</sub>	I <sub>E</sub> = -50μA	-5	1	-	V
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> = -50V	-	-	-500	nA
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = -4V	-	-	-500	nA
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = -5mA, I_B = -0.25mA$	-	1	-300	mV
DC current gain	h <sub>FE</sub>	$V_{CE} = -5V$ , $I_C = -1mA$	100	250	600	-
Input resistance	R <sub>1</sub>	-	3.29	4.7	6.11	kΩ
Transition frequency	f <sub>T</sub> *3	V <sub>CE</sub> = -10V, I <sub>E</sub> = 5mA, f = 100MHz	-	250	-	MHz

<sup>\*1</sup> Each terminal mounted on a reference land.

<sup>\*2</sup> Mounted on a ceramic board(7.0×5.0×0.6mm).

<sup>\*3</sup> Characteristics of built-in transistor

# ● Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.1 Grounded emitter propagation characteristics

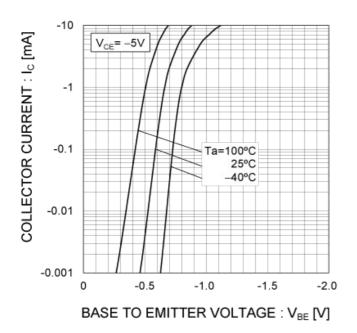


Fig.2 Grounded emitter output characteristics

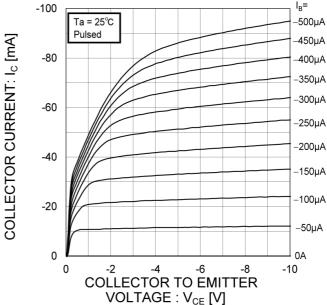


Fig.3 DC Current gain vs. Collector Current

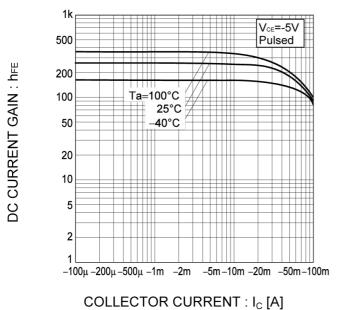
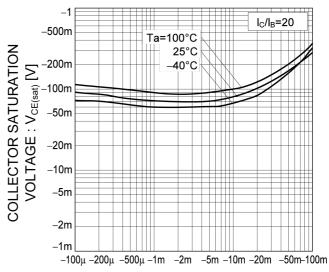
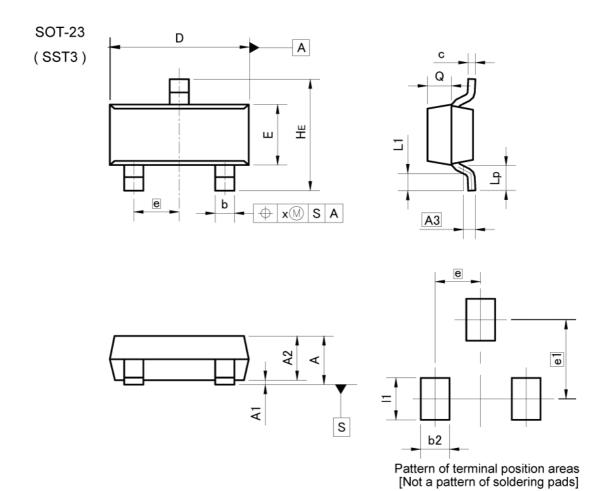


Fig.4 Collector-emitter saturation voltage vs. Collector Current



COLLECTOR CURRENT : I<sub>C</sub> [A]

# Dimensions



DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.90	1.20	0.035	0.047
A1	0.00	0.10	0.000	0.004
A2	0.85	1.15	0.033	0.045
A3	0.3	25	0.0	10
b	0.35	0.50	0.014	0.020
С	0.09	0.25	0.004	0.010
D	2.70	3.10	0.106	0.122
E	1.20	1.50	0.047	0.059
е	0.95		0.0	37
HE	2.20	2.60	0.087	0.102
L1	0.20	00	0.008	_
Lp	0.30	2,-3	0.012	-
Q	0.40	0.60	0.016	0.024
х	- ,,	0.10	e <del></del>	0.004

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
b2	-	0.60	_	0.024	
e1	1.70		0.067		
- 11	-3	0.90	-	0.035	

Dimension in mm/inches



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CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
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  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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