PNP -100mA -50V Digital Transistor (Bias Resistor Built-in Transistor)

Datasheet

AEC-Q101 Qualified

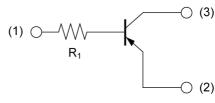
| Parameter | Value |
|------------------|--------|
| V _{CEO} | -50V |
| I _C | -100mA |
| R ₁ | 4.7kΩ |

Outline SOT-723 SC-105AA (1) (VMT3)

Features

- 1) Built-In Biasing Resistor
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary NPN Types: DTC143TM FHA

•Inner circuit



- (1) BASE
- (2) EMITTER
- (3) COLLECTOR

Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

| Part No. | Package | Package size | Taping code | Reel size (mm) | Tape width (mm) | Basic ordering unit.(pcs) | Marking |
|--------------|-------------------|-----------------|----------------|-------------------|-----------------|---------------------------------|---------|
| DTA143TM FHA | SOT-723 (VMT3) | 1212 | T2L | 180 | 8 | 8000 | 93 |

● Absolute maximum ratings (T_a = 25°C)

| Parameter | Symbol | Values | Unit |
|------------------------------|-------------------|-------------|------|
| Collector-base voltage | V_{CBO} | -50 | V |
| Collector-emitter voltage | V_{CEO} | -50 | V |
| Emitter-base voltage | V _{EBO} | -5 | V |
| Collector current | I _C | -100 | mA |
| Power dissipation | P _D *1 | 150 | mW |
| Junction temperature | Tj | 150 | °C |
| Range of storage temperature | T _{stg} | -55 to +150 | င |

● Electrical characteristics (T_a = 25°C)

| Davamatav | Cymah al | Conditions | Values | | | l lm:4 | |
|--------------------------------------|-----------------------------|---|--------|------|------|--------|--|
| Parameter | Parameter Symbol Conditions | | Min. | Тур. | Max. | Unit | |
| Collector-base breakdown voltage | BV _{CBO} | I _C = -50μA | -50 | - | - | V | |
| Collector-emitter breakdown voltage | BV _{CEO} | I _C = -1mA | -50 | - | - | V | |
| Emitter-base breakdown voltage | BV _{EBO} | I _E = -50μA | -5 | - | - | V | |
| Collector cut-off current | I _{CBO} | V _{CB} = -50V | - | - | -500 | nA | |
| Emitter cut-off current | I _{EBO} | V _{EB} = -4V | 1 | - | -500 | nA | |
| Collector-emitter saturation voltage | V _{CE(sat)} | $I_C = -5mA$, $I_B = -0.25mA$ | - | - | -300 | mV | |
| DC current gain | h _{FE} | $V_{CE} = -5V, I_{C} = -1mA$ | 100 | 250 | 600 | - | |
| Input resistance | R ₁ | - | 3.29 | 4.7 | 6.11 | kΩ | |
| Transition frequency | f _T *2 | V _{CE} = -10V, I _E = 5mA, f = 100MHz | - | 250 | - | MHz | |

^{*1} Each terminal mounted on a reference land.

^{*2} Characteristics of built-in transistor

● Electrical characteristic curves (T_a =25°C)

Fig.1 Grounded emitter propagation characteristics

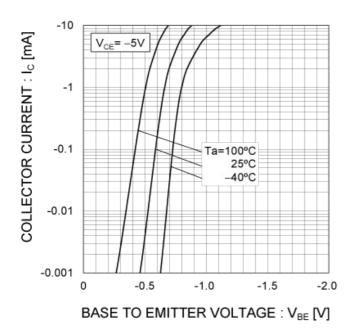


Fig.2 Grounded emitter output characteristics

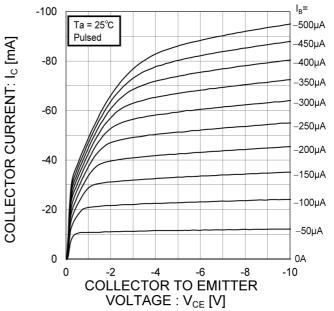


Fig.3 DC Current gain vs. Collector Current

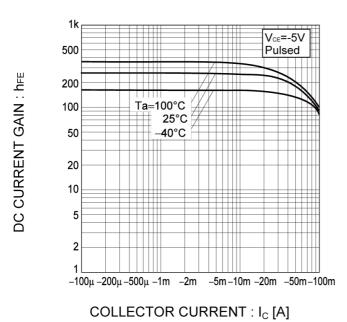
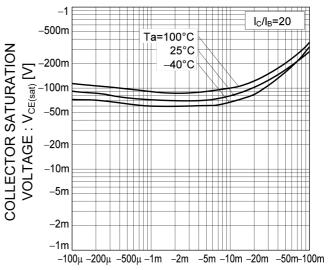
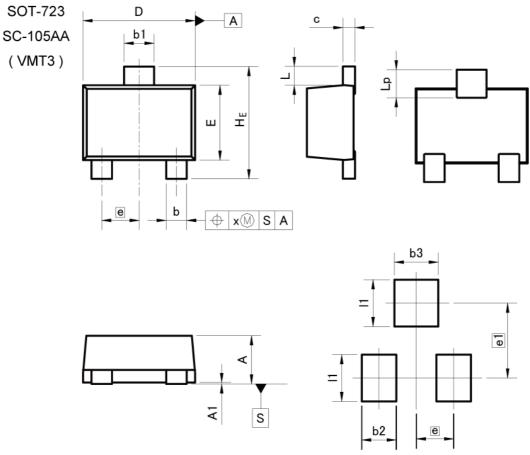


Fig.4 Collector-emitter saturation voltage vs. Collector Current



COLLECTOR CURRENT : I_C [A]

Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

| DIM | MILIM | ETERS | INCHES | | |
|-----|-------|-------|--------|-------|--|
| DIM | MIN | MAX | MIN | MAX | |
| Α | 0.45 | 0.55 | 0.018 | 0.022 | |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 | |
| b | 0.17 | 0.27 | 0.007 | 0.011 | |
| b1 | 0.27 | 0.37 | 0.011 | 0.015 | |
| С | 0.08 | 0.18 | 0.003 | 0.007 | |
| D | 1.10 | 1.30 | 0.043 | 0.051 | |
| E | 0.70 | 0.90 | 0.028 | 0.035 | |
| е | 0. | 40 | 0.0 | 02 | |
| HE | 1.10 | 1.30 | 0.043 | 0.051 | |
| L | 0.10 | 0.30 | 0.004 | 0.012 | |
| Lp | 0.20 | 0.40 | 0.008 | 0.016 | |
| х | - | 0.10 | _ | 0.004 | |

| DIM | MILIM | ETERS | INCHES | | |
|-----|--------|-------|--------|-------|--|
| DIM | MIN | MAX | MIN | MAX | |
| b2 | - | 0.37 | _ | 0.015 | |
| b3 | _ | 0.47 | _ | 0.019 | |
| e1 | 0.80 | | 0.031 | | |
| 11 | - 0.50 | | - | 0.020 | |

Dimension in mm/inches



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| Ť | JÁPAN | USA | EU | CHINA |
|---|----------|----------|------------|---------|
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 - [c] the Products are exposed to direct sunshine or condensation
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- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
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 exceeding the recommended storage time period.
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