PNP -100mA -50V Digital Transistors (Bias Resistor Built-in Transistors)

Datasheet

## **AEC-Q101 Qualified**

Parameter	Value
V <sub>CC</sub>	-50V
I <sub>C(MAX.)</sub>	-100mA
R <sub>1</sub>	4.7kΩ
R <sub>2</sub>	47kΩ

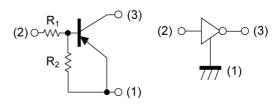
# Outline



## Features

- 1) Built-In Biasing Resistors
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Complementary NPN Types: DTC143ZCA HZG

## •Inner circuit



- (1) GND (+) (EMITTER)
- (2) IN (BASE)
- (3) OUT (COLLECTOR)

# Application

INVERTER, INTERFACE, DRIVER

# Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTA143ZCA HZG	SOT-23 (SST3)	2924	T116	180	8	3000	E13

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

Parameter	Symbol	Values	Unit
Supply voltage	V <sub>CC</sub>	-50	V
Input voltage	V <sub>IN</sub>	-30 to 5	V
Output current	Io	-100	mA
Collector current	I <sub>C(MAX)</sub> *1	-100	mA
Device a dispiration	P <sub>D</sub> *2	200	mW
Power dissipation		350	mW
Junction temperature	Tj	150	°C
Range of storage temperature	T <sub>stg</sub>	-55 to +150	°C

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Dougnoston	C: resh al	Conditions	Values			1.1	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
land the second	$V_{l(off)}$	$V_{CC} = -5V, I_{O} = -100\mu A$	-	-	-0.5	V	
Input voltage	V <sub>I(on)</sub>	$V_O = -0.3V$ , $I_O = -5mA$	-1.3	-	-		
Output voltage	V <sub>O(on)</sub>	I <sub>O</sub> = -5mA, I <sub>I</sub> = -0.25mA	-	-100	-300	mV	
Input current	l <sub>l</sub>	V <sub>I</sub> = -5V	-	-	-1.8	mA	
Output current	I <sub>O(off)</sub>	$V_{CC} = -50V, V_{I} = 0V$	-	-	-500	nA	
DC current gain	G <sub>I</sub>	$V_{O} = -5V, I_{O} = -10mA$	80	-	-	-	
Input resistance	R <sub>1</sub>	-	3.29	4.7	6.11	kΩ	
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	-	8	10	12	-	
Transition frequency $f_T^{\star 1}$		V <sub>CE</sub> = -10V, I <sub>E</sub> = 5mA, f = 100MHz	-	250	-	MHz	

<sup>\*1</sup> Characteristics of built-in transistor.

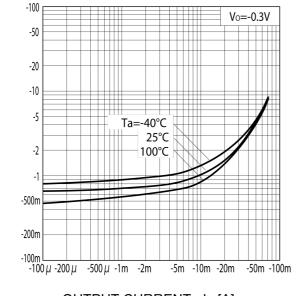
<sup>\*2</sup> Each terminal mounted on a reference land.

<sup>\*3</sup> Mounted on a ceramic board(7.0×5.0×0.6mm).

INPUT VOLTAGE: V<sub>I(on)</sub> [V]

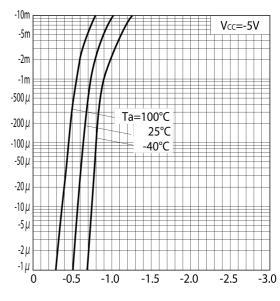
# ● Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)



OUTPUT CURRENT : Io [A]

Fig.2 Output current vs. input voltage (OFF characteristics)



INPUT VOLTAGE: V<sub>I(off)</sub> [V]

Fig.3 Output current vs. output voltage

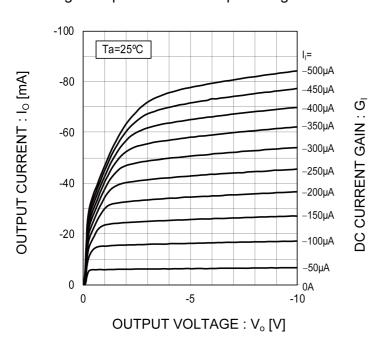
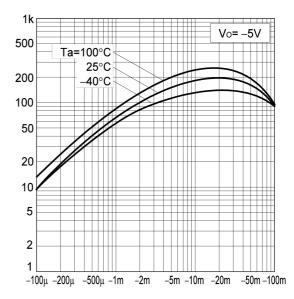


Fig.4 DC current gain vs. output current

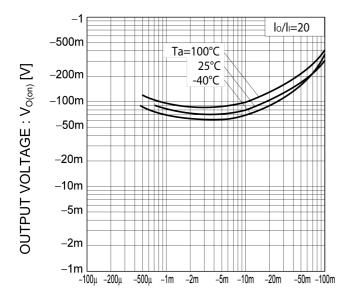


OUTPUT CURRENT : Io [A]

OUTPUT CURRENT : Io [A]

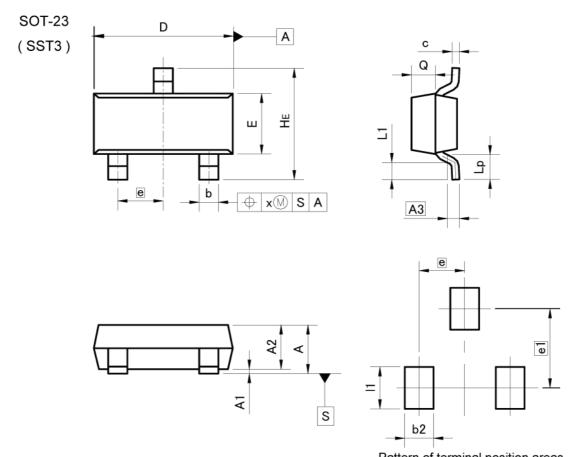
# ●Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.5 Output voltage vs. output current



OUTPUT CURRENT : Io [A]

# Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES	
DIM	MIN		MIN	MAX	
Α	0.90	1.20	0.035	0.047	
A1	0.00	0.10	0.000	0.004	
A2	0.85	1.15	0.033	0.045	
A3	0.3	25	0.0	010	
b	0.35	0.50	0.014	0.020	
С	0.09	0.25	0.004	0.010	
D	2.70	3.10	0.106	0.122	
E	1.20	1.50	0.047	0.059	
е	0.95		0.037		
HE	2.20	2.60	0.087	0.102	
L1	0.20	0.74	0.008	-	
Lp	0.30	9.73	0.012	I	
Q	0.40	0.60	0.016	0.024	
х	- ,	0.10	-	0.004	
	·	·		•	

	DIM	MILIMETERS		INCHES		
DIM		MIN	MAX	MIN	MAX	
	b2		0.60	_	0.024	
	e1	1.	70	0.067		
	11	-2	0.90	-	0.035	

Dimension in mm/inches



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  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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## Precaution for Mounting / Circuit board design

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
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  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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