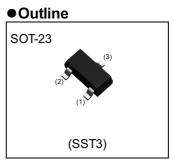
ROHM DTB113ZC

-500mA/-50V Digital transistor (with built-in resistors)

| Value |
|--------|
| -50V |
| -500mA |
| 1kΩ |
| 10kΩ |
| |



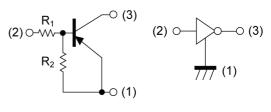
Inner circuit

Features

1) Built-In Biasing Resistors,

 $R_1 = 1.0 k\Omega, R_2 = 10 k\Omega$

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary NPN Type: DTD113ZC



(1) GND (+) (EMITTER)(2) IN (BASE)(3) OUT (COLLECTOR)

Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

| Part No. | Package | Package size | Taping code | Reel size (mm) | Tape width (mm) | Basic ordering unit.(pcs) | Marking |
|----------|------------------|-----------------|----------------|-------------------|--------------------|---------------------------------|---------|
| DTB113ZC | SOT-23 (SST3) | 2924 | T116 | 180 | 8 | 3000 | G11 |

• Absolute maximum ratings ($T_a = 25^{\circ}C$)

| Parameter | Symbol | Values | Unit |
|------------------------------|------------------------|-------------|------|
| Supply voltage | V _{CC} | -50 | V |
| Input voltage | V _{IN} | -10 to 5 | V |
| Collector current | I _{C(MAX)} *1 | -500 | mA |
| Power dissipation | P _D *2 | 200 | mW |
| Junction temperature | Τ _j | 150 | °C |
| Range of storage temperature | T _{stg} | -55 to +150 | °C |

• Electrical characteristics (T_a = 25°C)

| Deremeter | Symbol Conditions - | | Values | | | 1.1 |
|----------------------|--|--|--------|------|------|------|
| Parameter | | | Min. | Тур. | Max. | Unit |
| Inputivoltogo | V _{I(off)} | V _{CC} = -5V, I _O = -100µA | - | - | -0.3 | V |
| Input voltage | V _{I(on)} | V _O = -0.3V, I _O = -20mA | -3.0 | - | - | V |
| Output voltage | V _{O(on)} | I _O = -50mA, I _I = -2.5mA | - | -100 | -300 | mV |
| Input current | I ₁ V ₁ = -5V | | - | - | -7.2 | mA |
| Output current | I _{O(off)} | V _{CC} = -50V, V _I = 0V | - | - | -500 | nA |
| DC current gain | current gain G_1^{*3} $V_0 = -5V$, $I_0 = -50$ mA | | 56 | - | - | - |
| Input resistance | R ₁ | - | 0.7 | 1 | 1.3 | kΩ |
| Resistance ratio | R_2/R_1 | - | 8 | 10 | 12 | - |
| Transition frequency | f _T *1 | V _{CE} = -10V, I _E = 50mA, f = 100MHz | - | 200 | - | MHz |

*1 Characteristics of built-in transistor

*2 Each terminal mounted on a reference land.

*3 Pulsed



•Electrical characteristic curves (T_a =25°C)

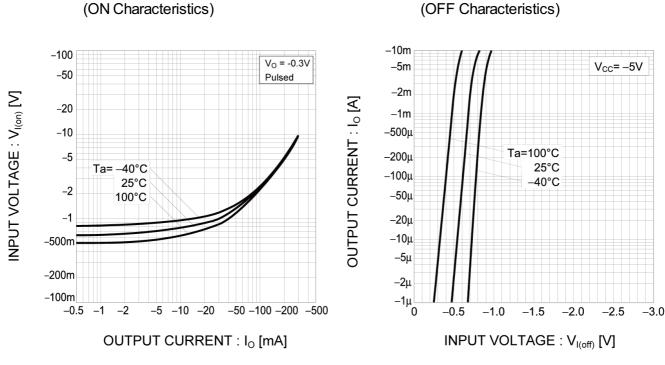
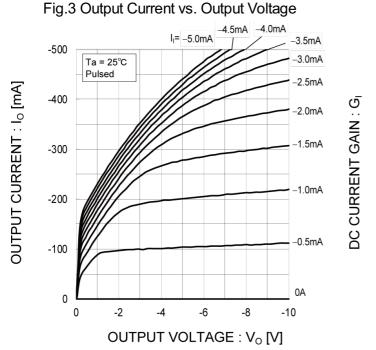
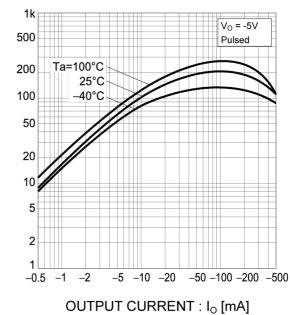


Fig.1 Input Voltage vs. Output Current (ON Characteristics)

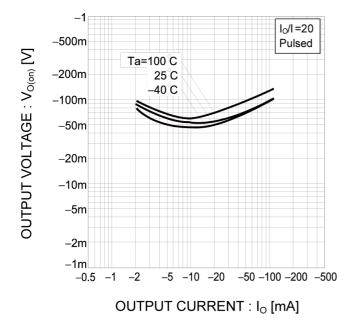
Fig.4 DC Current Gain vs. Output Current

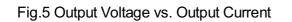
Fig.2 Output Current vs. Input Voltage





•Electrical characteristic curves (T_a =25°C)

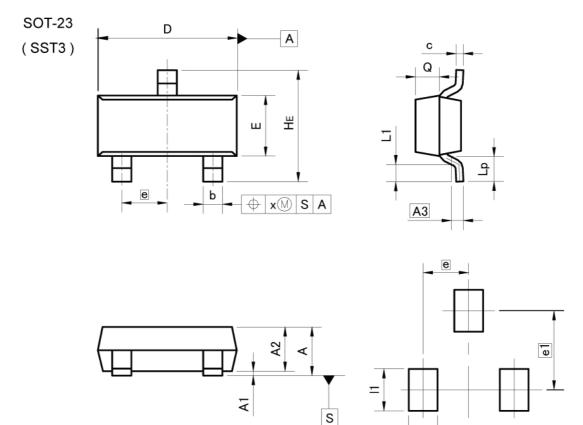






DTB113ZC

Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

b2

| DIM | MILIM | ETERS | INCHES | | |
|-----|-------|-------|--------|-------|--|
| DIM | MIN | MAX | MIN | MAX | |
| Α | 0.90 | 1.20 | 0.035 | 0.047 | |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 | |
| A2 | 0.85 | 1.15 | 0.033 | 0.045 | |
| A3 | 0.: | 25 | 0.0 |)10 | |
| b | 0.35 | 0.50 | 0.014 | 0.020 | |
| С | 0.09 | 0.25 | 0.004 | 0.010 | |
| D | 2.70 | 3.10 | 0.106 | 0.122 | |
| E | 1.20 | 1.50 | 0.047 | 0.059 | |
| е | 0.9 | 95 | 0.037 | | |
| HE | 2.20 | 2.60 | 0.087 | 0.102 | |
| L1 | 0.20 | - | 0.008 | - | |
| Lp | 0.30 | - | 0.012 | | |
| Q | 0.40 | 0.60 | 0.016 | 0.024 | |
| х | - | 0.10 | - | 0.004 | |
| | | | | | |
| | MILIM | ETEDS | INC | HES | |

| DIM | MILIM | ETERS | INCHES | | |
|-----|-------|-------|--------|-------|--|
| DIW | MIN | MAX | MIN | MAX | |
| b2 | - | 0.60 | - | 0.024 | |
| e1 | 1.1 | 70 | 0.067 | | |
| 1 | - | 0.90 | - | 0.035 | |

Dimension in mm/inches



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|---|--------|--------|------------|---------|
| ſ | CLASSⅢ | | CLASS II b | |
| | CLASSⅣ | CLASSⅢ | CLASSⅢ | CLASSII |

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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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