NPN 100mA 50V Digital Transistor (Bias Resistor Built-in Transistor)

AEC-Q101 Qualified

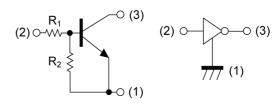
Parameter	Value
V _{CC}	50V
I _{C(MAX.)}	100mA
R ₁	10kΩ
R ₂	10kΩ

● Outline SOT-323 SC-70 (2) (1) (UMT3)

Features

- 1) Built-In Biasing Resistors, $R_1 = R_2 = 10k\Omega$
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary PNP Types: DTA114EU3 HZG

•Inner circuit



- (1) GND (EMITTER)
- (2) IN (BASE)
- (3) OUT (COLLECTOR)

Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTC114EU3 HZG	SOT-323 (UMT3)	2021	T106	180	8	3000	24

● Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Values	Unit
Supply voltage	V _{CC}	50	V
Input voltage	V _{IN}	-10 to 40	V
Output current	Io	50	mA
Collector current	I _{C(MAX)} *1	100	mA
Power dissipation	P _D *2	200	mW
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

●Electrical characteristics (T_a = 25°C)

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Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input voltage	$V_{I(off)}$	V _{CC} = 5V, I _O = 100μA	-	-	0.5	- v	
Input voltage	V _{I(on)}	V _O = 0.3V, I _O = 10mA	3.0	-	-		
Output voltage	V _{O(on)}	I _O = 10mA, I _I = 0.5mA	-	100	300	mV	
Input current	I _I	V _I = 5V	-	-	880	μA	
Output current	I _{O(off)}	$V_{CC} = 50V, V_{I} = 0V$	-	-	500	nA	
DC current gain	G _I	$V_{O} = 5V, I_{O} = 5mA$	30	-	-	-	
Input resistance	R ₁	-	7	10	13	kΩ	
Resistance ratio	R ₂ /R ₁	-	0.8	1.0	1.2	-	
Transition frequency	f _T *1	$V_{CE} = 10V, I_{E} = -5mA,$ f = 100MHz	-	250	-	MHz	

^{*1} Characteristics of built-in transistor.

^{*2} Each terminal mounted on a reference land.

● Electrical characteristic curves (T_a =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)

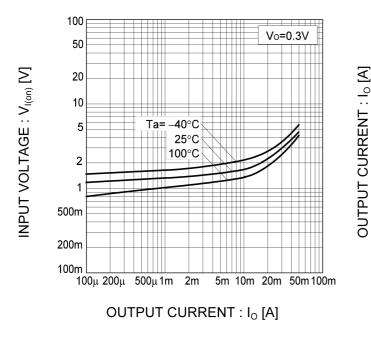
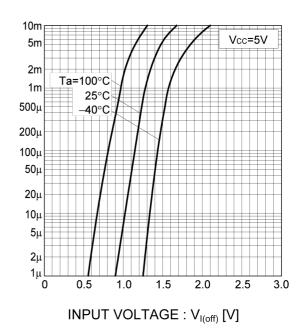


Fig.2 Output current vs. input voltage (OFF characteristics)



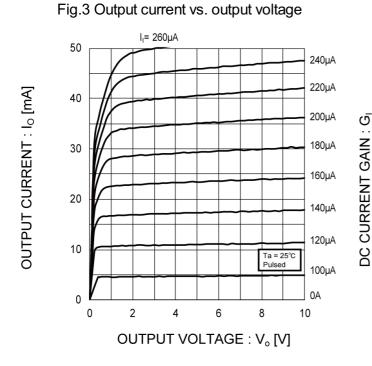
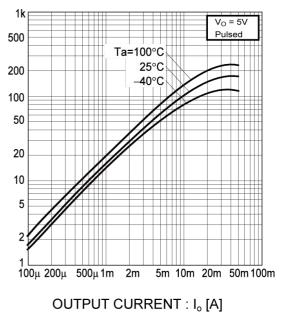
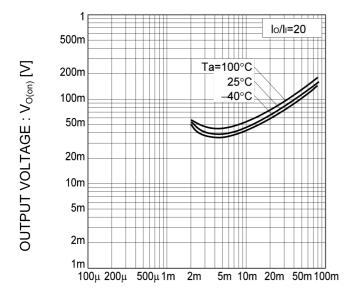


Fig.4 DC current gain vs. output current



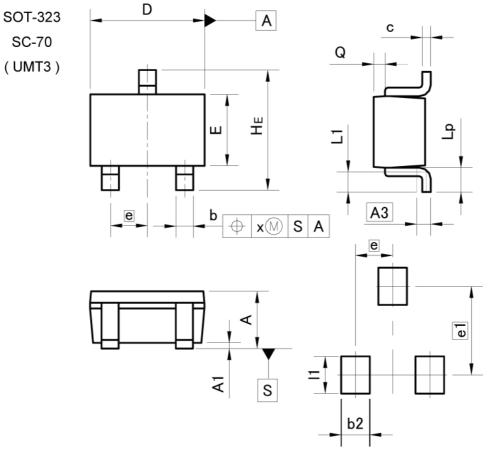
●Electrical characteristic curves (T_a =25°C)

Fig.5 Output voltage vs. output current



OUTPUT CURRENT : I_{\circ} [A]

Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	MILIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX		
Α	0.80	1.00	0.031	0.039		
A1	0.00	0.10	0.000	0.004		
A3	0.5	25	0.010			
b	0.25	0.40	0.010	0.016		
С	0.10	0.20	0.004	0.008		
D	1.90	2.10	0.075	0.083		
E	1.15	1.35	0.045	0.053		
е	0.65		0.026			
HE	2.00	2.20	0.079	0.087		
L1	0.10	0.40	0.004	0.016		
Lp	0.25	0.55	0.010	0.022		
Q	0.10	0.30	0.004	0.012		
х	_	0.10	_	0.004		

DIM	MILIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
b2	_	- 0.50		0.020	
e1	1.55		0.0	61	
11	- 0.65		-	0.026	

Dimension in mm/inches

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Ì	JÁPAN	USA	EU	CHINA
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	CLASSIV	CLASSIII	CLASSIII	CLASSII

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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