

Parameter	Value
V_{CC}	50V
$I_{C(MAX.)}$	100mA
R_1	100k Ω
R_2	100k Ω

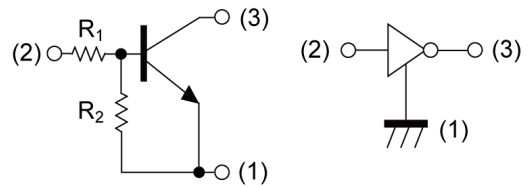
●Outline



●Features

- 1) Built-In Biasing Resistors, $R_1 = R_2 = 100k\Omega$
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit) .
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary PNP Types: DTA115ECA

●Inner circuit



- (1) GND (EMITTER)
- (2) IN (BASE)
- (3) OUT (COLLECTOR)

●Application

INVERTER, INTERFACE, DRIVER

●Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTC115ECA	SOT-23 (SST3)	2924	T116	180	8	3000	29

● **Absolute maximum ratings** ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Values	Unit
Supply voltage	V_{CC}	50	V
Input voltage	V_{IN}	-10 to 40	V
Output current	I_O	20	mA
Collector current	$I_{C(MAX)}^{*1}$	100	mA
Power dissipation	P_D^{*2}	200	mW
	P_D^{*3}	350	mW
Junction temperature	T_j	150	$^\circ\text{C}$
Range of storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

● **Electrical characteristics** ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input voltage	$V_{I(off)}$	$V_{CC} = 5V, I_O = 100\mu\text{A}$	-	-	0.5	V
	$V_{I(on)}$	$V_O = 0.3V, I_O = 1\text{mA}$	3.0	-	-	
Output voltage	$V_{O(on)}$	$I_O = 5\text{mA}, I_I = 0.25\text{mA}$	-	100	300	mV
Input current	I_I	$V_I = 5V$	-	-	150	μA
Output current	$I_{O(off)}$	$V_{CC} = 50V, V_I = 0V$	-	-	500	nA
DC current gain	G_I	$V_O = 5V, I_O = 5\text{mA}$	82	-	-	-
Input resistance	R_1	-	70	100	130	$\text{k}\Omega$
Resistance ratio	R_2/R_1	-	0.8	1.0	1.2	-
Transition frequency	f_T^{*1}	$V_{CE} = 10V, I_E = -5\text{mA}, f = 100\text{MHz}$	-	250	-	MHz

*1 Characteristics of built-in transistor

*2 Each terminal mounted on a reference land

*3 Mounted on a ceramic board(7.0×5.0×0.6mm).

●Electrical characteristic curves ($T_a=25^\circ\text{C}$)

Fig.1 Input voltage vs. output current (ON characteristics)

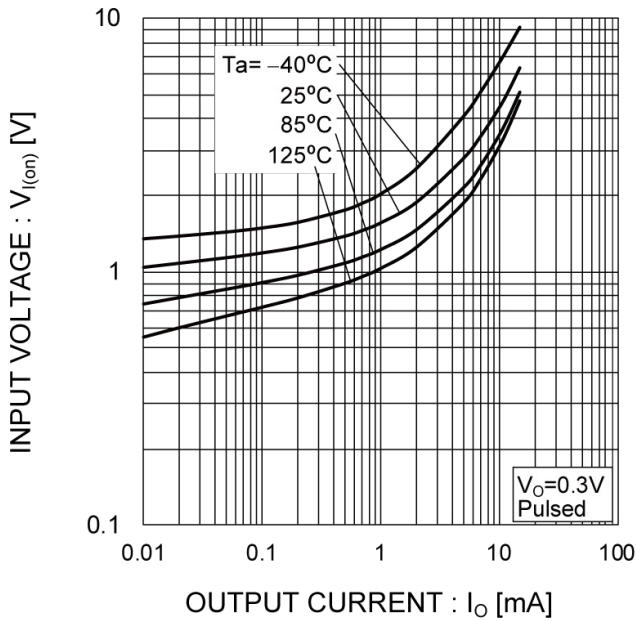


Fig.2 Output current vs. input voltage (OFF characteristics)

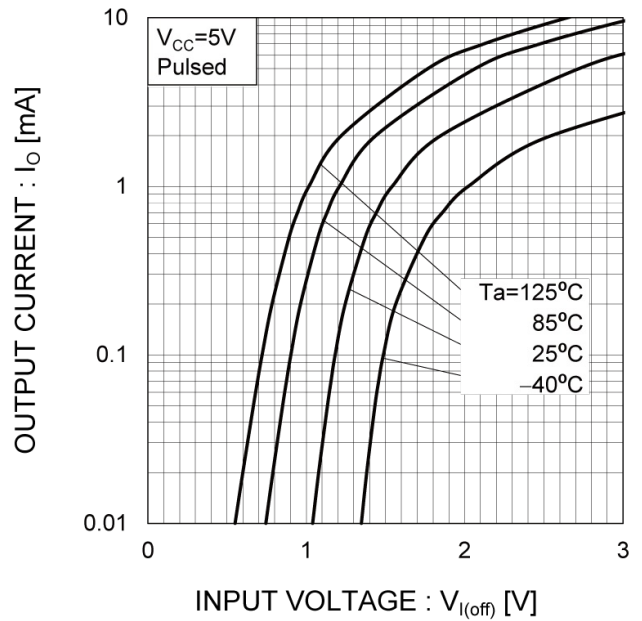


Fig.3 Output current vs. output voltage

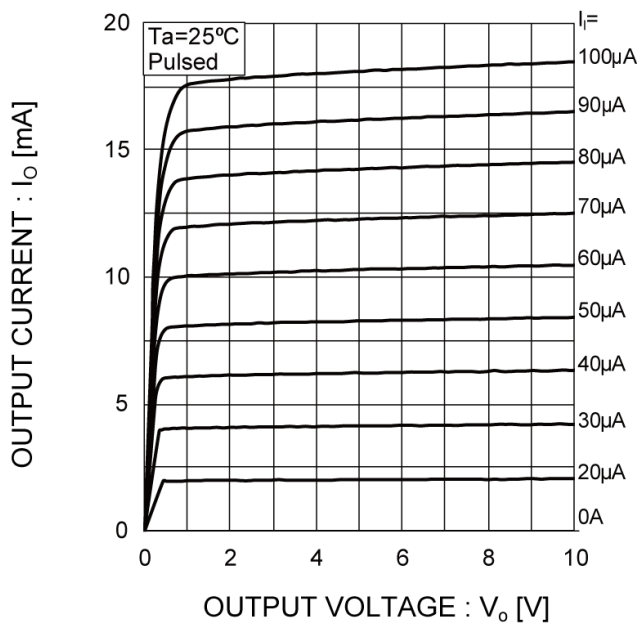
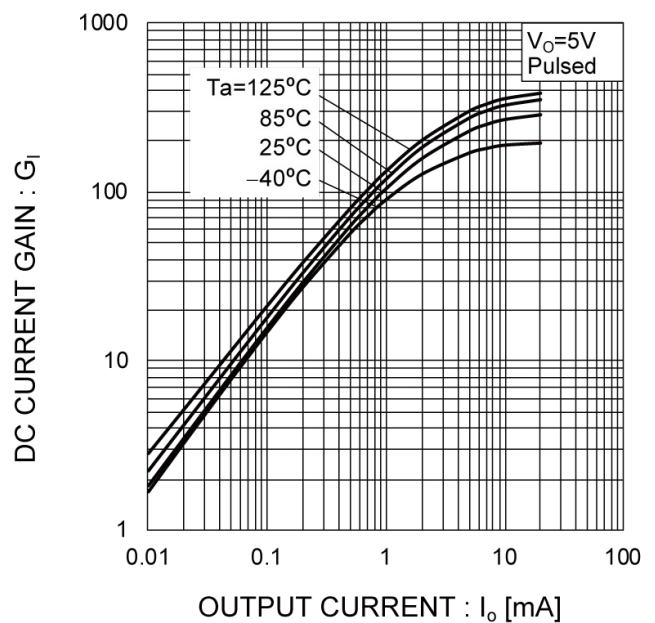
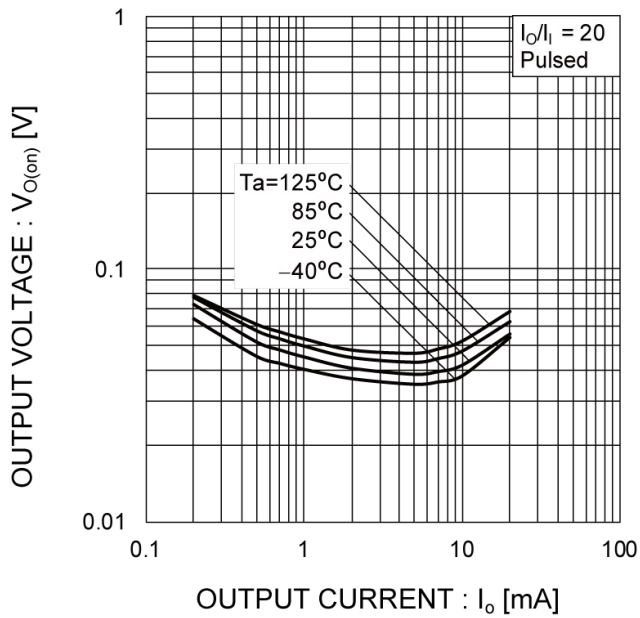


Fig.4 DC current gain vs. output current



● Electrical characteristic curves ($T_a = 25^\circ\text{C}$)

Fig.5 Output voltage vs. output current



●Dimensions

SOT-23
(SST3)



Pattern of terminal position areas
[Not a pattern of soldering pads]

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.90	1.20	0.035	0.047
A1	0.00	0.10	0.000	0.004
A2	0.85	1.15	0.033	0.045
A3	0.25		0.010	
b	0.35	0.50	0.014	0.020
c	0.09	0.25	0.004	0.010
D	2.70	3.10	0.106	0.122
E	1.20	1.50	0.047	0.059
e	0.95		0.037	
HE	2.20	2.60	0.087	0.102
L1	0.20	-	0.008	-
Lp	0.30	-	0.012	-
Q	0.40	0.60	0.016	0.024
x	-	0.10	-	0.004

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b2	-	0.60	-	0.024
e1	1.70		0.067	
l1	-	0.90	-	0.035

Dimension in mm/inches

Notice

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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

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 - Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
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 - Use of the Products in places subject to dew condensation
- The Products are not subject to radiation-proof design.
- Please verify and confirm characteristics of the final or mounted products in using the Products.
- In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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