NPN 100mA 50V Digital Transistor (Bias Resistor Built-in Transistors

Datasheet

AEC-Q101 Qualified

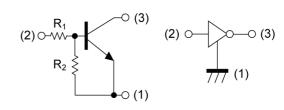
| Parameter | Value |
|----------------------|-------|
| V _{CC} | 50V |
| I _{C(MAX.)} | 100mA |
| R ₁ | 2.2kΩ |
| R ₂ | 2.2kΩ |

● Outline SOT-323 SC-70 (2) (1) (UMT3)

Features

- 1) Built-In Biasing Resistors, $R_1 = R_2 = 2.2k\Omega$
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary PNP Types: DTA123EU3 HZG

•Inner circuit



- (1) GND (EMITTER)
- (2) IN (BASE)
- (3) OUT (COLLECTOR)

Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

| Part No. | Package | Package size | Taping code | Reel size (mm) | Tape width (mm) | Basic ordering unit.(pcs) | Marking |
|---------------|-------------------|-----------------|----------------|-------------------|-----------------|---------------------------------|---------|
| DTC123EU3 HZG | SOT-323 (UMT3) | 2021 | T106 | 180 | 8 | 3000 | 22 |

● Absolute maximum ratings (T_a = 25°C)

| Parameter | Symbol | Values | Unit |
|------------------------------|------------------------|-------------|------|
| Supply voltage | V _{CC} | 50 | V |
| Input voltage | V _{IN} | -10 to 12 | V |
| Output current | Io | 100 | mA |
| Collector current | I _{C(MAX)} *1 | 100 | mA |
| Power dissipation | P _D *2 | 200 | mW |
| Junction temperature | Tj | 150 | °C |
| Range of storage temperature | T _{stg} | -55 to +150 | °C |

• Electrical characteristics $(T_a = 25^{\circ}C)$

| Davanatav | Cy reads ad | Conditions | Values | | | 1.114 | |
|----------------------|--------------------------------|---|--------|------|------|-------|--|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit | |
| Input voltage | $V_{I(off)}$ | V _{CC} = 5V, I _O = 100μA | - | - | 0.5 | - | |
| Input voltage | V _{I(on)} | $V_{O} = 0.3V, I_{O} = 20mA$ | 3.0 | - | - | | |
| Output voltage | V _{O(on)} | I _O = 10mA, I _I = 0.5mA | - | 100 | 300 | mV | |
| Input current | l _l | V _I = 5V | - | - | 3.8 | mA | |
| Output current | I _{O(off)} | $V_{CC} = 50V, V_{I} = 0V$ | - | - | 500 | nA | |
| DC current gain | G _I | $V_{O} = 5V, I_{O} = 20mA$ | 20 | - | - | - | |
| Input resistance | R ₁ | - | 1.54 | 2.2 | 2.86 | kΩ | |
| Resistance ratio | R ₂ /R ₁ | - | 0.8 | 1.0 | 1.2 | - | |
| Transition frequency | f _T *1 | V _{CE} = 10V, I _E = -5mA, f = 100MHz | - | 250 | - | MHz | |

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference land.

● Electrical characteristic curves (T_a =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)

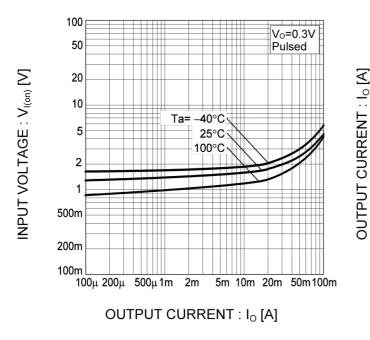


Fig.2 Output current vs. input voltage (OFF characteristics)

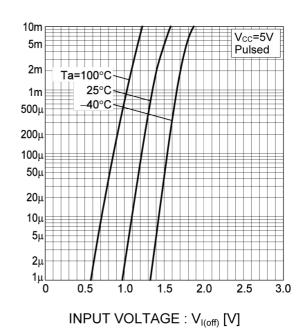


Fig.3 Output current vs. output voltage

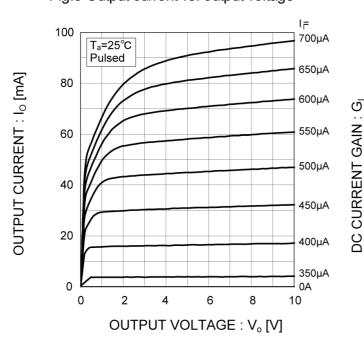
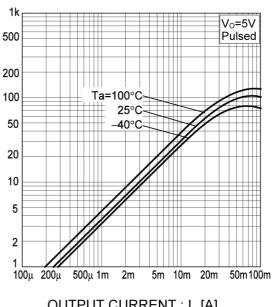
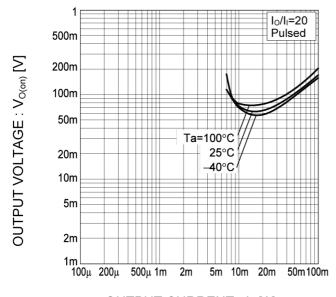


Fig.4 DC current gain vs. output current



● Electrical characteristic curves (T_a =25°C)

Fig.5 Output voltage vs. output current



OUTPUT CURRENT : Io [A]

Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

| DIM | MILIM | ETERS | INCHES | | |
|-----|-------|-------|--------|-------|--|
| DIM | MIN | MAX | MIN | MAX | |
| Α | 0.80 | 1.00 | 0.031 | 0.039 | |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 | |
| A3 | 0.5 | 25 | 0.0 | 10 | |
| b | 0.25 | 0.40 | 0.010 | 0.016 | |
| С | 0.10 | 0.20 | 0.004 | 0.008 | |
| D | 1.90 | 2.10 | 0.075 | 0.083 | |
| E | 1.15 | 1.35 | 0.045 | 0.053 | |
| е | 0.65 | | 0.026 | | |
| HE | 2.00 | 2.20 | 0.079 | 0.087 | |
| L1 | 0.10 | 0.40 | 0.004 | 0.016 | |
| Lp | 0.25 | 0.55 | 0.010 | 0.022 | |
| Q | 0.10 | 0.30 | 0.004 | 0.012 | |
| х | _ | 0.10 | _ | 0.004 | |

| DIM | MILIM | MILIMETERS | | INCHES | | |
|------|--------|------------|-----|--------|--|--|
| DIM | MIN | MAX | MIN | MAX | | |
| b2 | _ | 0.50 | _ | 0.020 | | |
| e1 | 1.55 | | 0.0 | 61 | | |
| - 11 | - 0.65 | | - | 0.026 | | |

Dimension in mm/inches



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| Ť | JÁPAN | USA | EU | CHINA |
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