NPN 100mA 50V Digital Transistor (Bias Resistor Built-in Transistor)

## Datasheet

#### **AEC-Q101 Qualified**

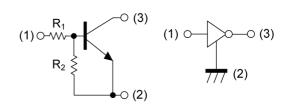
| Parameter            | Value |
|----------------------|-------|
| V <sub>CC</sub>      | 50V   |
| I <sub>C(MAX.)</sub> | 100mA |
| R <sub>1</sub>       | 47kΩ  |
| R <sub>2</sub>       | 47kΩ  |

# Outline SOT-416FL SC-89 (EMT3F)

#### Features

- 1) Built-In Biasing Resistors,  $R_1 = R_2 = 47k\Omega$
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary PNP Types: DTA144EEB HZG

#### •Inner circuit



- (1) IN (BASE)
- (2) GND (EMITTER)
- (3) OUT (COLLECTOR)

## Application

INVERTER, INTERFACE, DRIVER

### Packaging specifications

| Part No.      | Package              | Package<br>size | Taping<br>code | Reel size<br>(mm) | Tape width (mm) | Basic<br>ordering<br>unit.(pcs) | Marking |
|---------------|----------------------|-----------------|----------------|-------------------|-----------------|---------------------------------|---------|
| DTC144EEB HZG | SOT-416FL<br>(EMT3F) | 1616            | TL             | 180               | 8               | 3000                            | 26      |

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

| Parameter                    | Symbol                 | Values      | Unit |
|------------------------------|------------------------|-------------|------|
| Supply voltage               | V <sub>CC</sub>        | 50          | V    |
| Input voltage                | V <sub>IN</sub>        | -10 to 40   | V    |
| Output current               | Io                     | 30          | mA   |
| Collector current            | I <sub>C(MAX)</sub> *1 | 100         | mA   |
| Power dissipation            | P <sub>D</sub> *2      | 150         | mW   |
| Junction temperature         | T <sub>j</sub>         | 150         | °C   |
| Range of storage temperature | T <sub>stg</sub>       | -55 to +150 | °C   |

# • Electrical characteristics $(T_a = 25^{\circ}C)$

| Davamatav            | Cymahal  | Canditions                                    | Values |      |      | l limit |  |
|----------------------|--|---|--------|------|------|---------|--|
| Parameter            | Symbol   | Conditions                                    | Min.   | Тур. | Max. | Unit    |  |
| Input voltage        | $V_{I(off)}$   | V <sub>CC</sub> = 5V, I <sub>O</sub> = 100μA  | -      | -    | 0.5  | \/      |  |
| Input voltage        | V <sub>I(on)</sub>                                       | $V_O = 0.3V$ , $I_O = 2mA$                    | 3.0    | -    | -    | V       |  |
| Output voltage       | V <sub>O(on)</sub>                                       | I <sub>O</sub> = 10mA, I <sub>I</sub> = 0.5mA | -      | 100  | 300  | mV      |  |
| Input current        | l <sub>l</sub>   | V <sub>I</sub> = 5V                           | -      | -    | 180  | μA      |  |
| Output current       | I <sub>O(off)</sub>                                      | $V_{CC} = 50V, V_{I} = 0V$                    | -      | -    | 500  | nA      |  |
| DC current gain      | G <sub>I</sub>   | $V_{O} = 5V, I_{O} = 5mA$                     | 68     | -    | -    | -       |  |
| Input resistance     | R <sub>1</sub>   | -   | 32.9   | 47   | 61.1 | kΩ      |  |
| Resistance ratio     | R <sub>2</sub> /R <sub>1</sub>                           | -   | 0.8    | 1.0  | 1.2  | -       |  |
| Transition frequency | $f_{T}^{*1}$ $V_{CE} = 10V, I_{E} = -5mA,$<br>f = 100MHz |   | -      | 250  | -    | MHz     |  |

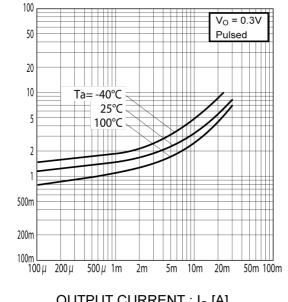
<sup>\*1</sup> Characteristics of built-in transistor

<sup>\*2</sup> Each terminal mounted on a reference land.

INPUT VOLTAGE: V<sub>I(on)</sub> [V]

## ● Electrical characteristic curves (T<sub>a</sub> =25°C)

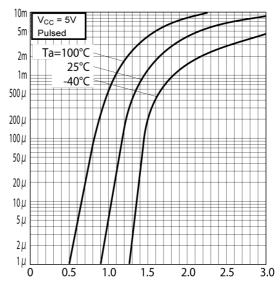
Fig.1 Input voltage vs. output current (ON characteristics)



OUTPUT CURRENT: Io [A]

Fig.2 Output current vs. input voltage (OFF characteristics)

OUTPUT CURRENT : I<sub>o</sub> [A]



INPUT VOLTAGE :  $V_{I(off)}$  [V]

Fig.3 Output current vs. output voltage

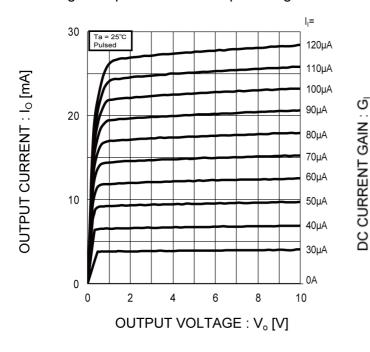
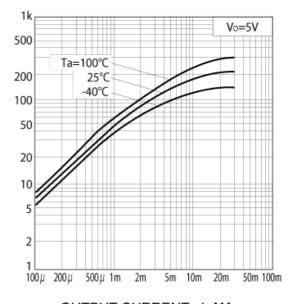


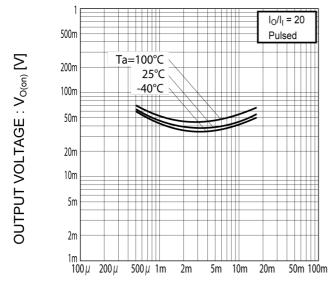
Fig.4 DC current gain vs. output current



OUTPUT CURRENT: Io [A]

## ●Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.5 Output voltage vs. output current



OUTPUT CURRENT : Io [A]

## Dimensions



| DIM | MILIMETERS |      | INCHES |       |  |
|-----|------------|------|--------|-------|--|
| DIM | MIN        | MAX  | MIN    | MAX   |  |
| Α   | 0.65       | 0.85 | 0.026  | 0.033 |  |
| A1  | 0.00       | 0.10 | 0.000  | 0.004 |  |
| A2  | 0.60       | 0.80 | 0.024  | 0.031 |  |
| b   | 0.21       | 0.36 | 0.008  | 0.014 |  |
| С   | 0.08       | 0.18 | 0.003  | 0.007 |  |
| D   | 1.50       | 1.70 | 0.059  | 0.067 |  |
| E   | 0.76       | 0.96 | 0.030  | 0.038 |  |
| е   | 0.50       |      | 0.020  |       |  |
| HE  | 1.50       | 1.70 | 0.059  | 0.067 |  |
| L   | 0.37       |      | 0.015  |       |  |
| Lp  | 0.35       | 0.55 | 0.014  | 0.022 |  |
| х   | -          | 0.10 | -      | 0.004 |  |

| DIM  | MILIM | ETERS | INCHES |       |  |
|------|-------|-------|--------|-------|--|
| DIM  | MIN   | MAX   | MIN    | MAX   |  |
| b2   | _     | 0.46  | _      | 0.018 |  |
| e1   | _     | 1.05  | -      | 0.041 |  |
| - 11 | -     | 0.65  | -      | 0.026 |  |

Dimension in mm/inches



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| (itata i) inadicai zgaipinani alacamatan al tira apacina ippinationa |          |            |          |  |  |  |
|--|----------|------------|----------|--|--|--|
| JAPAN  | USA      | EU         | CHINA    |  |  |  |
| CLASSⅢ   | CLASSIII | CLASS II b | CLASSⅢ   |  |  |  |
| CLASSIV  |          | CLASSⅢ     | CLASSIII |  |  |  |

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  - [d] the Products are exposed to high Electrostatic
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