NPN 100mA 50V Digital Transistor (Bias Resistor Built-in Transistor)

Datasheet

AEC-Q101 Qualified

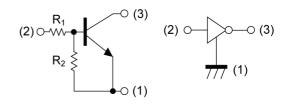
| Parameter | Value | |
|----------------------|-------|--|
| V _{CC} | 50V | |
| I _{C(MAX.)} | 100mA | |
| R ₁ | 47kΩ | |
| R_2 | 47kΩ | |

● Outline SOT-416 SC-75A (2) (EMT3)

Features

- 1) Built-In Biasing Resistors, $R_1 = R_2 = 47k\Omega$
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary PNP Types: DTA144EE FRA

•Inner circuit



- (1) GND (EMITTER)
- (2) IN (BASE)
- (3) OUT (COLLECTOR)

Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

| Part No. | Package | Package size | Taping code | Reel size (mm) | Tape width (mm) | Basic ordering unit.(pcs) | Marking |
|--------------|-------------------|-----------------|----------------|-------------------|-----------------|---------------------------------|---------|
| DTC144EE FRA | SOT-416 (EMT3) | 1616 | TL | 180 | 8 | 3000 | 26 |

● Absolute maximum ratings (T_a = 25°C)

| Parameter | Symbol | Values | Unit |
|------------------------------|------------------------|-------------|------|
| Supply voltage | V _{CC} | 50 | V |
| Input voltage | V _{IN} | -10 to 40 | V |
| Output current | Io | 30 | mA |
| Collector current | I _{C(MAX)} *1 | 100 | mA |
| Power dissipation | P _D *2 | 150 | mW |
| Junction temperature | T _j | 150 | °C |
| Range of storage temperature | T _{stg} | -55 to +150 | °C |

● Electrical characteristics (T_a = 25°C)

| Davanatav | Cy reads ad | Conditions | Values | | | 1.114 | |
|--|--------------------------------|---|--------|------|------|-------|--|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit | |
| Input voltage | $V_{I(off)}$ | V _{CC} = 5V, I _O = 100μA | - | - | 0.5 | - v | |
| Input voltage | V _{I(on)} | $V_O = 0.3V$, $I_O = 2mA$ | 3.0 | - | - | | |
| Output voltage | V _{O(on)} | I _O = 10mA, I _I = 0.5mA | - | 100 | 300 | mV | |
| Input current | I _I | V _I = 5V | - | - | 180 | μΑ | |
| Output current I _{O(off)} V _{CC} | | $V_{CC} = 50V, V_{I} = 0V$ | - | - | 500 | nA | |
| DC current gain | G _I | $V_{O} = 5V, I_{O} = 5mA$ | 68 | - | - | - | |
| Input resistance | R ₁ | - | 32.9 | 47 | 61.1 | kΩ | |
| Resistance ratio | R ₂ /R ₁ | - | 0.8 | 1.0 | 1.2 | - | |
| Transition frequency | f _T *1 | V _{CE} = 10V, I _E = -5mA, f = 100MHz | - | 250 | - | MHz | |

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference land.

● Electrical characteristic curves (T_a =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)

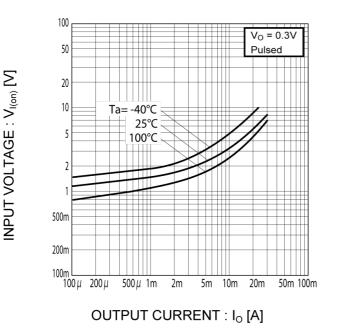


Fig.2 Output current vs. input voltage (OFF characteristics)

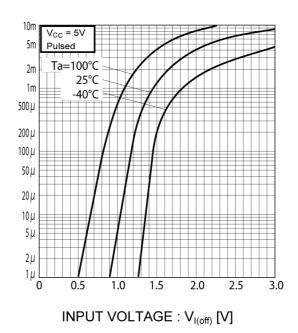


Fig.3 Output current vs. output voltage

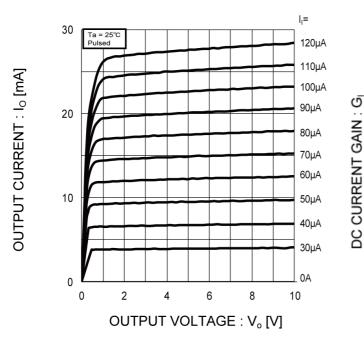
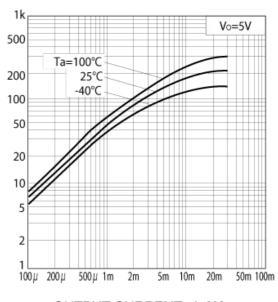


Fig.4 DC current gain vs. output current

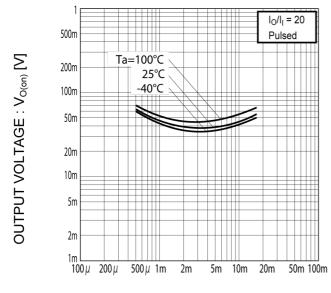


OUTPUT CURRENT: Io [A]

OUTPUT CURRENT : I_o [A]

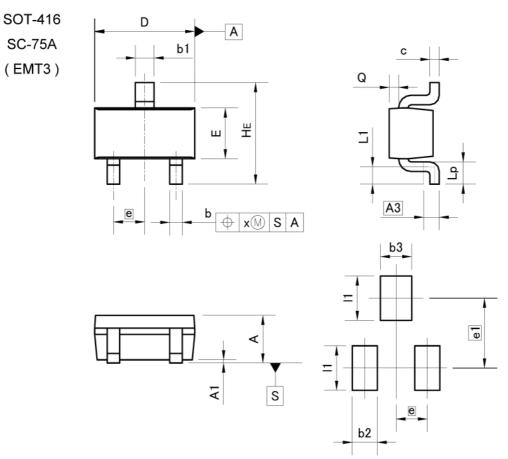
●Electrical characteristic curves (T_a =25°C)

Fig.5 Output voltage vs. output current



OUTPUT CURRENT : Io [A]

Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

| DIM | MILIM | ETERS | INCHES | | |
|-----|-------|-------|------------------|----------------|--|
| DIM | MIN | MAX | MIN | MAX | |
| Α | 0.60 | 0.80 | 0.024 | 0.031 | |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 | |
| A3 | 0. | 25 | 25 0.0 | | |
| b | 0.15 | 0.30 | 0.006 | 0.012 | |
| b1 | 0.25 | 0.40 | 0.010 | 0.016 | |
| С | 0.10 | 0.20 | 0.004 | 0.008 | |
| D | 1.50 | 1.70 | 0.059 | 0.067 | |
| E | 0.70 | 0.90 | 0.028 | 0.035 | |
| е | 0. | 50 | 0.0 | 20 | |
| HE | 1.40 | 1.80 | 0.055 | 0.071 | |
| L1 | 0.10 | - | 0.004 | - | |
| Lp | 0.15 | - | 0.006 | % - | |
| Q | 0.05 | 0.25 | 0.002 | 0.010 | |
| х | - | 0.10 | , - , | 0.004 | |

| DIM | MILIM | ETERS | INCHES | | |
|-----|-------|-------|--------|----------------|--|
| DIM | MIN | MAX | MIN | MAX | |
| b2 | 1 | 0.40 | - | 0.016 0.020 | |
| b3 | I | 0.50 | - | | |
| e1 | 1.10 | | 0.0 | 43 | |
| l1 | | | | 0.028 | |

Dimension in mm/inches



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| Ť | JÁPAN | USA | EU | CHINA |
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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
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 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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 - [c] the Products are exposed to direct sunshine or condensation
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- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
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