500mA/50V Digital transistor (with built-in resistors)

| Parameter | Value | | |
|-----------------|-------|--|--|
| V _{CC} | 50V | | |
| I _C | 500mA | | |
| R ₁ | 10kΩ | | |
| R ₂ | 10kΩ | | |

SOT-346 SC-59

(SMT3)

Features

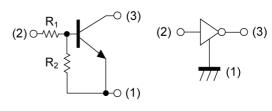
- 1)Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.(see equivalent circuit)
- 2)The bias resistors consist of thin-film resistors with complete isolation to allow negative

biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.

3)Only the on/off conditions need to be set for operation, making the device desigh easy.

•Inner circuit

Outline



- (1) GND (EMITTER)
- (2) IN (BASE)
- (3) OUT (COLLECTOR)

Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

| Part No. | Package | Package size | Taping code | Reel size (mm) | Tape width (mm) | Basic ordering unit.(pcs) | Marking |
|----------|-------------------|-----------------|----------------|-------------------|-----------------|---------------------------------|---------|
| DTD114EK | SOT-346 (SMT3) | 2928 | T146 | 180 | 8 | 3000 | F24 |

● Absolute maximum ratings (T_a = 25°C)

| Parameter | Symbol | Values | Unit |
|------------------------------|-------------------|-------------|------|
| Supply voltage | V _{CC} | 50 | V |
| Input voltage | V _{IN} | -10 to 40 | V |
| Collector current | I _C *1 | 500 | mA |
| Power dissipation | P _D *2 | 200 | mW |
| Junction temperature | T _j | 150 | °C |
| Range of storage temperature | T _{stg} | -55 to +150 | °C |

● Electrical characteristics (T_a = 25°C)

| Danamatan | C: resh al | Conditions | Values | | | 11.7 | |
|----------------------|--------------------------------|--|--------|------|------|------|--|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit | |
| Input valtage | $V_{I(off)}$ | V _{CC} = 5V, I _O = 100μA | - | - | 0.5 | - V | |
| Input voltage | V _{I(on)} | V _O = 0.3V, I _O = 10mA | 3.0 | - | - | | |
| Output voltage | V _{O(on)} | I _O = 50mA, I _I = 2.5mA | - | 100 | 300 | mV | |
| Input current | I _I | V _I = 5V | - | - | 880 | μA | |
| Output current | I _{O(off)} | V _{CC} = 50V, V _I = 0V | - | - | 500 | nA | |
| DC current gain | G _I *3 | V _O = 5V, I _O = 50mA | 56 | - | - | - | |
| Input resistance | R ₁ | - | 7 | 10 | 13 | kΩ | |
| Resistance ratio | R ₂ /R ₁ | - | 0.8 | 1.0 | 1.2 | - | |
| Transition frequency | f _T *1 | V _{CE} = 10V, I _E = -50mA, f = 100MHz | - | 200 | - | MHz | |

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference land

^{*3} Pulsed

● Electrical characteristic curves (T_a =25°C)

Fig.1 Input Voltage vs. Output Current (ON Characteristics)

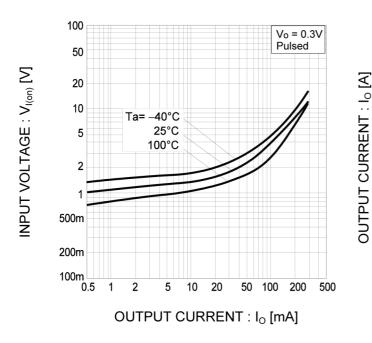


Fig.2 Output Current vs. Input Voltage (OFF Characteristics)

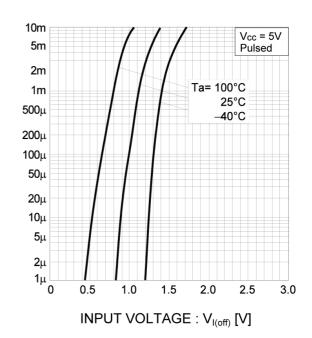


Fig.3 Output Current vs. Output Voltage

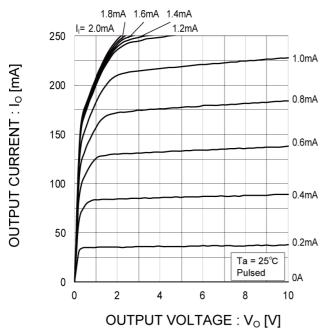
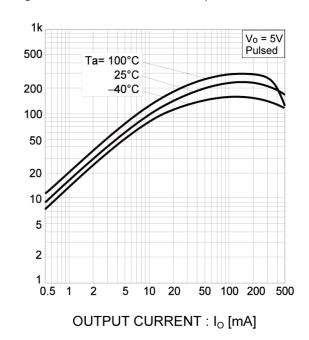


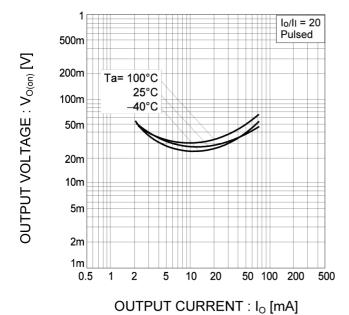
Fig.4 DC Current Gain vs. Output Current



DC CURRENT GAIN: G

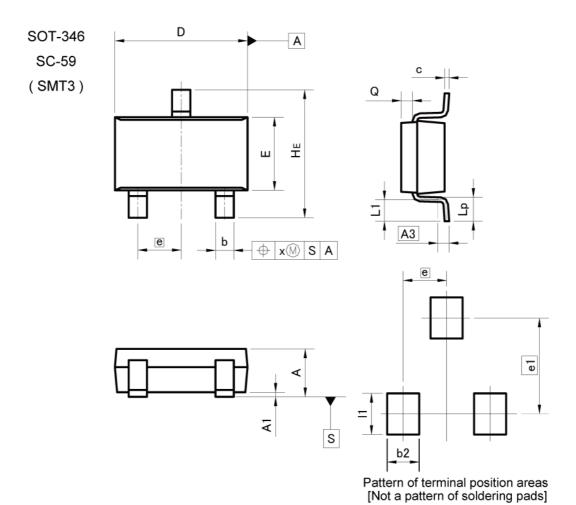
●Electrical characteristic curves (T_a =25°C)

Fig.5 Output Voltage vs. Output Current



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Dimensions



| DIM | DIM MILIMETERS | | INCHES | | |
|-----|----------------|------|----------------|-------|--|
| DIM | MIN | MAX | MIN | MAX | |
| Α | 1.00 | 1.30 | 0.039 | 0.051 | |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 | |
| A3 | 0.3 | 25 | 0.010 | | |
| b | 0.35 | 0.50 | 0.014 | 0.020 | |
| С | 0.09 | 0.25 | 0.004 | 0.010 | |
| D | 2.80 | 3.00 | 0.110 | 0.118 | |
| E | 1.50 | 1.80 | 0.059 | 0.071 | |
| е | 0.95 | | 0.037 | | |
| HE | 2.60 | 3.00 | 0.102 | 0.118 | |
| L1 | 0.30 | 0.60 | 0.012 | 0.024 | |
| Lp | 0.40 | 0.70 | 0.016 | 0.028 | |
| Q | 0.20 | 0.30 | 0.008 | 0.012 | |
| х | - | 0.10 | e= | 0.004 | |
| у | - 2 | 0.10 | (- | 0.004 | |

| DIM | MILIM | ETERS | INCHES | | |
|------|-------|-------|--------|-------|--|
| DIM | MIN | MAX | MIN | MAX | |
| b2 | - | 0.60 | _ | 0.024 | |
| e1 | 2.10 | | 0.083 | | |
| - 11 | -3 | 0.90 | - | 0.035 | |

Dimension in mm/inches



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|---------|----------|------------|-----------|
| CLASSⅢ | CLACCIII | CLASS II b | CL ACCIII |
| CLASSIV | CLASSII | CLASSⅢ | CLASSⅢ |

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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
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 exceeding the recommended storage time period.
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