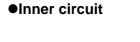


NPN 500mA 40V Digital Transistors (Bias Resistor Built-in Transistors)

Parameter	Value	
V_{CEO}	40V	
I _C	500mA	
R	2.2kΩ	



● Features

- 1) Built-In Biasing Resistors
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Complementary PNP Types :DTB123TK
- 6) Lead Free/RoHS Compliant.

Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	ordering	Marking
DTD123TK	SMT3	(mm) 2928	T146	180	8	unit (pcs) 3,000	F02

Outline

● **Absolute maximum ratings** (Ta = 25°C)

Parameter	Symbol	Values	Unit
Collector-base voltage	V _{CBO}	50	V
Collector-emitter voltage	V _{CEO}	40	V
Emitter-base voltage	V_{EBO}	5	V
Collector current	I _C	500	mA
Power dissipation	P _d *2	200	mW
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

●Electrical characteristics(Ta = 25°C)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Collector-base breakdown voltage	BV_CBO	I _C = 50μA	50	-	-	V
Collector-emitter breakdown voltage	BV _{CEO}	I _C = 1mA	40	-	-	V
Emitter-base breakdown voltage	BV_{EBO}	I _E = 50μA	5	-	-	V
Collector cut-off current	I _{CBO}	V _{CB} = 50V	1	-	0.5	μΑ
Emitter cut-off current	I _{EBO}	V _{EB} = 4V	1	-	0.5	μΑ
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{C} / I_{B} = 50 \text{mA} / 2.5 \text{mA}$	1	-	0.3	V
DC current gain	h _{FE}	V_{CE} = 5V , I_{C} = 50mA	100	250	600	-
Emitter-base resistance	R	-	1.54	2.2	2.86	kΩ
Transition frequency	f _T *1	$V_{CE} = 10V, I_{E} = -50mA,$ f = 100MHz	ı	200	-	MHz

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference footprint

●Electrical characteristic curves(Ta = 25°C)

characteristics

10 $V_{CE}=5V$ $V_{CE}=5$

Fig.1 Grounded emitter propagation

Fig.2 Grounded emitter output characteristics $I_i =$ 500 5.0mA Ta=25°C 4.5mA COLLECTOR CURRENT : I_C (mA) 4.0mA 400 3.5mA 3.0mA 300 2.5mA 2.0mA 200 1.5mA 1.0mA 100 0.5mA 0 0A 0 5 10 **COLLECTOR TO EMITTER** VOLTAGE: V_{CE} (V)

Fig.3 DC Current gain vs. Collector Current 1k V_{CE}=5V 500 DC CURRENT GAIN: hFE 200 100 Ta= 100°C 25°C 50 -40°C 20 10 2 20 50 100 200 0.5

COLLECTOR CURRENT: I_C (mA)

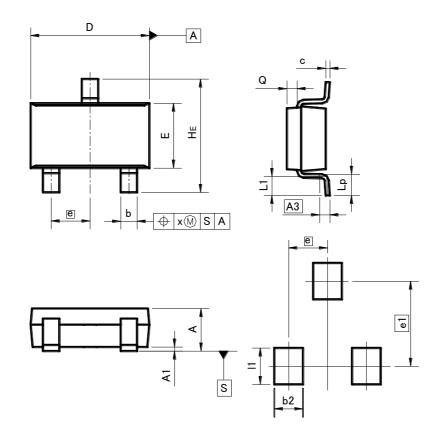
vs. Collector Current $I_{\rm C}/I_{\rm B}=20$ 500m Ta= 100°C 200m COLLECTOR SATURATION 25°C VOLTAGE: V_{CE}(sat) (V) -40°C 100m 50m 20m 10m 5m 2m 1m ∐ 0.5

COLLECTOR CURRENT : I_C (mA)

Fig.4 Collector-emitter saturation voltage

●Dimensions (Unit:mm)

SMT3



Patterm of terminal position areas

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	1.00	1.30	-	0.051	
A1	0.00	0.10	0	0.004	
A3	0.25		0.01		
b	0.35	0.50	0.014	0.02	
С	0.09	0.25	0.004	0.01	
D	2.80	3.00	0.11	0.118	
E	1.50	1.80	0.059	0.071	
е	0.95		0.0	04	
HE	2.60	3.00	0.102	0.118	
L1	0.30	0.60	0.012	0.024	
Lp	0.40	0.70	0.016	0.028	
Q	0.20	0.30	0.008	0.012	
х	_	0.10	_	0.004	
у	_	0.10	_	0.004	

DIM	MILIMI	ETERS	INCHES		
DIM	MIN MAX		MIN	MAX	
e1	2.	2.10		08	
b2		0.60	-	0.024	
11	-	0.90	-	0.035	

Dimension in mm/inches

Notes

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SMUN5114DW1T1G SMUN2111T1G NSVDTC144EM3T5G DTC124ECA-TP DTC123TM3T5G DTA114ECA-TP DTA113EM3T5G

DCX115EK-7-F DTC113EM3T5G NSVMUN5135DW1T1G NSVMUN2237T1G