

## 20V Nch + Pch Small Signal MOSFET

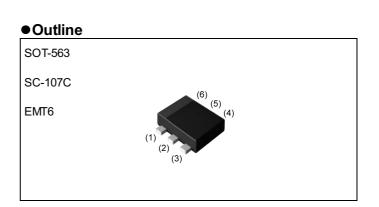
Symbol	Tr1:Nch	Tr2:Pch
V <sub>DSS</sub>	20V	-20V
R <sub>DS(on)</sub> (Max.)	1.0Ω	1.2Ω
I <sub>D</sub>	±200mA ±200m	
P <sub>D</sub>	150mW	

## Features

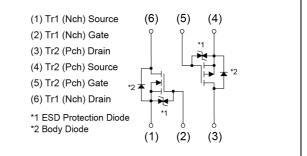
Application

Switching

- 1) Low on resistance.
- 2) Small package(VMT6)
- 3) Low voltage drive(1.2V drive)



## ●Inner circuit



## Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
	Basic ordering unit (pcs)	8000
	Taping code	T2R
	Marking	M02

## • Absolute maximum ratings ( $T_a = 25^{\circ}C$ ,unless otherwise specified)

Parameter		Sympol	Value		Unit	
		Symbol	Tr1:Nch	Tr2:Pch	Unit	
Drain - Source voltage	V <sub>DSS</sub>	20	-20	V		
Continuous drain current	۱ <sub>D</sub>	±200	±200	mA		
Pulsed drain current	I <sub>DP</sub> <sup>*1</sup>	±400	±400	mA		
Gate - Source voltage		V <sub>GSS</sub>	±8	±10	V	
Dowor discinction	total	D *2	150		·····	
Power dissipation	element	P <sub>D</sub> *2	120		mW	
Junction temperature	Tj	150		°C		
Operating junction and storage temperature range		T <sub>stg</sub>	-55 to +150		°C	

## •Electrical characteristics (T<sub>a</sub> = 25°C) <Tr1>

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA	20	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$			29.0	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V	-	-	1	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±8V, $V_{DS}$ = 0V	-	-	±10	μA	
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA	0.3	-	1.0	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I <sub>D</sub> = -1mA referenced to 25°C	-	-1.6	-	mV/°C	
	R <sub>DS(on)</sub> *3	V <sub>GS</sub> = 4.0V, I <sub>D</sub> = 200mA	-	0.7	1.0		
		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 200mA	-	0.8	1.2	Ω	
Static drain - source on - state resistance		V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 200mA	-	1.0	1.4		
		V <sub>GS</sub> = 1.5V, I <sub>D</sub> = 40mA	-	1.2	2.4	_	
		V <sub>GS</sub> = 1.2V, I <sub>D</sub> = 20mA	-	1.6	4.8		
Forward Transfer Admittance	Y <sub>fs</sub>  *3	V <sub>DS</sub> = 10V, I <sub>D</sub> = 200mA	200	-	-	mS	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	25	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 10V	-	10	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	10	-		
Turn - on delay time	t <sub>d(on)</sub> *3	$V_{DD} \simeq 10$ V, $V_{GS}$ = 4.0V	-	5	-		
Rise time	t <sub>r</sub> *3	I <sub>D</sub> = 150mA	-	10	-		
Turn - off delay time	$t_{d(off)}^{*3}$	R <sub>L</sub> = 67Ω	-	15	-	ns	
Fall time	t <sub>f</sub> *3	R <sub>G</sub> = 10Ω	-	10	-	]	

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward voltage	$V_{SD}^{*3}$	V <sub>GS</sub> = 0V, I <sub>S</sub> = 100mA	-	-	1.2	V



## •Electrical characteristics (T<sub>a</sub> = 25°C) <Tr2>

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -1mA	-20	-	-	v	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	I <sub>D</sub> = -1mA referenced to 25°C	-	-21.9	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V	-	-	-1	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{DS}$ = 0V, $V_{GS}$ = ±10V	I	-	±10	μA	
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = -10V, I <sub>D</sub> = -100µA	-0.3	-	-1.0	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I <sub>D</sub> = -1mA referenced to 25°C	-	2.4	-	mV/°C	
	R <sub>DS(on)</sub> *3	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -200mA	-	0.8	1.2	Ω	
		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -100mA	-	1.0	1.5		
Static drain - source on - state resistance		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -100mA	-	1.3	2.2		
		V <sub>GS</sub> = -1.5V, I <sub>D</sub> = -40mA	-	1.6	3.5	-	
		V <sub>GS</sub> = -1.2V, I <sub>D</sub> = -10mA	-	2.4	9.6		
Forward Transfer Admittance	Y <sub>fs</sub>  *3	V <sub>DS</sub> = -10V, I <sub>D</sub> = -200mA	200	-	-	mS	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	115	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -10V	-	10	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	6	-		
Turn - on delay time	t <sub>d(on)</sub> *3	$V_{DD} \simeq -10V$ , $V_{GS} = -4.5V$	-	6	-		
Rise time	t <sub>r</sub> *3	I <sub>D</sub> = -100mA	-	4	-	<b>D</b> 0	
Turn - off delay time	$t_{d(off)}^{*3}$	R <sub>L</sub> = 100Ω	-	17	-	ns	
Fall time	t <sub>f</sub> *3	R <sub>G</sub> = 10Ω	-	17	-		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Forward voltage	V <sub>SD</sub> *3	V <sub>GS</sub> = 0V, I <sub>S</sub> = -200mA	-	-	-1.2	V	

\*1 Pw $\leq$ 10µs , Duty cycle $\leq$ 1%

\*2 Each terminal mounted on a reference land.

\*3 Pulsed



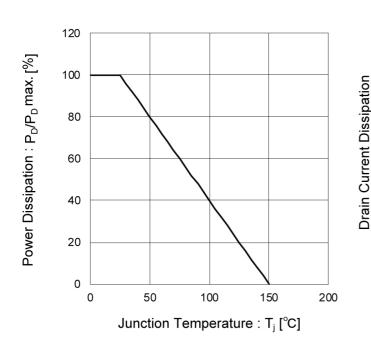


Fig.1 Power Dissipation Derating Curve

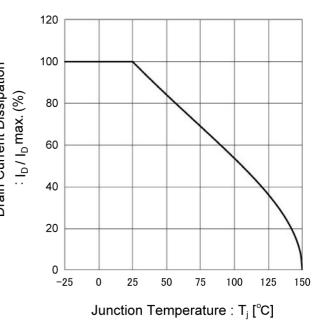
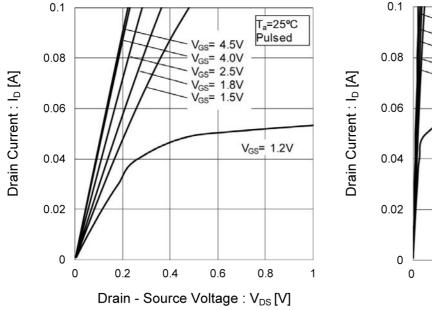
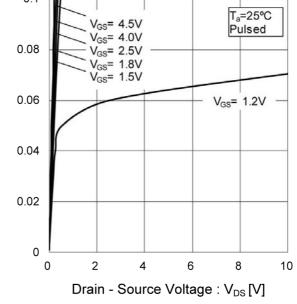


Fig.2 Drain Current Derating Curve

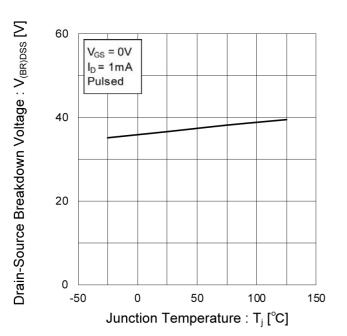
Fig.4 Typical Output Characteristics(II)





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#### Fig.5 Breakdown Voltage vs. Junction Temperature

Fig.6 Typical Transfer Characteristics

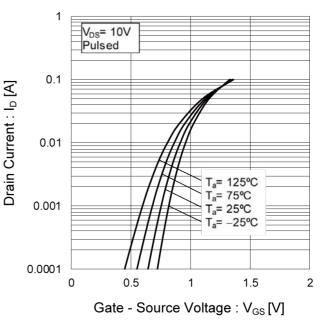


Fig.7 Gate Threshold Voltage vs. Junction Temperature



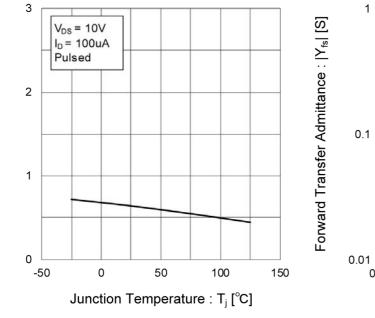
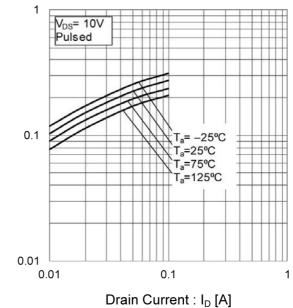
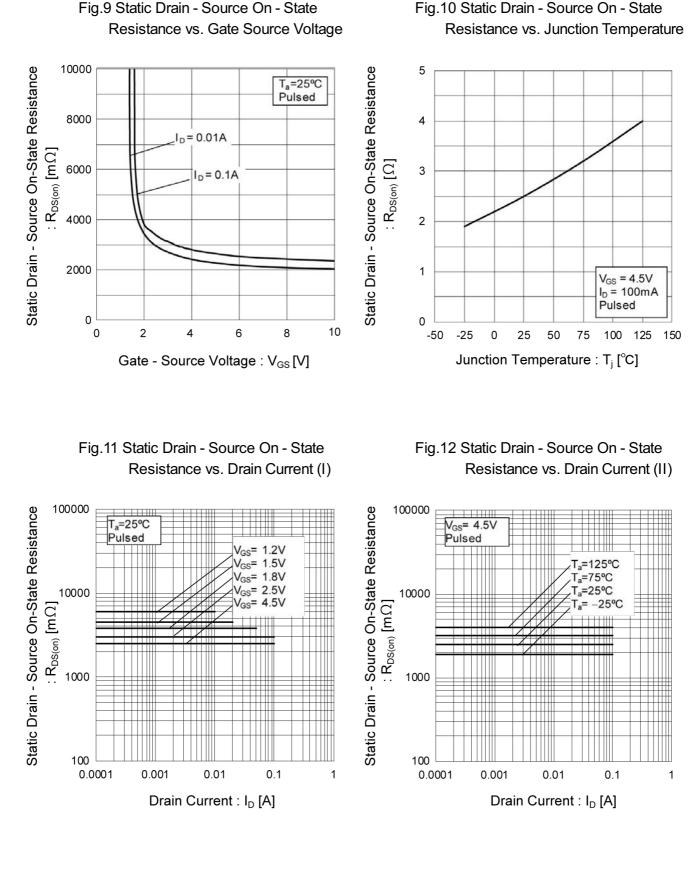


Fig.8 Forward Transfer Admittance vs. Drain Current







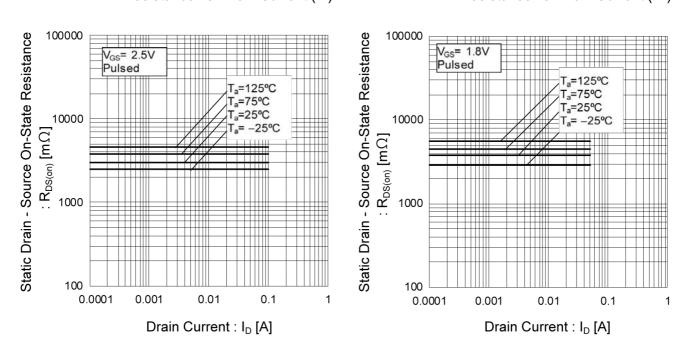


Fig.13 Static Drain - Source On - State Resistance vs. Drain Current (III)

Fig.15 Static Drain - Source On - State

Resistance vs. Drain Current (V)

Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (VI)

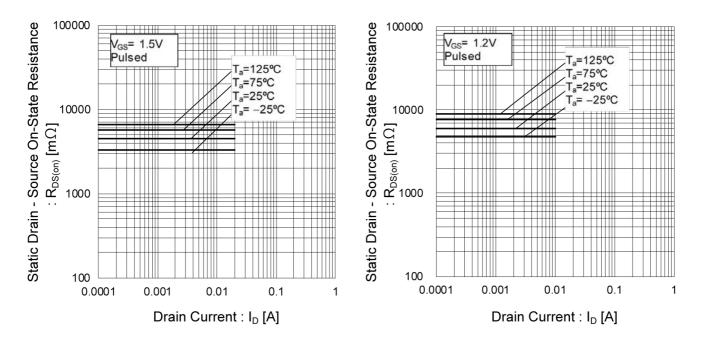
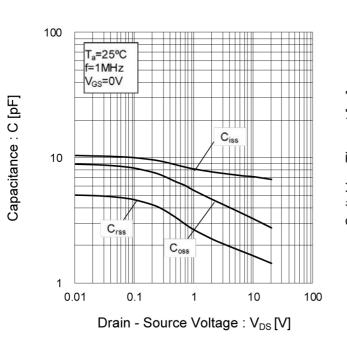


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (IV)



Fig.17 Typical Capacitance vs.

Drain - Source Voltage



#### Fig.18 Switching Characteristics

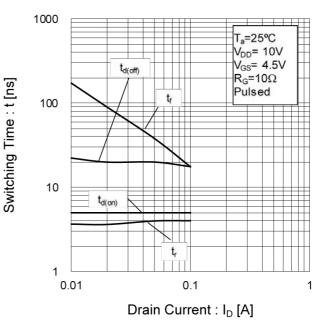
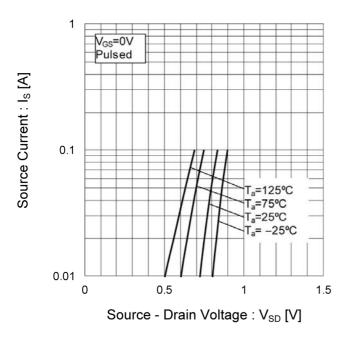


Fig.19 Source Current vs. Source Drain Voltage





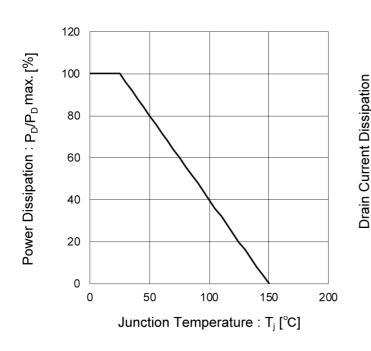
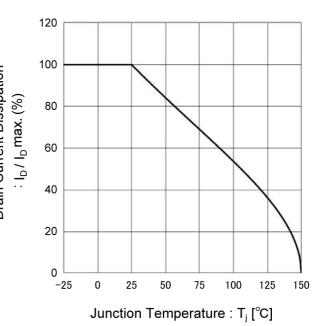


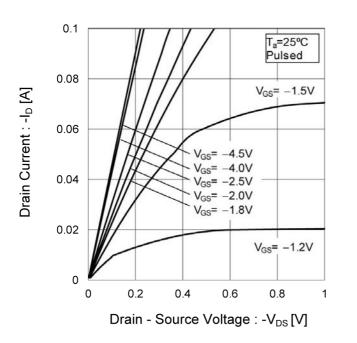
Fig.20 Power Dissipation Derating Curve

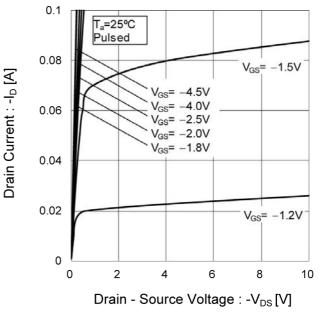


## Fig.21 Drain Current Derating Curve

Fig.22 Typical Output Characteristics(I) Fig

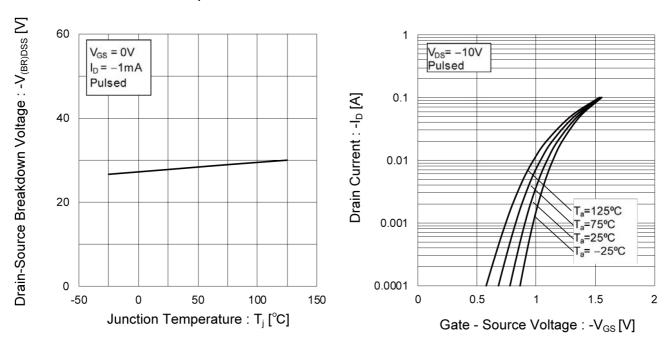
Fig.23 Typical Output Characteristics(II)





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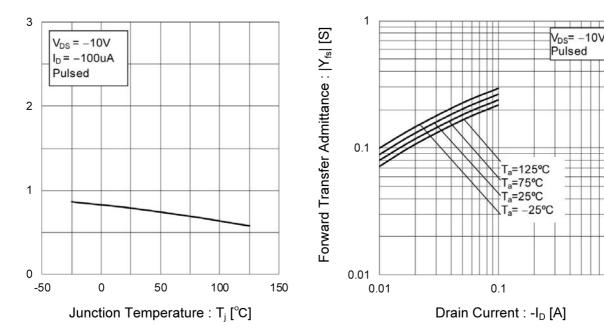




#### Fig.24 Breakdown Voltage vs. Junction Temperature

Fig.26 Gate Threshold Voltage vs. Junction Temperature



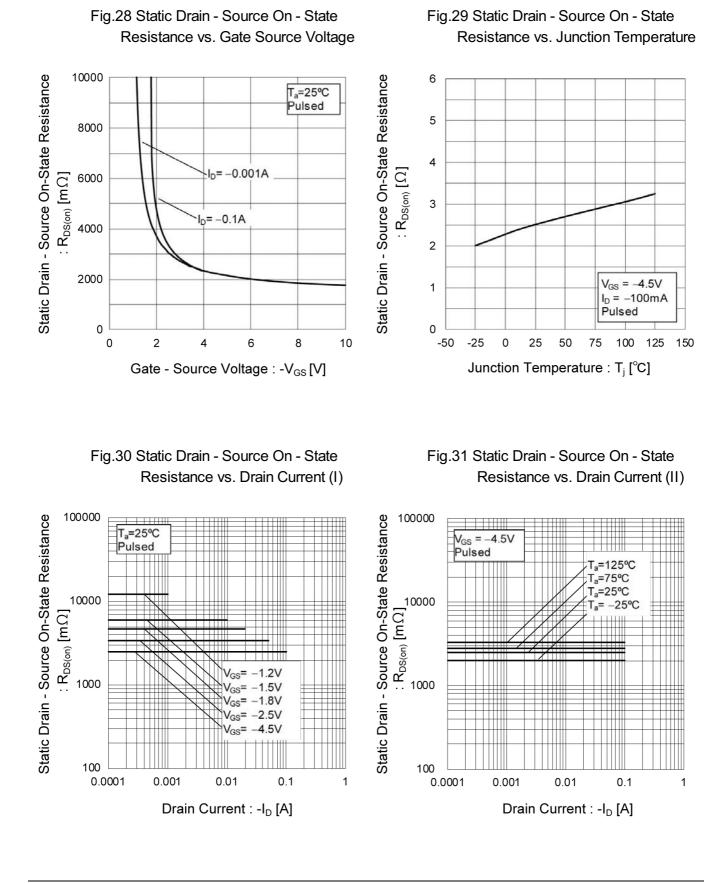


# Fig.25 Typical Transfer Characteristics

Fig.27 Forward Transfer Admittance vs. Drain Current



1







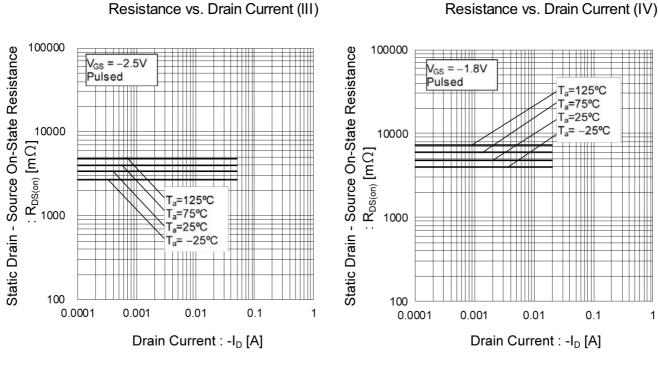


Fig.32 Static Drain - Source On - State Resistance vs. Drain Current (III)

Fig.33 Static Drain - Source On - State

Fig.34 Static Drain - Source On - State Resistance vs. Drain Current (V) Fig.35 Static Drain - Source On - State Resistance vs. Drain Current (VI)

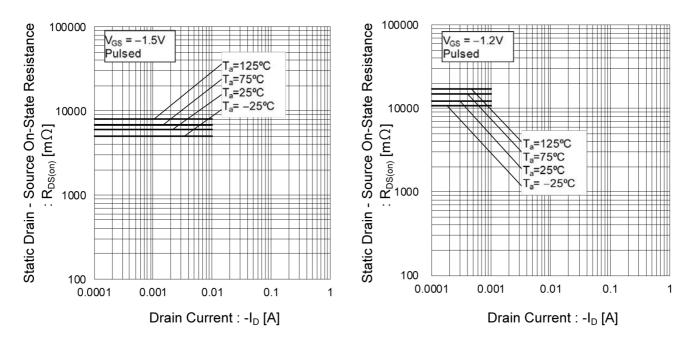
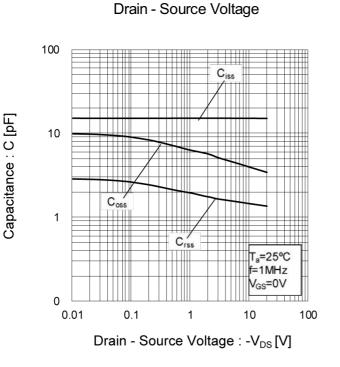




Fig.36 Typical Capacitance vs.



#### Fig.37 Switching Characteristics

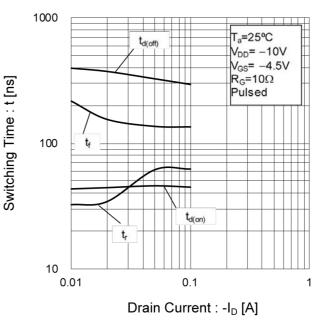
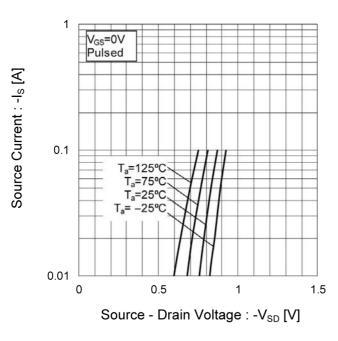


Fig.38 Source Current vs. Source Drain Voltage





#### Measurement circuits <Tr1>

#### Fig. 1-1 SWITCHING TIME MEASUREMENT CIRCUIT

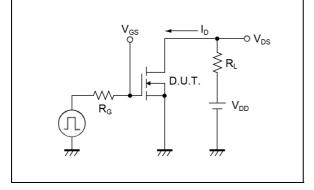
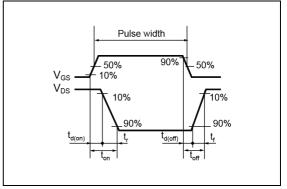
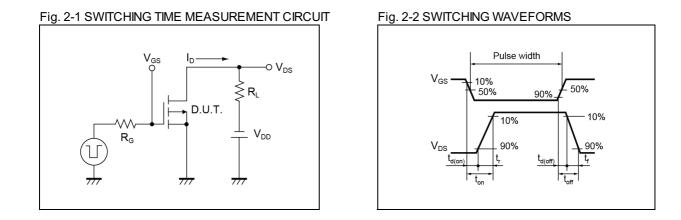


Fig. 1-2 SWITCHING WAVEFORMS





#### Measurement circuits <Tr2>

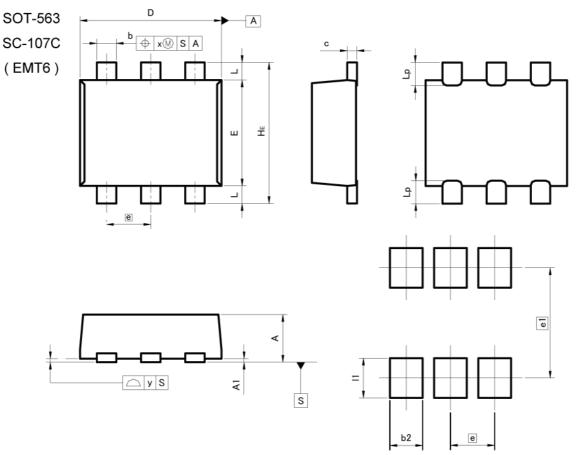


#### Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



#### Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
	MIN	MAX	MIN	MAX
A	0.45	0.55	0.018	0.022
A1	0.00	0.10	0.000	0.004
b	0.17	0.27	0.007	0.011
С	0.08	0.18	0.003	0.007
D	1.50	1.70	0.059	0.067
E	1.10	1.30	0.043	0.051
е	0.	50	0.0	20
HE	1.50	1.70	0.059	0.067
L	0.10	0.30	0.004	0.012
Lp	-	0.35	-	0.014
x	<u>1</u> 0	0.10		0.004
у		0.10		0.004
<b>DIII</b>	MILIM	ETERS	INC	HES
	MIN	MAX	MIN	MAX
b2		0.37	-	0.015
e1	1.	25	0.0	49
11		0.45	-	0.018

Dimension in mm/inches



# Notice

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	CLASSⅣ	CLASSIL	CLASSⅢ	CLASSI

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  - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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  - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

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When disposing Products please dispose them properly using an authorized industry waste company.

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