(6) (5)



Power manegement (Dual transistors)

Outline

SOT-563

SC-107C

<For Tr1>

Parameter	Value
V _{CEO}	-12V
I _C	-500mA

<For DTr2>

Parameter	Value	
V _{CC}	50V	
I _{C(Max.)}	100mA	

● Features ● Inner circuit

- 1)Power switching circuit in a single package.
- 2) Mounting cost and area can be cut in half.

(1) Tr Emitter (6) (5)
(2) Tr Base
(3) DTr OUT(Collector)
(4) DTr GND(Emitter)
(5) DTr IN(Base)
(6) Tr Collector
(1) (2)

EMT6

Application

Power manegement

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
EMF5	SOT-563 (EMT6)	1616	T2R	180	8	8000	F5

(3)

● Absolute maximum ratings (T_a = 25°C)

<Tr1>

Parameter	Symbol	Limits	Unit
Collector-base voltage	V _{CBO}	-15	V
Collector-emitter voltage	V _{CEO}	-12	V
Emitter-base voltage	V _{EBO}	-6	V
Calle atom as unresent	I _C	-500	mA
Collector current	I _{CP} *1	-1	Α

<DTr2>

Parameter	Symbol	Limits	Unit
Supply voltage	V _{CC}	50	V
Input voltage	V _{IN}	-10 to 40	V
Output current	Io	30	mA
Collector current	I _{C(MAX)}	100	mA

<Tr1> <DTr2>

Parameter	Symbol	Limits	Unit
Power dissipation	P _D *2*4	150	mW
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	-55 to 150	°C

● Electrical characteristics (T_a = 25°C) <For Tr1>

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Collector-base breakdown voltage	BV _{CBO}	I _C = -10μA	-15	-	-	V
Collector-emitter breakdown voltage	BV _{CEO}	I _C = -1mA	-12	-	-	V
Emitter-base breakdown voltage	BV_{EBO}	I _E = -10μA	-6	-	-	V
Collector cut-off current	I _{CBO}	V _{CB} = -15V	-	1	-100	nA
Emitter cut-off current	I _{EBO}	V _{EB} = -6V	-	1	-100	nA
Collector-emitter saturation voltage	$V_{CE(sat)}$	I _C = -200mA, I _B = -10mA	-	-100	-250	mV
DC current gain	h _{FE}	$V_{CE} = -2V, I_{C} = -10mA$	270	-	680	-
Transition frequency	f _T	V _{CE} = -2V, I _E = 10mA, f = 100MHz	-	260	-	MHz
Output capacitance	C _{ob}	$V_{CB} = -10V, I_{E} = 0A,$ f = 1MHz	-	6.5	-	pF

● Electrical characteristics (Ta = 25°C) < For DTr2>

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
	$V_{l(off)}$	$V_{CC} = 5V, I_{O} = 100 \mu A$	-	-	0.5	V
Input voltage	V _{I(on)}	$V_O = 0.3V$, $I_O = 2mA$	3.0	-	-	V
Output voltage	V _{O(on)}	I _O / I _I = 10mA / 0.5mA	-	100	300	mV
Input current	l _l	V _I = 5V	-	-	180	μA
Output current	I _{O(off)}	V _{CC} = 50V, VI = 0V	-	-	500	nA
DC current gain	G _I	V _O = 5V, I _O = 5mA	68	-	-	-
Input resistance	R ₁	-	32.9	47	61.1	kΩ
Resistance ratio	R ₂ /R ₁	-	0.8	1.0	1.2	-
Transition frequency	f _T *3	V _{CE} = 10V, I _E = -5mA, f = 100MHz		250	-	MHz

^{*1} Pw=1ms Single pulse

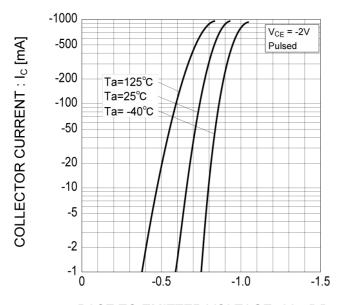
^{*2} Each termunal mounted on a reference land.

^{*3} Characteristics of built-in transistor.

^{*4 120}mW per element must not be exceeded.

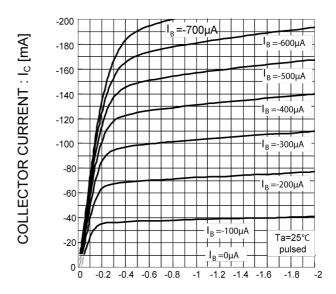
● Electrical characteristic curves(T_a=25°C) <For Tr1>

Fig.1 Ground Emitter Propagation Characteristics



BASE TO EMITTER VOLTAGE : V_{BE} [V]

Fig.2 Typical Output Characteristics



COLLECTOR TO EMITTER VOLTAGE: V_{CE} [V]

Fig.3 DC Current Gain vs. Collector Current (I)

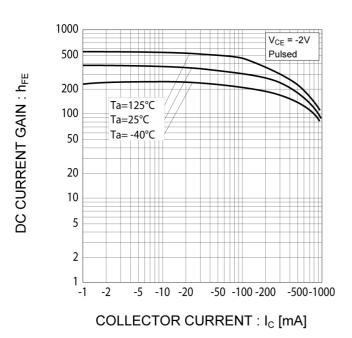
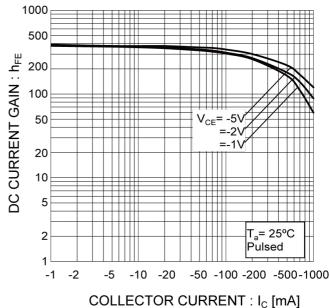


Fig.4 DC Current Gain vs. Collector Current (II)



● Electrical characteristic curves(T_a=25°C) <For Tr1>

Fig.5 Collector-Emitter Saturation Voltage vs. Collector Current (I)

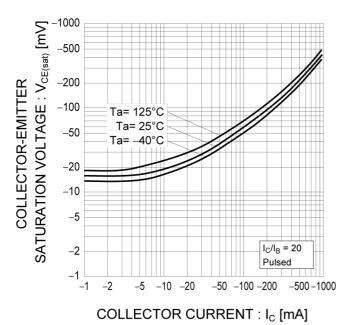
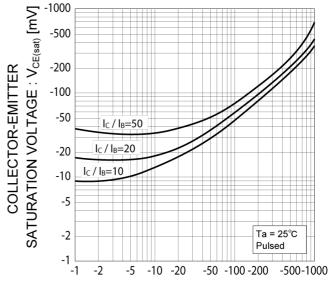


Fig.6 Collector-Emitter Saturation
Voltage vs. Collector Current (II)



COLLECTOR CURRENT : I_C [mA]

Fig.7 Base-Emitter Saturation Voltage vs. Collector Current

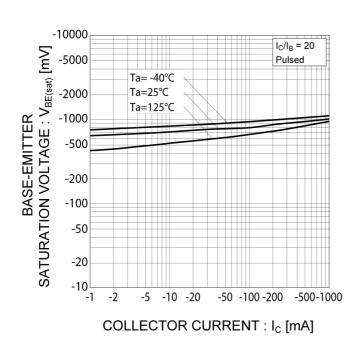
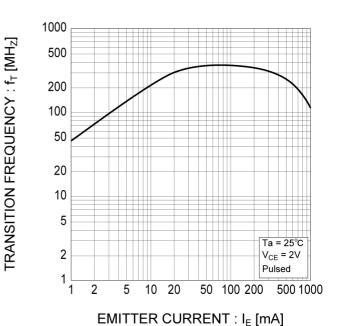


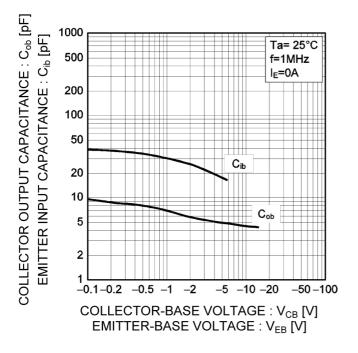
Fig.8 Gain Bandwidth Product vs. Emitter Current

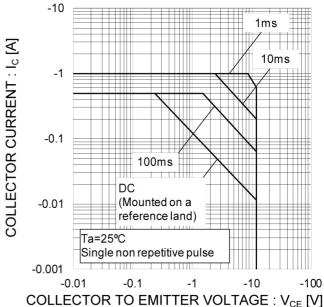


● Electrical characteristic curves(T_a=25°C) <For Tr1>

Fig.9 Emitter Input Capacitance vs.
Emitter-Base Voltage
Collector Output Capacitance vs.
Collector-Base Voltage

Fig.10 Safe Operating Area





ROHM

● Electrical characteristic curves(T_a=25°C) < For DTr2>

Fig.11 Input voltage vs. output current (ON characteristics)

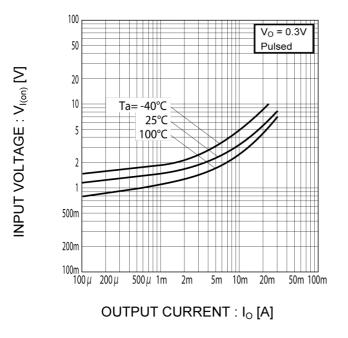


Fig.12 Output current vs. input voltage (OFF characteristics)

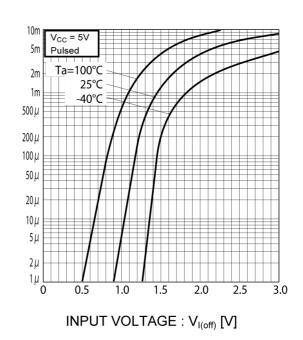


Fig.13 Output current vs. output voltage

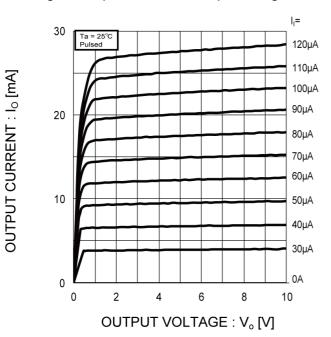
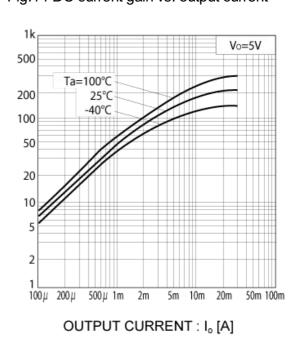


Fig.14 DC current gain vs. output current

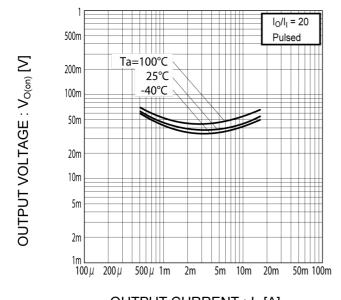


DC CURRENT GAIN: G

OUTPUT CURRENT : Io [A]

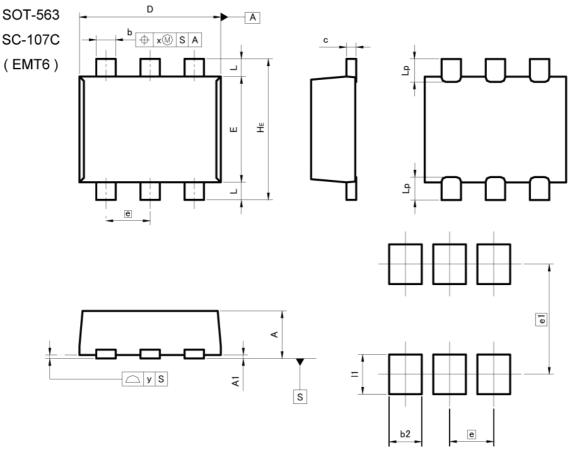
● Electrical characteristic curves(T_a=25°C) < For DTr2>

Fig.15 Output voltage vs. output current



OUTPUT CURRENT : Io [A]

●外形寸法図



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.45	0.55	0.018	0.022
A1	0.00	0.10	0.000	0.004
b	0.17	0.27	0.007	0.011
С	0.08	0.18	0.003	0.007
D	1.50	1.70	0.059	0.067
E	1.10	1.30	0.043	0.051
е	0.	50	0.0	20
HE	1.50	1.70	0.059	0.067
L	0.10	0.30	0.004	0.012
Lp	_	0.35	_	0.014
х	_	0.10	_	0.004
У	-	0.10	-	0.004

DIM	MILIM	ETERS	INCHES		
DIM	MIN MAX		MIN	MAX	
b2	_	0.37	_	0.015	
e1	1.3	1.25		49	
11	-	0.45	-	0.018	

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CL ACCTI
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 8. Confirm that operation temperature is within the specified range described in the product specification.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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