

### General purpose (Dual digital transistors)

### **AEC-Q101 Qualified**

| Parameter            | DTr1 and DTr2 |  |  |
|----------------------|---------------|--|--|
| V <sub>CC</sub>      | 50V           |  |  |
| I <sub>C(MAX.)</sub> | 100mA         |  |  |
| R <sub>1</sub>       | 10kΩ          |  |  |
| R <sub>2</sub>       | 10kΩ          |  |  |

# EMT6

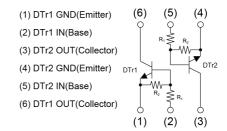
Outline
SOT-563

SC-107C

### Features

- 1)Two DTC114E chips in a EMT6 package.
- 2)Mounting possible with EMT3 automatic mounting machines.
- 3)Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

### •Inner circuit



## Application

INVERTER, INTERFACE, DRIVER

# Packaging specifications

| Part No.  | Package           | Package<br>size | Taping<br>code | Reel size<br>(mm) | Tape width (mm) | Basic<br>ordering<br>unit.(pcs) | Marking |
|-----------|-------------------|-----------------|----------------|-------------------|-----------------|---------------------------------|---------|
| EMH11 FHA | SOT-563<br>(EMT6) | 1616            | T2R            | 180               | 8               | 8000                            | H11     |

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

<For DTr1 and DTr2 in common>

| Parameter                    | Symbol                 | Values      | Unit     |
|------------------------------|------------------------|-------------|----------|
| Supply voltage               | V <sub>CC</sub>        | 50          | V        |
| Input voltage                | V <sub>IN</sub>        | -10 to 40   | V        |
| Output current               | Io                     | 50          | mA       |
| Collector current            | I <sub>C(MAX)</sub> *1 | 100         | mA       |
| Power dissipation            | P <sub>D</sub> *2*3    | 150         | mW/TOTAL |
| Junction temperature         | Tj                     | 150         | °C       |
| Range of storage temperature | T <sub>stg</sub>       | -55 to +150 | °C       |

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

<For DTr1 and DTr2 in common>

| Daramatar            | Cymah al                       | Conditions                                    | Values |      |      | Lloit |  |
|----------------------|--------------------------------|---|--------|------|------|-------|--|
| Parameter            | Symbol                         | Conditions                                    | Min.   | Тур. | Max. | Unit  |  |
| Input voltage        | $V_{I(off)}$                   | V <sub>CC</sub> = 5V, I <sub>O</sub> = 100μA  | -      | -    | 0.5  | V     |  |
| Input voltage        | V <sub>I(on)</sub>             | $V_O = 0.3V$ , $I_O = 2mA$                    | 3.0    | -    | -    | V     |  |
| Output voltage       | V <sub>O(on)</sub>             | I <sub>O</sub> = 10mA, I <sub>I</sub> = 0.5mA | ı      | 100  | 300  | mV    |  |
| Input current        | I <sub>I</sub>                 | V <sub>I</sub> = 5V                           | -      | -    | 880  | μA    |  |
| Output current       | I <sub>O(off)</sub>            | $V_{CC} = 50V, V_{I} = 0V$                    | -      | -    | 500  | nA    |  |
| DC current gain      | G <sub>I</sub>                 | $V_{O} = 5V, I_{O} = 5mA$                     | 30     | -    | -    | -     |  |
| Input resistance     | R <sub>1</sub>                 | -   | 7      | 10   | 13   | kΩ    |  |
| Resistance ratio     | R <sub>2</sub> /R <sub>1</sub> | -   | 0.8    | 1.0  | 1.2  | -     |  |
| Transition frequency | f <sub>T</sub>                 | $V_{CE} = 10V, I_{E} = -5mA,$<br>f = 100MHz   | -      | 250  | 1    | MHz   |  |

<sup>\*1</sup> Characteristics of built-in transistor

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<sup>\*2</sup> Each terminal mounted on a reference land.

<sup>\*3 120</sup>mW per element must not be exceeded.

# ● Electrical characteristic curves (T<sub>a</sub> = 25°C)

<For DTr1 and DTr2 in common>

Fig.1 Input Voltage vs. Output Current (ON Characteristics)

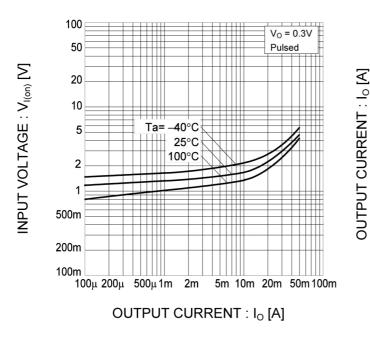


Fig.2 Output Current vs. Input Voltage (OFF Characteristics)

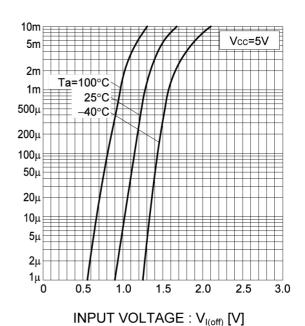


Fig.3 Output Current vs. Output Voltage

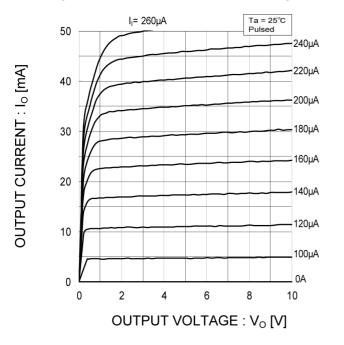
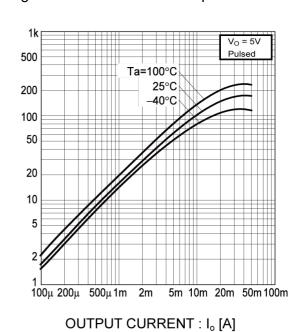


Fig.4 DC Current Gain vs. Output Current

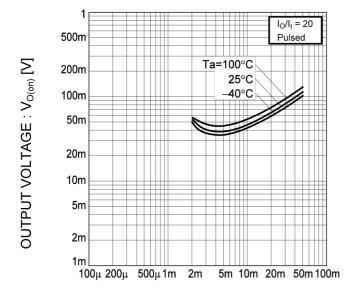


DC CURRENT GAIN: G

# ● Electrical characteristic curves (T<sub>a</sub> = 25°C)

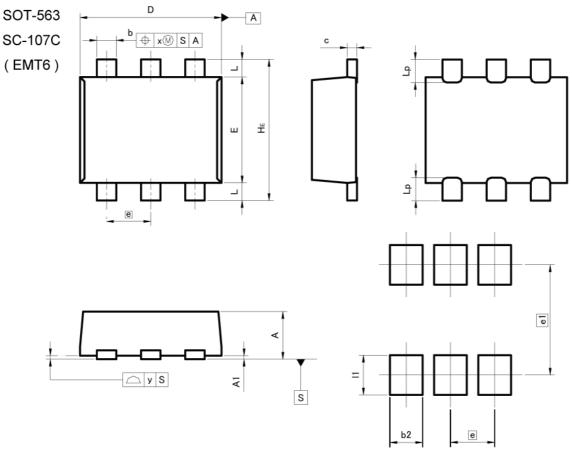
<For DTr1 and DTr2 in common>

Fig.5 Output Voltage vs. Output Current



OUTPUT CURRENT : Io [A]

### Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

| DIM | MILIM | ETERS | INCHES |       |  |
|-----|-------|-------|--------|-------|--|
| DIM | MIN   | MAX   | MIN    | MAX   |  |
| Α   | 0.45  | 0.55  | 0.018  | 0.022 |  |
| A1  | 0.00  | 0.10  | 0.000  | 0.004 |  |
| b   | 0.17  | 0.27  | 0.007  | 0.011 |  |
| С   | 0.08  | 0.18  | 0.003  | 0.007 |  |
| D   | 1.50  | 1.70  | 0.059  | 0.067 |  |
| E   | 1.10  | 1.30  | 0.043  | 0.051 |  |
| е   | 0.    | 50    | 0.020  |       |  |
| HE  | 1.50  | 1.70  | 0.059  | 0.067 |  |
| L   | 0.10  | 0.30  | 0.004  | 0.012 |  |
| Lp  | -     | 0.35  | _      | 0.014 |  |
| x   | _     | 0.10  | _      | 0.004 |  |
| У   | -     | 0.10  | -      | 0.004 |  |

| DIM  | MILIMI | ETERS | INCHES |       |  |  |
|------|--------|-------|--------|-------|--|--|
| DIM  | MIN    | MAX   | MIN    | MAX   |  |  |
| b2   | -      | 0.37  | -      | 0.015 |  |  |
| e1   | 1.25   |       | 0.049  |       |  |  |
| - 11 | - 0.45 |       | -      | 0.018 |  |  |

Dimension in mm/inches



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| Ì | JÁPAN   | USA      | EU         | CHINA  |
|---|---------|----------|------------|--------|
|   | CLASSⅢ  | CLASSIII | CLASS II b | СГУССШ |
|   | CLASSIV | CLASSIII | CLASSIII   | CLASSⅢ |

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  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 8. Confirm that operation temperature is within the specified range described in the product specification.
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For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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NSVMUN2237T1G NSVDTC143ZM3T5G SMUN5335DW1T2G SMUN5216DW1T1G