NPN 100mA 50V Complex Digital Transistors (Bias Resistor Built-in Transistors)

| Parameter | Tr1 and Tr2 |
|----------------------|------------------|
| V _{CC} | 50V |
| I _{C(MAX.)} | 100mA |
| R ₁ | 4.7 k Ω |
| R_2 | 47kΩ |

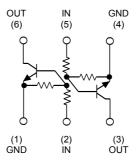
Outline

EMT6 (5) EMH25 (SC-107C)

Features

- 1) Built-In Biasing Resistors.
- 2) Two DTC143Z chips in one package.
- 3) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 4) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 5) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 6) Lead Free/RoHS Compliant.

•Inner circuit



Application

Inverter circuit, Interface circuit, Driver circuit

Packaging specifications

| Part No. | Package | Package size (mm) | Taping code | Reel size (mm) | Tape width (mm) | Basic ordering unit (pcs) | Marking |
|----------|---------|-------------------------|----------------|-------------------|-----------------|---------------------------------|---------|
| EMH25 | EMT6 | 1616 | T2R | 180 | 8 | 8,000 | H25 |

● Absolute maximum ratings (Ta = 25°C)

<For Tr1 and Tr2 in common>

| Parameter | Symbol | Values | Unit |
|------------------------------|-------------------------|---------------------------|------|
| Supply voltage | V _{CC} | 50 | V |
| Input voltage | V _{IN} | −5 to +30 | V |
| Output current | Io | 100 | mA |
| Collector current | I _{C(MAX.)} *1 | 100 | mA |
| Power dissipation | P _D *2 | 150 (Total) ^{*3} | mW |
| Junction temperature | T _j | 150 | °C |
| Range of storage temperature | T _{stg} | -55 to +150 | °C |

●Electrical characteristics(Ta = 25°C)

<For Tr1 and Tr2 in common>

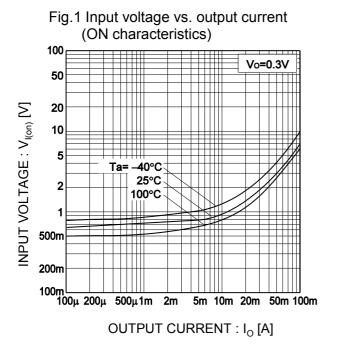
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit |
|----------------------|--------------------------------|--|------|------|------|------|
| Input voltage | $V_{I(off)}$ | $V_{CC} = 5V, I_{O} = 100 \mu A$ | - | - | 0.5 | V |
| Input voltage | $V_{I(on)}$ | $V_{\rm O} = 0.3 V, I_{\rm O} = 5 \text{mA}$ | 1.3 | - | - | V |
| Output voltage | $V_{O(on)}$ | $I_{O}/I_{I} = 5mA/0.25mA$ | - | 0.1 | 0.3 | V |
| Input current | I ₁ | V _I = 5V | - | - | 1.8 | mA |
| Output current | I _{O(off)} | $V_{CC} = 50V, V_I = 0V$ | - | - | 0.5 | μА |
| DC current gain | G _I | $V_0 = 5V, I_0 = 10mA$ | 80 | - | - | - |
| Input resistance | R ₁ | - | 3.29 | 4.7 | 6.11 | kΩ |
| Resistance ratio | R ₂ /R ₁ | - | 8 | 10 | 12 | - |
| Transition frequency | f _T *1 | $V_{CE} = 10V, I_{E} = -5mA,$ f = 100MHz | - | 250 | - | MHz |

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference footprint

^{*3 120}mW per element must not be exceeded.

●Electrical characteristic curves(Ta = 25°C)



(OFF characteristics) 10m Vcc=5V 5m 2m OUTPUT CURRENT : Io [A] 1m **500**μ 200μ -100°C 100μ 25°C 40°C 50μ 20_µ 10μ 5μ 2μ 1μ 1.5 3.0 INPUT VOLTAGE : V_{I(off)}[V]

Fig.2 Output current vs. input voltage

Fig.3 Output current vs. output voltage

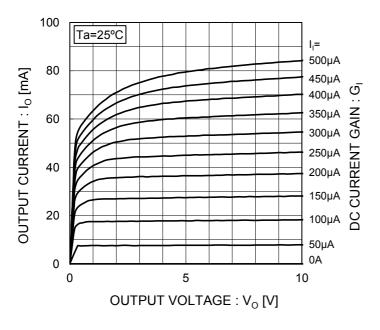
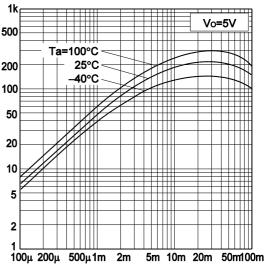
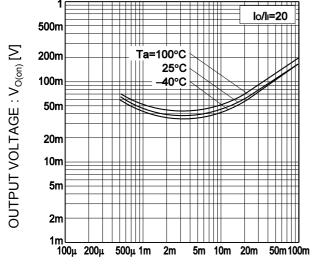


Fig.4 DC current gain vs. output current

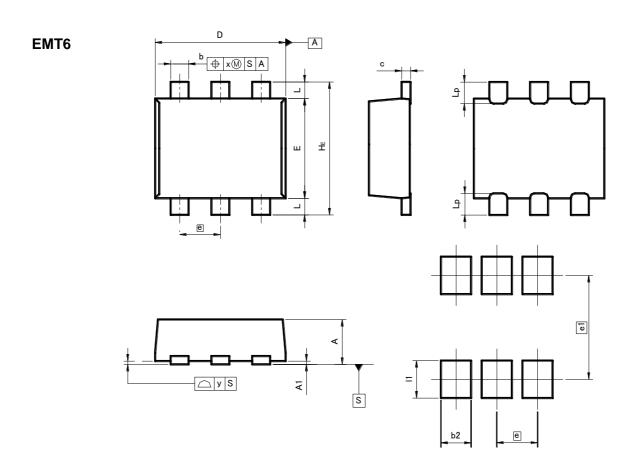


●Electrical characteristic curves(Ta = 25°C)

Fig.5 Output voltage vs. output current



●Dimensions (Unit : mm)



Patterm of terminal position areas

| DIM | MILIM | ETERS | INCHES | | |
|-------|-------|-------|--------|-------|--|
| DIIVI | MIN | MAX | MIN | MAX | |
| A1 | 0.00 | 0.10 | 0 | 0.004 | |
| Α | 0.45 | 0.55 | 0.018 | 0.022 | |
| b | 0.17 | 0.27 | 0.007 | 0.011 | |
| С | 0.08 | 0.18 | 0.003 | 0.007 | |
| D | 1.50 | 1.70 | 0.059 | 0.067 | |
| E | 1.10 | 1.30 | 0.043 | 0.051 | |
| е | 0. | 50 | 0.02 | | |
| HE | 1.50 | 1.70 | 0.059 | 0.067 | |
| L | 0.10 | 0.30 | 0.004 | 0.012 | |
| Lp | _ | 0.35 | _ | 0.014 | |
| х | _ | 0.10 | - | 0.004 | |
| У | _ | 0.10 | | 0.004 | |

| DIM | MILIMI | ETERS | INCHES | | |
|-----|--------|-------|--------|-------|--|
| DIN | MIN | MAX | MIN | MAX | |
| e1 | 1.25 | | 0.049 | | |
| b2 | - | 0.37 | ı | 0.015 | |
| l1 | _ | 0.45 | _ | 0.018 | |

Dimension in mm/inches

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SMUN5235T1G SMUN5330DW1T1G SSVMUN5312DW1T2G RN1303(TE85L,F) RN4605(TE85L,F) TTEPROTOTYPE79

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NSVMUN2237T1G NSVDTC143ZM3T5G SMUN5335DW1T2G SMUN5216DW1T1G