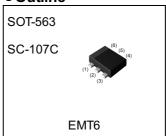


Complex Digital Transistors (Bias Resistor Built-in Transistors)

| Parameter | DTr1 and DTr2 | | |
|----------------------|---------------|--|--|
| V _{CC} | 50V | | |
| I _{C(MAX.)} | 100mA | | |
| R ₁ | 10kΩ | | |
| R ₂ | 47kΩ | | |

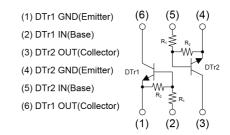
Outline



Features

- 1)Two DTC014Y chips in a EMT package.
- 2)Mounting possible with EMT3 automatic mounting machines.
- 3)Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

●Inner circuit



Application

SWITCHING CIRCUIT, INVERTER CIRCUIT, INTERFACE CIRCUIT

Packaging specifications

| Part No. | Package | Package size | Taping code | Reel size (mm) | Tape width (mm) | Basic ordering unit.(pcs) | Marking |
|----------|-------------------|-----------------|----------------|-------------------|-----------------|---------------------------------|---------|
| EMH59 | SOT-563 (EMT6) | 1616 | T2R | 180 | 8 | 8000 | H59 |

● Absolute maximum ratings (T_a = 25°C)

<For DTr1 and DTr2 in common>

| Parameter | Symbol | Values | Unit |
|------------------------------|------------------------|-------------|------|
| Supply voltage | V _{CC} | 50 | V |
| Input voltage | V _{IN} | 40 to -6 | V |
| Output current | Io | 70 | mA |
| Collector current | I _{C(MAX)} *1 | 100 | mA |
| Power dissipation | P _D *2*3 | 150 | mW |
| Junction temperature | Tj | 150 | °C |
| Range of storage temperature | T _{stg} | -55 to +150 | °C |

● Electrical characteristics (T_a = 25°C)

<For DTr1 and DTr2 in common>

| Downwortow | Cymah ol | Canditions | Values | | | 1.1-:4 | |
|----------------------|--------------------------------|---|--------|------|------|--------|--|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit | |
| lanut valtaga | $V_{l(off)}$ | V _{CC} = 5V, I _O = 100μA | - | - | 0.5 | V | |
| Input voltage | V _{I(on)} | V _O = 0.3V, I _O = 5mA 1.7 - | | - |) V | | |
| Output voltage | V _{O(on)} | I _O = 5mA, I _I = 0.5mA | - | 50 | 150 | mV | |
| Input current | I _I | V _I = 5V | - | - | 880 | μA | |
| Output current | I _{O(off)} | V _{CC} = 50V, V _I = 0V | - | - | 500 | nA | |
| DC current gain | G _I | V _O = 10V, I _O = 5mA | 80 | - | - | - | |
| Input resistance | R ₁ | - | 7 | 10 | 13 | kΩ | |
| Resistance ratio | R ₂ /R ₁ | - | 3.7 | 4.7 | 5.7 | - | |
| Transition frequency | f _T *1 | $V_{CE} = 10V, I_{E} = -5mA,$ f = 100MHz | - | 250 | - | MHz | |

^{*1} Characteristics of built-in transistor.

^{*2} Each terminal mounted on a reference land.

^{*3 120}mW per element must not be exceeded.

● Electrical characteristic curves (T_a = 25°C)

<For DTr1 and DTr2 in common>

Fig.1 Input voltage vs. output current (ON characteristics)

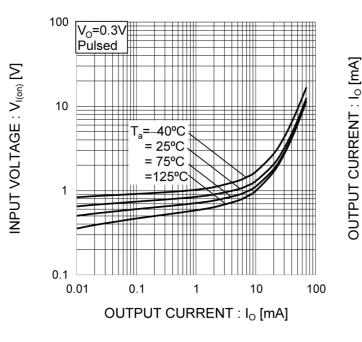


Fig.2 Output current vs. input voltage (OFF characteristics)

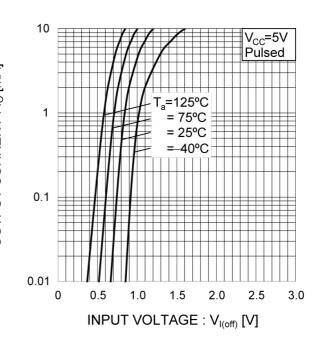


Fig.3 Output current vs. output voltage

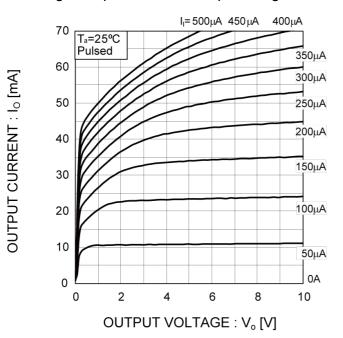
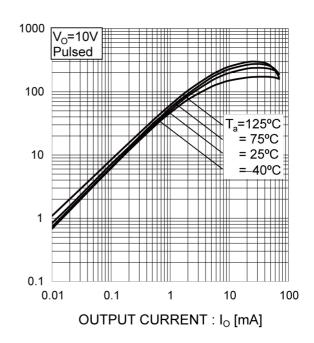


Fig.4 DC current gain vs. output current

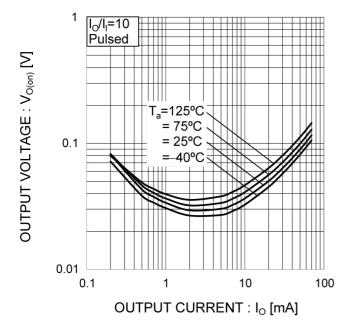


OC CURRENT GAIN: G

● Electrical characteristic curves (T_a = 25°C)

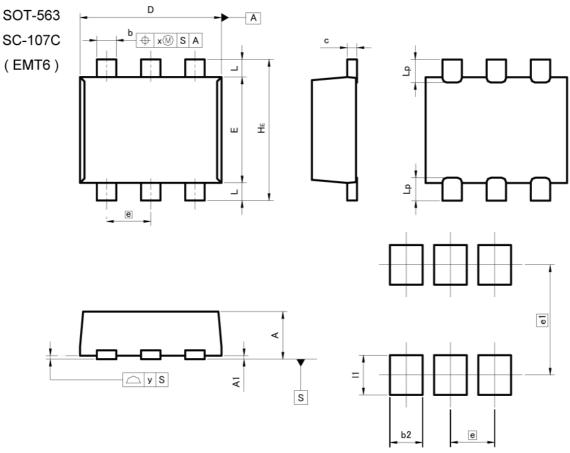
<For DTr1 and DTr2 in common>

Fig.5 Output voltage vs. output current





Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

| DIM | MILIM | ETERS | INCHES | | |
|-----|-------|-------|--------|-------|--|
| DIM | MIN | MAX | MIN | MAX | |
| Α | 0.45 | 0.55 | 0.018 | 0.022 | |
| A1 | 0.00 | 0.10 | 0.000 | 0.004 | |
| b | 0.17 | 0.27 | 0.007 | 0.011 | |
| С | 0.08 | 0.18 | 0.003 | 0.007 | |
| D | 1.50 | 1.70 | 0.059 | 0.067 | |
| E | 1.10 | 1.30 | 0.043 | 0.051 | |
| е | 0. | 50 | 0.020 | | |
| HE | 1.50 | 1.70 | 0.059 | 0.067 | |
| L | 0.10 | 0.30 | 0.004 | 0.012 | |
| Lp | - | 0.35 | _ | 0.014 | |
| х | _ | 0.10 | _ | 0.004 | |
| У | - | 0.10 | - | 0.004 | |

| DIM | MILIMETERS | | INCHES | | |
|-----|------------|------|--------|-------|--|
| DIW | MIN | MAX | MIN | MAX | |
| b2 | - | 0.37 | ı | 0.015 | |
| e1 | 1.3 | 25 | 0.0 | 49 | |
| l1 | - | 0.45 | = | 0.018 | |

Dimension in mm/inches



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| JAPAN | USA | EU | CHINA |
|---------|----------|------------|-----------|
| CLASSⅢ | CLACCIII | CLASS II b | CL ACCIII |
| CLASSIV | CLASSII | CLASSⅢ | CLASSⅢ |

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
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 - [h] Use of the Products in places subject to dew condensation
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
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 exceeding the recommended storage time period.
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