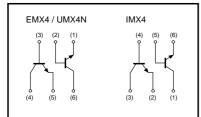
# High transition frequency (dual transistors) EMX4 / UMX4N / IMX4

## Features

- 1) Two 2SC3837K chips in a EMT or UMT or SMT package.
- 2) High transition frequency. (fT=1.5GHz)
- 3) Low output capacitance. (Cob=0.9pF)

### Equivalent circuits



## ● Absolute maximum ratings (Ta=25°C)

		Unit	
Vсво	30	V	
VCEO	20	V	
Vebo	3	V	
lc	50	mA	
Da	150(TOTAL)	mW *1	
	300(TOTAL)	*2	
Tj	150	°C	
Tstg	-55 to +150	°C	
	VCEO VEBO IC PC Tj	Vceo         20           Vebo         3           Ic         50           Pc         150(TOTAL)           300(TOTAL)         150           Tj         150           Tstg         -55 to +150	

\*2 200mW per element must not be exceeded.

#### Package, marking, and packaging specifications

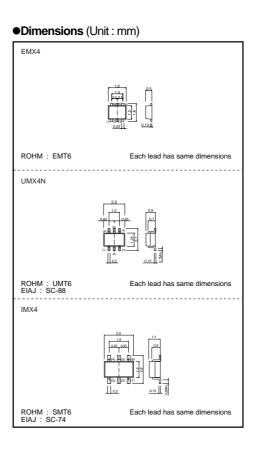
Туре	EMX4	UMX4N	IMX4	
Package	EMT6	UMT6	SMT6	
Marking	X4	X4	X4	
Code	T2R	TR	T108	
Basic ordering unit (pieces)	8000	3000	3000	

#### Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	30	-	-	V	Ic=10µA
Collector-emitter breakdown voltage	BVCEO	20	-	-	V	Ic=1mA
Emitter-base breakdown voltage	ВVево	3	-	-	V	Ιε=10μΑ
Collector cutoff current	Ісво	-	-	0.5	μA	Vcb=15V
Emitter cutoff current	Іево	-	-	0.5	μA	VEB=2V
DC current transfer ratio	hre	56	-	180	_	Vce/lc=10V/10mA
Collector-emitter saturation voltage	VCE(sat)	-	-	0.5	V	Ic/IB=20mA/4mA
Transition frequency	fτ	600	1500	-	MHz	Vce/IE=10V/ -10mA, f=200MHz *
Output capacitance	Cob	-	0.95	1.6	pF	Vcb/f=10V/1MHz, IE=0A
Collector-base time constant	rbb'•Cc	-	6	13	ps	Vсв=10V, Ic=10mA, f=31.8MHz
Noise factor	NF	-	4.5	-	dB	Vce=12V, Ic=2mA , f=200MHz , Rg=50Ω

nsition frequency of the device

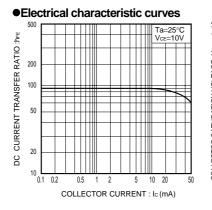
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.





# EMX4 / UMX4N / IMX4

## Transistors





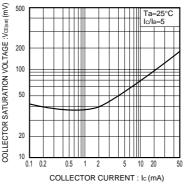


Fig.2 Collector-emitter saturation voltage vs. collector current

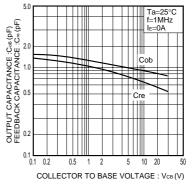
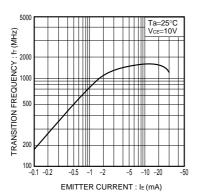
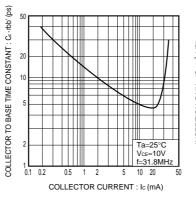
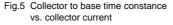


Fig.3 Capacitance vs. reverse bias voltage









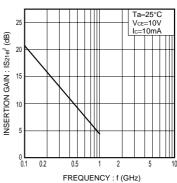


Fig.6 Insertion gain vs. frequency

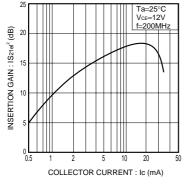
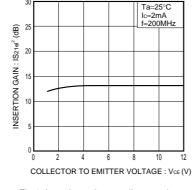
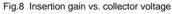
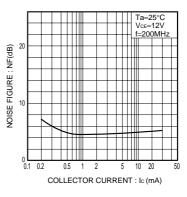


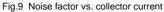
Fig.7 Insertion gain vs. collector current



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# EMX4 / UMX4N / IMX4

# Transistors

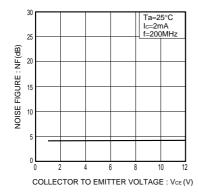


Fig.10 Noise factor vs. collector voltage



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Appendix1-Rev2.0

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