

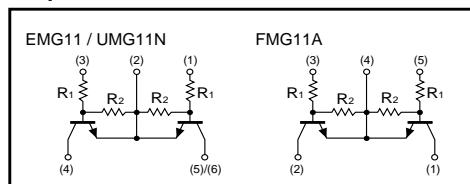
# Emitter common (dual digital transistors)

## EMG11 / UMG11N / FMG11A

### ●Features

- 1) Two DTA123Js chips in a EMT or UMT or SMT package.

### ●Equivalent circuit



### ●Package, marking, and packaging specifications

Type	EMG11	UMG11N	FMG11A
Package	EMT5	UMT5	SMT5
Marking	G11	G11	G11
Code	T2R	TR	T148
Basic ordering unit (pieces)	8000	3000	3000

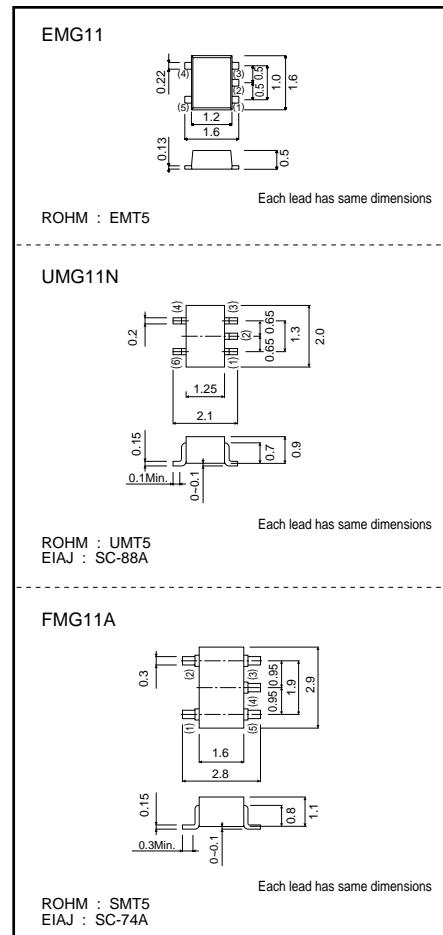
### ●Absolute maximum ratings ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Limits	Unit
Supply voltage	$V_{CC}$	50	V
Input voltage	$V_{IN}$	12 5	V
Output current	$I_O$	100	mA
Power dissipation	$P_d$	150(TOTAL) 300(TOTAL)	mW *1 *2
Storage temperature	$T_{STG}$	-50~+150	°C

\*1 120mW per element must not be exceeded.

\*2 200mW per element must not be exceeded.

### ●External dimensions (Units : mm)



### ●Electrical characteristics ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	$V_I$ (off)	—	—	0.5	V	$V_{CC}=5\text{V}$ , $I_O=100\mu\text{A}$
	$V_I$ (on)	1.1	—	—		$V_o=0.3\text{V}$ , $I_O=5\text{mA}$
Output voltage	$V_O$ (on)	—	0.1	0.3	V	$I_O=5\text{mA}$ , $I_I=0.25\text{mA}$
Input current	$I_I$	—	—	3.6	mA	$V_I=5\text{V}$
Output current	$I_O$ (off)	—	—	0.5	μA	$V_{CC}=50\text{V}$ , $V_I=0\text{V}$
DC current gain	$G_I$	80	—	—	—	$I_O=10\text{mA}$ , $V_O=5\text{V}$
Input resistance	$R_I$	—	2.2	—	kΩ	—
Transition frequency	$f_T$	—	250	—	MHz	$V_{CE}=10\text{V}$ , $I_E=-5\text{mA}$ , $f=100\text{MHz}$ *
Resistance ratio	$R_2/R_1$	17	21	26	—	—

\*Transition frequency of the device.

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