

General-purpose Operational Amplifiers /Comparators

TROPHY SERIES





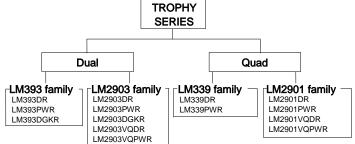
Comparators

LM393DR/PWR/DGKR,LM2903DR/PWR/DGKR/VQDR/VQPWR LM339DR/PWR,LM2901DR/PWR/VQDR/VQPWR

No.11094EBT03

Description

The Universal Standard family LM393 / LM399 / LM2903 / LM2901 monolithic ICs integrate two/four independent comparators on a single chip and feature high gain, low power consumption, and an operating voltage range from 2[V] to 36[V] (single power supply).



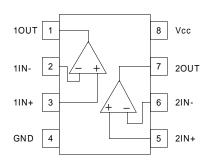
Features

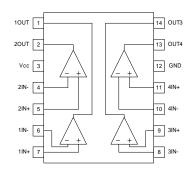
1) Operating temperature range

Commercial Grade LM339/393 family : $0[^{\circ}C]$ to + $70[^{\circ}C]$ Extended Industrial Grade LM2903/2901 family : $-40[^{\circ}C]$ to +125 $[^{\circ}C]$

- 2) Open collector output
- 3) Single / dual power supply compatible
- Low supply current 0.8[mA] typ. (LM393/339/2903/2901 family)
- 5) Low input-bias current: 25[nA] typ.
- 6) Low input-offset voltage: 2[mV] typ.
- 7) Differential input voltage range equal to maximum rating
- 8) Low output saturation voltage
- 9) TTL,MOS,CMOS compatible output

Pin Assignment





SOIC8

LM393DR LM2903DR LM2903VQDR

TSSOP8

LM393PWR LM2903PWR LM2903VQPWR

MSOP8/VSSOP8

LM393DGKR LM2903DGKR

SOIC14

LM339DR LM2901DR LM2901VQDR

TSSOP14

LM339PWR LM2901PWR LM2901VQPWR ● Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Ratings						
Farameter	Symbol	LM393 family	LM339 family	LM2903 family	LM2901 family	Unit		
Supply Voltage	Vcc-GND	+36						
Input Differential Voltage	Vid	±36						
Common-mode Input	Vicm	-0.3 to +36						
Operating Temperature	Topr	0 to +70 -40 to +125				°C		
Storage Temperature Range	Tstg	-65 to +150						
Maximum Junction Temperature	Tj	+150						

Electric Characteristics

OLM393/339 family(Unless otherwise specified, Vcc=+5[V])

		Temperature range	Limits										
Parameter	Symbol		LM393 family		nily	LI	M339 fan	nily	Unit	condition	Fig. No.		
			Min.	Тур.	Max.	Min.	Тур.	Max.					
Input Offset Voltage (*1)	VIO	25°C	_	2	7	_	2	7	mV	Vcc=5 to 30[V],VO=1.4[V]	88		
par ooor voltage		Full range	_	_	9	-	_	9		VIC=VIC(min)			
Input Offset Current (*1)	IIO	25°C	_	5	50	_	5	50	nA	VO=1.4[V]	88		
input onset ourient	110	Full range	-	_	250	-	_	150	117.	VO=1म्vj	00		
Input Bias Current (*1)	IIB	25°C	-	25	250	-	25	250	nA	VO=1.4[V]	88		
Input bias current	ПБ	Full range	-	_	400	-	_	400	IIA	VO=1.4[V]	00		
Common-mode Input	VICR	25°C	-	_	Vcc-1.5	-	_	Vcc-1.5		-	88		
Voltage Range	VICK	Full range	-	_	Vcc-2.0	_	_	Vcc-2.0	V		00		
Large Signal Differential Voltage Amplification	AVD	25°C	25	200	_	25	200	_	V/mA	Vcc=15[V] VO=1.4 to 11.4[V], RL ≥ 15[kΩ], VRL=15[V]	88		
High Level	IOH	25°C	-	0.1	-	-	0.1	_	nA	VID=1[V],VO=5[V]	89		
Output Current	1011	Full range	_	_	1	-	_	1	μΑ	VID=1[V],VO=30[V]	00		
Low Level	VOL	25°C	-	150	400	-	150	400	mV	\/\D_4D/LIQL_4[mA]	89		
Output Voltage	VOL	Full range	-	_	700	_	_	700	mv	VID=-1[V],IOL=4[mA]			
Low Level Output Current	IOL	25°C	6	_	-	6	16	_	mA	VID=-1[V],VOL=1.5[V]	89		
Commello Comment	100	25°C	-	0.8	1	-	0.8	2	^	RL=∞,Vcc=5V	- 89		
Supply Current	ICC	Full range	-	_	2.5	_	_	_	mA	RL=∞,Vcc=30[V]			
Response Time	Tre	Tre	Tro	25°C	_	1.3	_	_	1.3	_	μs	RL=5.1[k Ω],VRL=5[V],CL=15pF VIN=100[mVp-p], overdrive=5[mV]	89
			230	-	0.3	_	-	0.3	_	PO	RL=5.1[kΩ],VRL=5[V], CL=15pF VIN=TTL-Level input step Vref=1.4[V]	30	

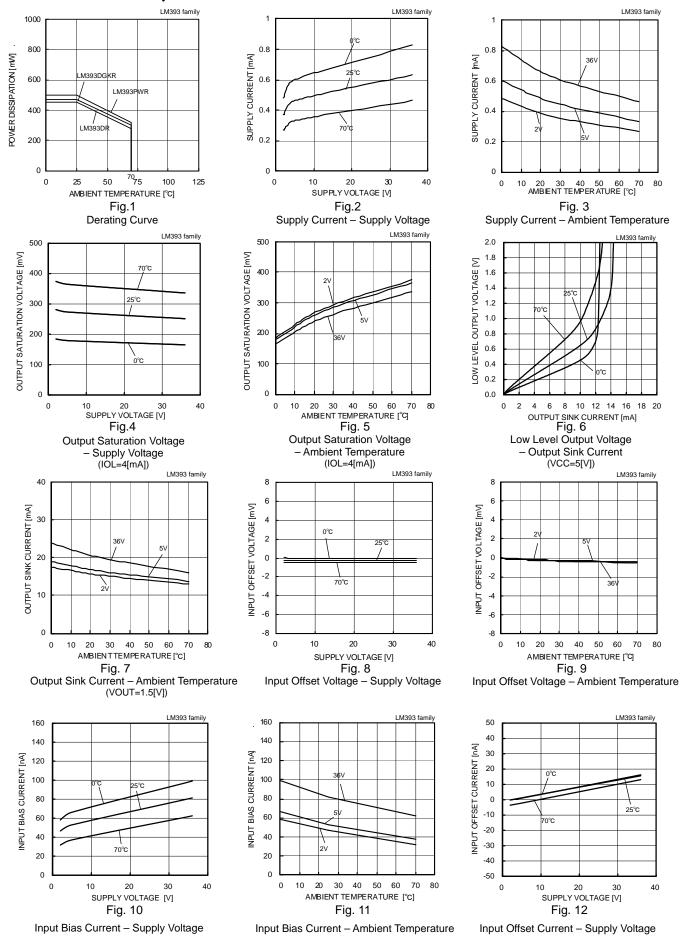
^(*1) Absolute value

OLM2903/2901 family(Unless otherwise specified, Vcc=+5[V])

Parameter					Limits								
		Symbol	Temperature range	LM2903 family			LM2901 family			Unit	Condition	Fig. No	
				Min.	Тур.	Max.	Min.	Тур.	Max.				
Input Offset Voltage (*2)		VIO	25°C	-	2	7	_	2	7	mV	Vcc=5 to MAX ⁾ ,VO=1.4[V]	88	
	Tonago		Full range	-	_	15	_	_	15		VIC=VIC (min)		
Input Offset	Current (*2)	IIO	25°C	-	5	50	-	5	50	nA	VO=1.4[V]	88	
			Full range	1	_	200	-	_	200		(.)		
Input Bias C	Current (*2)	IIB	25°C	-	25	250	-	25	250	nA	VO=1.4[V]	88	
			Full range	1	_	500	-	_	500		VO_1. [V]		
Common-mo		VICR	25°C	1	_	Vcc-1.5	-	_	Vcc-1.5	٧	_	88	
Voltage Ran	nge		Full range	-	_	Vcc-2.0	_	_	Vcc-2.0	•			
Large Signa Voltage Amp		AVD	25°C	25	100	_	25	100	_	V/mV	$Vcc=15[V], VOUT=1.4 to 11.4[V], RL \ge 15[kΩ], VRL=15[V]$	88	
High Level		ЮН	25°C	-	0.1	_	_	0.1	_	nA	VID=1[V], VOH=5[V]	89	
Output Curre	ent	1011	Full range	-	_	1	_	_	1	μΑ	VID=1[V], VOH=MAX		
	LM2901 ^(*3)		25°C	-	150	400	_	150	500				
Low Level Output Voltage	LM2901V ^(*3)	VOL	25°C	-	150	400	_	150	400	mV	VIN(-)=1[V],VIN(+)=0[V] ISINK≦4[mA]	89	
			Full range	-	_	700	_	_	700				
Low Level C	Output Current	IOL	25°C	6	16	_	6	16	-	mA	VID=-1[V], VOL=1.5[V]	89	
Supply Current		ICC	25°C	-	0.8	2	_	0.8	2	mA	RL=∞,Vcc=5V	89	
		100	250	-	1	2.5	_	1	2.5	IIIA	RL=∞,Vcc=MAX(*7)	OS	
Deenens T	"m a	Tro	25°C	_	1.3	_	_	1.3	_		RL=5.1[Q],VRL=5[V],CL=15pF VIN=100[mVp-p], Overdrive=5[mV]	90	
Response Time		Tre	25°C	-	0.3	_	_	0.3	_	μs	RL=5.1[kΩ],VRL=5[V], CL=15pF VIN=TTL-Level input step Vref=1.4[V]	89	

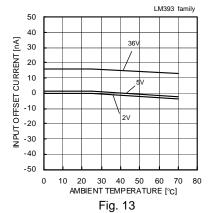
^(*2) Absolute value
(*3) Supply Voltage Maximum Value LM2901DR, LM2901PWR MAX=30[V], LM2901VQDR, LM2901VQPWR MAX=32[V]

● Reference Data LM393 family

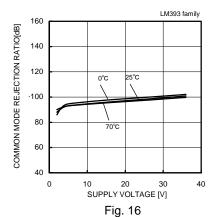


(*)The data above is ability value of sample, it is not guaranteed. LM393family:0[°C]~+70[°C]

● Reference Data LM393 family



Input Offset Current – Ambient Temperature



Common Mode Rejection Ratio
– Supply Voltage

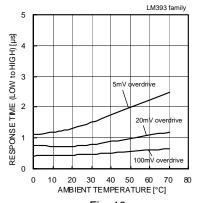
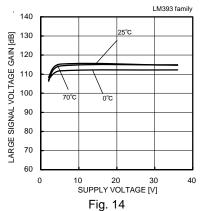
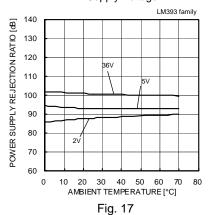


Fig. 19
Response Time (Low to High)
– Ambient Temperature
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])



Large Signal Voltage Gain
– Supply Voltage



Common Mode Rejection Ratio

– Ambient Temperature

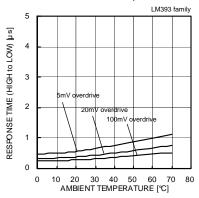
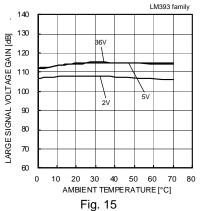


Fig. 20
Response Time (High to Low)
–Ambient Temperature
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])



Large Signal Voltage Gain

– Ambient Temperature

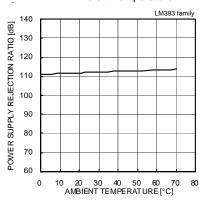
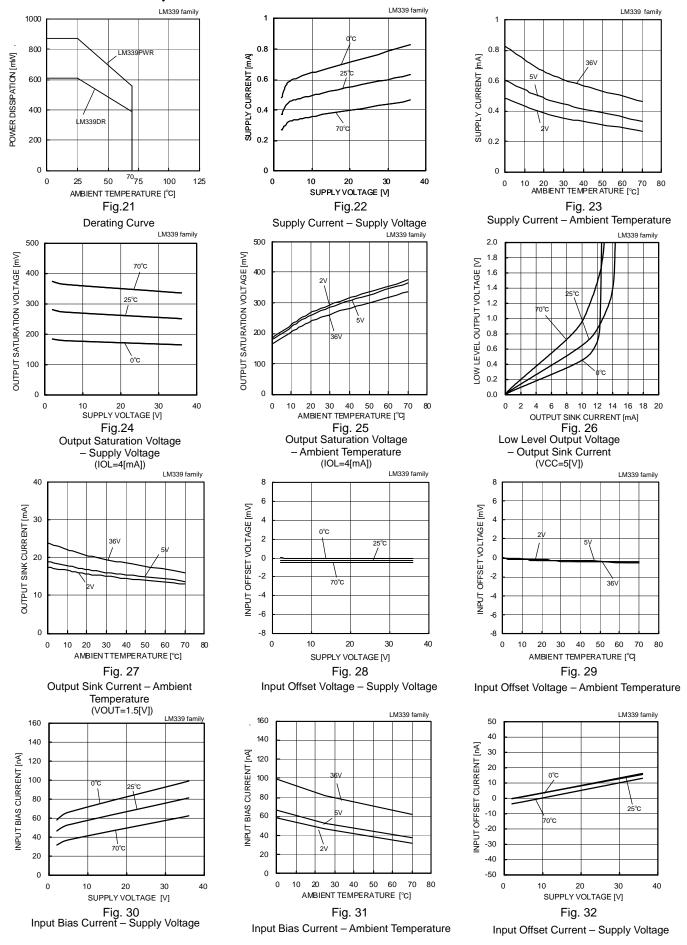


Fig. 18
Power Supply Rejection Ratio
– Ambient Temperature

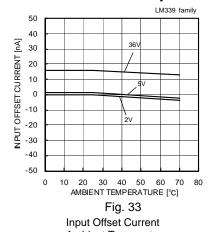
(*)The data above is ability value of sample, it is not guaranteed. $LM393family:0[^{\circ}C] \sim +70[^{\circ}C]$

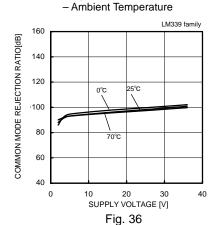
● Reference Data LM339 family

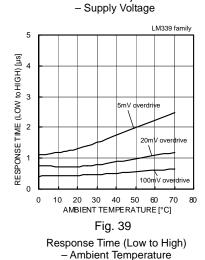


(*)The data above is ability value of sample, it is not guaranteed. LM339family:0[°C]~+70[°C]

● Reference Data LM339 family

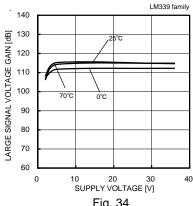


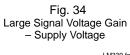


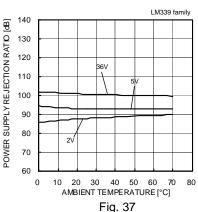


 $(VCC=5[V],VRL=5[V],RL=5.1[k\Omega])$

Common Mode Rejection Ratio







Common Mode Rejection Ratio

– Ambient Temperature

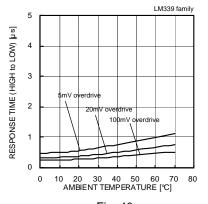
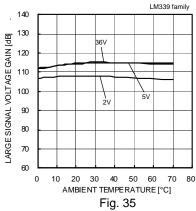
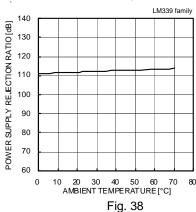


Fig. 40
Response Time (High to Low)
–Ambient Temperature
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])



Large Signal Voltage Gain

– Ambient Temperature

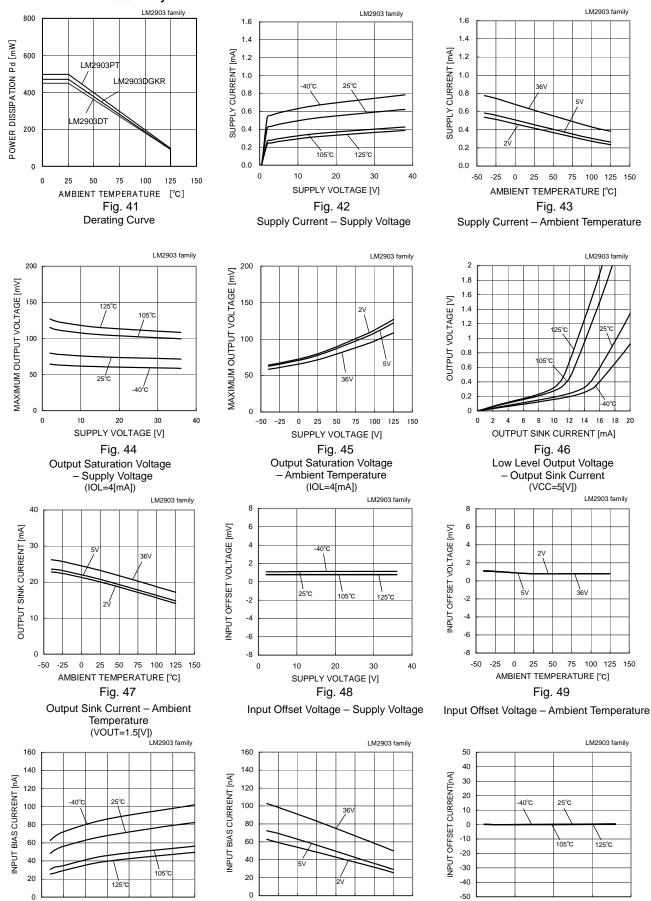


Power Supply Rejection Ratio

– Ambient Temperature

(*)The data above is ability value of sample, it is not guaranteed. BA10393F:-40[°C]~+70[°C]

● Reference Data LM2903 family



10 15 20 25 30

SUPPLY VOLTAGE [V]

Fig. 50

Input Bias Current - Supply Voltage

0

AMBIENT TEMPERATURE [°C]

Fig. 51

Input Bias Current - Ambient Temperature

100 125 150

0

-50 -25 0 25 50 75

(*)The data above is ability value of sample, it is not guaranteed.LM2903family:-40[°C]~+125[°C]

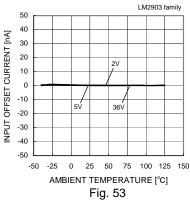
20

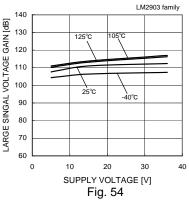
SUPPLY VOLTAGE [V] Fig. 52

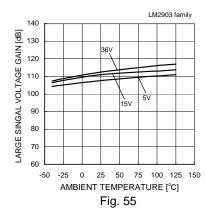
Input Offset Current - Supply Voltage

30

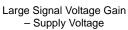
● Reference Data LM2903 family





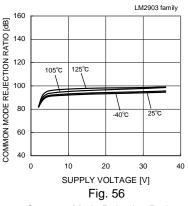


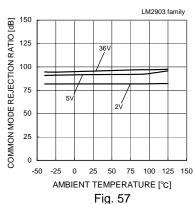
Input Offset Current – Ambient Temperature

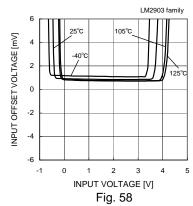


Large Signal Voltage Gain

– Ambient Temperature



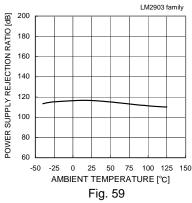


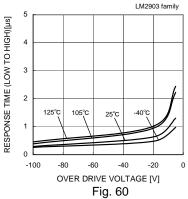


Common Mode Rejection Ratio
– Supply Voltage

Common Mode Rejection Ratio
- Ambient Temperature

Input Offset Voltage – Input Voltage (VCC=5V)





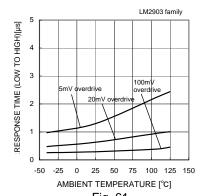


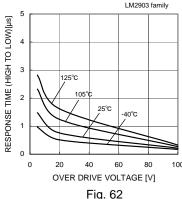
Fig. 59
Power Supply Rejection Ratio
– Ambient Temperature

Fig. 60

Response Time (Low to High)

– Over Drive Voltage
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

Fig. 61
Response Time (Low to High)
– Ambient Temperature
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])



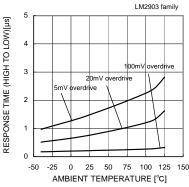


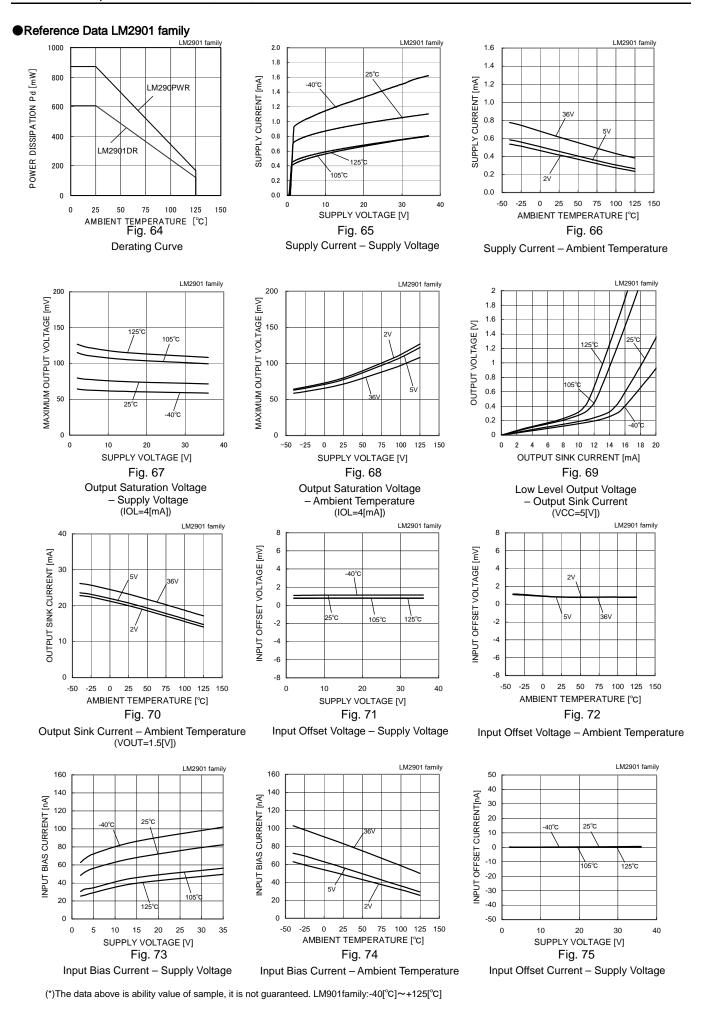
Fig. 62
Response Time (High to Low)

– Over Drive Voltage
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

Fig. 63
Response Time (High to Low)

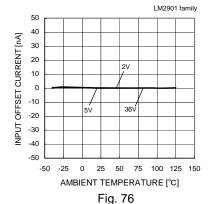
– Ambient Temperature
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

(*)The data above is ability value of sample, it is not guaranteed. LM2903family:-40[°C] \sim +125[°C]



10/17

● Reference Data LM2901 family



Input Offset Current – Ambient Temperature

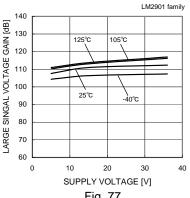


Fig. 77
Large Signal Voltage Gain
– Supply Voltage

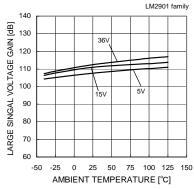


Fig. 78
Large Signal Voltage Gain
– Ambient Temperature

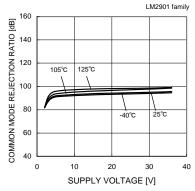


Fig. 79
Common Mode Rejection Ratio
– Supply Voltage

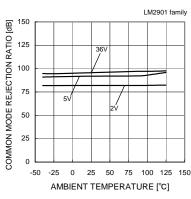


Fig. 80
Common Mode Rejection Ratio
– Ambient Temperature

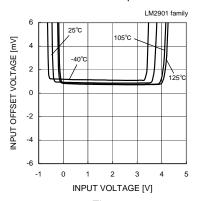
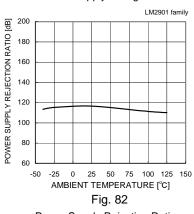
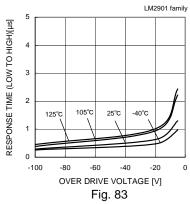


Fig. 81
Input Offset Voltage – Input Voltage (VCC=5V)

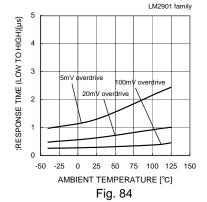


Power Supply Rejection Ratio
– Ambient Temperature



Response Time (Low to High)

– Over Drive Voltage
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])



Response Time (Low to High)

– Ambient Temperature
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

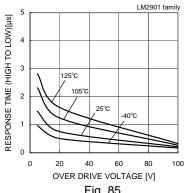


Fig. 85
Response Time (High to Low)
– Over Drive Voltage
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

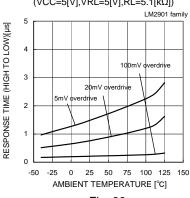


Fig. 86
Response Time (High to Low)
– Ambient Temperature
(VCC=5[V],VRL=5[V],RL=5.1[kΩ])

(*)The data above is ability value of sample, it is not guaranteed. LM901family:-40[°C] \sim +125[°C]

●Circuit Diagram

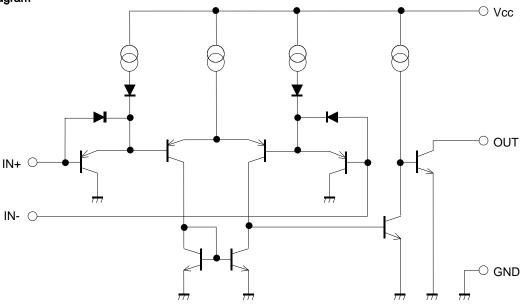


Fig.87 Circuit Diagram (each Comparator)

Measurement circuit 1 NULL Method measurement condition

Vcc,GND,EK,VICR Unit: [V]

100,0115,211,11011 0 mt : [1]													
Parameter	VF	S1 S2		S3	LM	393/LM	1339 far	mily	LM2	903/LN	12901 fa	amily	Calculation
Farameter	VF	31	32	33	Vcc	GND	EK	VICR	Vcc	GND	EK	VICR	Calculation
Input Offset Voltage	VF1	ON	ON	ON	5 to 30	0	-1.4	0	5 to 30	0	-1.4	0	1
Input Offset Current	VF2	OFF	OFF	ON	5	0	-1.4	0	5	0	-1.4	0	2
Input Dica Current	VF3	OFF	ON		5	0	-1.4	0	5	0	-1.4	0	2
Input Bias Current	VF4	ON	OFF	ON	5	0	-1.4	0	5	0	-1.4	0	3
Large Signal	VF5	ON	ON	ON ON	15	0	-1.4	0	15	0	-1.4	0	4
Voltage Gain	VF6	ON	ON		15	0	-11.4	0	15	0	-11.4	0	+

-Calculation-

1.Input offset voltage (VIO)

$$Vio = \frac{|VF1|}{1 + Rf/Rs} [V]$$

2.Input offset current (IIO)

$$lio = \frac{|VF2 - VF1|}{Ri(1+ Rf / Rs)}[A]$$

3.Input bias current (IIb)

$$Ib = \frac{|VF4 - VF3|}{2 \times Ri(1 + Rf / Rs)} [A]$$

4.Large signal differential voltage gain (AVD)

AV =
$$20x \text{ Log } \frac{10x (1+Rf/Rs)}{|VF6-VF5|}$$
 [dB]

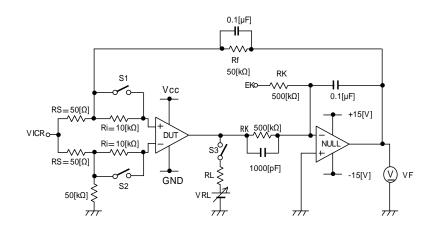


Fig.88 Measurement Circuit1 (each Comparator)

● Measurement Circuit2 Switch Condition

SW No.		SW 1	SW 2	SW 3	SW 4	SW 5	SW 6	SW 7
Supply Current	_	OFF						
Low Level Output Current	VOL=1.5[V]	OFF	ON	ON	OFF	ON	ON	OFF
Low Level Output Current	IOL=4[mA]	OFF	ON	ON	OFF	OFF	OFF	ON
High Level Output Current	VOH=36[V]	OFF	ON	ON	OFF	OFF	OFF	ON
Response Time	RL=5.1[kΩ] VRL=5[V]	ON	OFF	ON	ON	OFF	ON	OFF

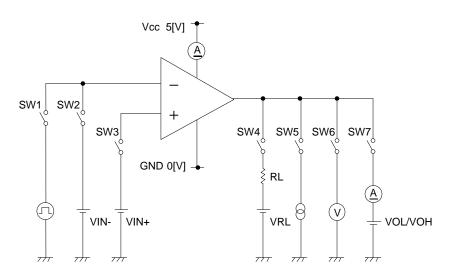


Fig.89 Measurement Circuit2 (each channel)

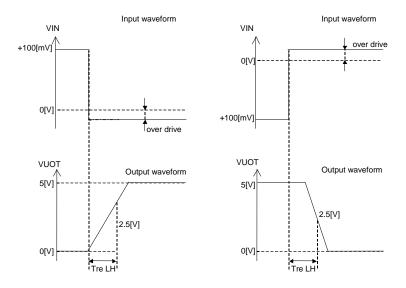


Fig.90 Response Time

Description of Electrical Characteristics

Described below are descriptions of the relevant electrical terms.

Please note that item names, symbols, and their meanings may differ from those on another manufacturer's documents.

1. Absolute maximum ratings

The absolute maximum ratings are values that should never be exceeded, since doing so may result in deterioration of electrical characteristics or damage to the part itself as well as peripheral components.

1.1 Power supply voltage (Vcc/GND)

Expresses the maximum voltage that can be supplied between the positive and negative power supply terminals without causing deterioration of the electrical characteristics or destruction of the internal circuitry.

1.2 Differential input voltage (VID)

Indicates the maximum voltage that can be supplied between the non-inverting and inverting terminals without damaging the IC.

1.3 Input common-mode voltage range (VICR)

Signifies the maximum voltage that can be supplied to non-inverting and inverting terminals without causing deterioration of the electrical characteristics or damage to the IC itself. Normal operation is not guaranteed within the input common-mode voltage range of the maximum ratings – use within the input common-mode voltage range of the electric characteristics instead.

1.4 Operating temperature range and storage temperature range (Topr,Tstg)

The operating temperature range indicates the temperature range within which the IC can operate. The higher the ambient temperature, the lower the power consumption of the IC. The storage temperature range denotes the range of temperatures the IC can be stored under without causing excessive deterioration of the electrical characteristics.

1.5 Power dissipation (Pd)

Indicates the power that can be consumed by a particular mounted board at ambient temperature (25°C). For packaged products, Pd is determined by maximum junction temperature and the thermal resistance.

2. Electrical characteristics

2.1 Input offset voltage (VIO)

Signifies the voltage difference between the non-inverting and inverting terminals. It can be thought of as the input voltage difference required for setting the output voltage to 0V.

2.2 Input offset current (IIO)

Indicates the difference of the input bias current between the non-inverting and inverting terminals.

2.3 Input bias current (IIB)

Denotes the current that flows into or out of the input terminal, it is defined by the average of the input bias current at the non-inverting terminal and the input bias current at the inverting terminal.

2.4 Input common-mode voltage range (VICR)

Indicates the input voltage range under which the IC operates normally.

2.5 Large signal differential voltage gain (AVD)

The amplifying rate (gain) of the output voltage against the voltage difference between the non-inverting and inverting terminals, it is (normally) the amplifying rate (gain) with respect to DC voltage.

AVD = (output voltage fluctuation) / (input offset fluctuation)

2.6 Supply current (ICC)

Indicates the current of the IC itself that flows under specific conditions and during no-load steady state.

2.7 Low level output current (IOL)

Denotes the maximum current that can be output under specific output conditions.

2.8 Low level output voltage (VOL)

Signifies the voltage range that can be output under specific output conditions.

2.9 High level output current (IOH)

Indicates the current that flows into the IC under specific input and output conditions.

2.10 Response time (tre)

The interval between the application of input and output conditions.

2.11 Common-mode rejection ratio (CMRR)

Denotes the ratio of fluctuation of the input offset voltage when the in-phase input voltage is changed (DC fluctuation).

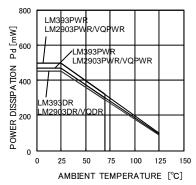
CMRR = (change of input common-mode voltage) / (input offset fluctuation)

2.12 Power supply rejection ratio (PSRR)

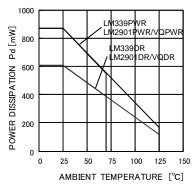
Signifies the ratio of fluctuation of the input offset voltage when the supply voltage is changed (DC fluctuation).

PSRR = (change in power supply voltage) / (input offset fluctuation)

Derating Curves



LM393DR/PWR/DGKR LM2903DR/PWR/DGKR/VQDR/VQPWR



LM339DR/PWR LM2901DR/PWR/VQDR/VQPWR

Power Dissipation

Package	Pd[W]	θ ja [°C/W]
SOIC8 (*8)	450	3.6
TSSOP8 (*6)	500	4.0
MSOP8/VSSOP8 (*7)	470	3.76

 θ ja = (Tj-Ta)/Pd[°C/W]

Power Dissipation

Package	Pd[W]	<i>θ</i> ja [°C/W]		
SOIC14	610	4.9		
TSSOP14	870	7.0		

 $\theta_{ja} = (T_{j}-T_{a})/Pd[^{\circ}C/W]$

Fig.91 Derating Curves

Precautions

1) Unused circuits

When there are unused circuits it is recommended that they be connected as in Fig.92, setting the non-inverting input terminalto a potential within the in-phase input voltage range (VICR).

2) Input terminal voltage

Applying GND + 36V to the input terminal is possible without causing deterioration of the electrical characteristics or destruction, irrespective of the supply voltage. However, this does not ensure normal circuit operation.

Please note that the circuit operates normally only when the input voltage is within the common mode input voltage range of the electric characteristics.

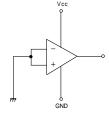


Fig.92 Disable circuit example

3) Power supply (single / dual)

The op-amp operates when the specified voltage supplied is between Vcc and GND. Therefore, the single supply op-amp can be used as a dual supply op-amp as well.

4) Power dissipation Pd

Using the unit in excess of the rated power dissipation may cause deterioration in electrical characteristics due to a rise in chip temperature, including reduced current capability. Therefore, please take into consideration the power dissipation (Pd) under actual operating conditions and apply a sufficient margin in thermal design. Refer to the thermal derating curves for more information.

5) Short-circuit between pins and erroneous mounting

Incorrect mounting may damage the IC. In addition, the presence of foreign particles between the outputs, the output and the power supply, or the output and GND may result in IC destruction.

6) Terminal short-circuits

When the output and Vcc terminals are shorted, excessive output current may flow, resulting in undue heat generation and, subsequently, destruction.

7) Operation in a strong electromagnetic field

Operation in a strong electromagnetic field may cause malfunctions.

8) Radiation

This IC is not designed to withstand radiation.

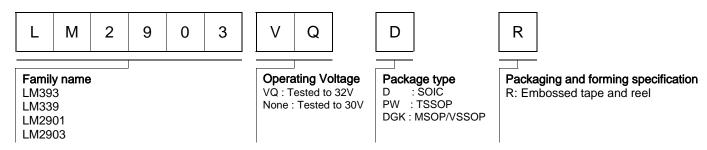
9) IC handing

Applying mechanical stress to the IC by deflecting or bending the board may cause fluctuations in the electrical characteristics due to piezoelectric (piezo) effects.

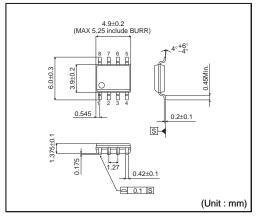
10) Board inspection

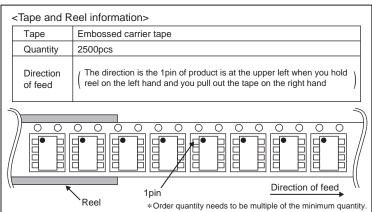
Connecting a capacitor to a pin with low impedance may stress the IC. Therefore, discharging the capacitor after every process is recommended. In addition, when attaching and detaching the jig during the inspection phase, ensure that the power is turned OFF before inspection and removal. Furthermore, please take measures against ESD in the assembly process as well as during transportation and storage.

Ordering part number

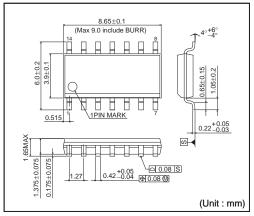


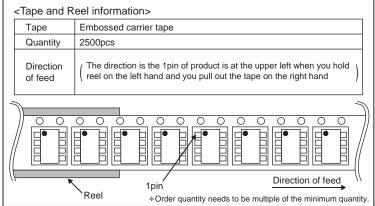
SOIC8



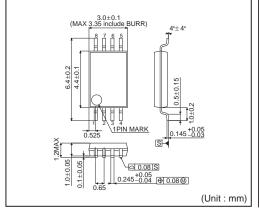


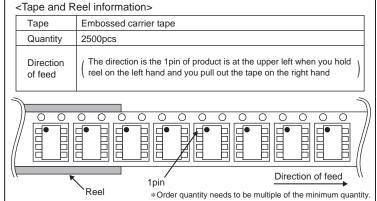
SOIC14



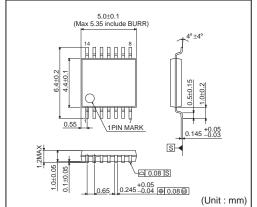


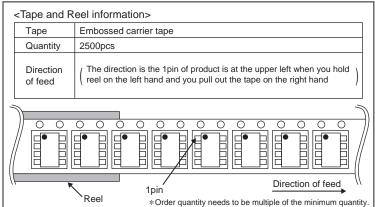
TSSOP8



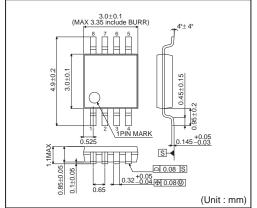


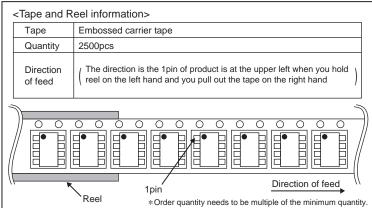
TSSOP14





MSOP / VSSOP8





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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA	
CLASSⅢ	CLASSⅢ	CLASS II b	CLASSIII	
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII	

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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

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Since our Products might fall under controlled goods prescribed by the applicable foreign exchange and foreign trade act, please consult with ROHM representative in case of export.

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