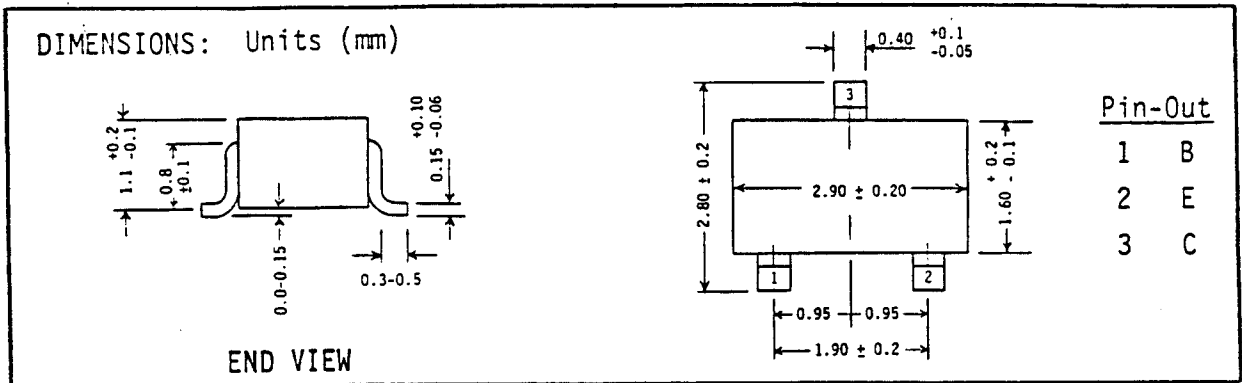


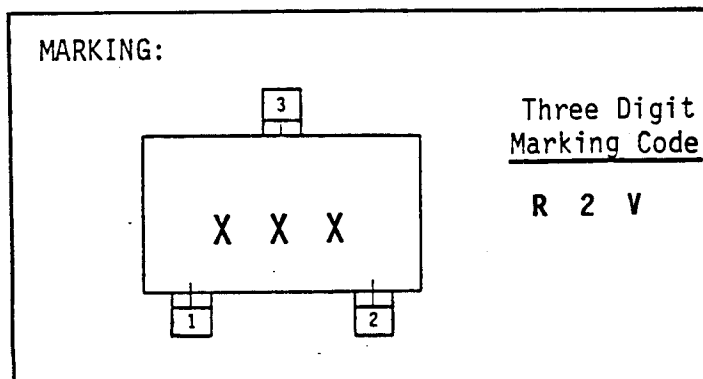
CUSTOMER	NO. MMST-A64
	PAGE 1 of 2
SUBJECT SOT-23 TRANSISTOR, PNP, SILICON	DATE January 14, 1987

ABSOLUTE MAXIMUM RATINGS: (Ta = 25°C)

Collector-Base Voltage	V _{CBO}	30 V
Collector-Emitter Voltage	V _{CEO}	30 V
Emitter-Base Voltage	V _{EBO}	10 V
Collector Current	I _C	500 mA
Power Dissipation-Free Air	P _D	200 mW
Power Dissipation-Ceramic Substrate	P _D	350 mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-55 to 150 °C
Solder Temperature (10 seconds)		260 °C



THE JAPANESE STYLE SC-59 PACKAGE



- PACKAGING:**
- _____ BULK, 500 per BAG
 - _____ MAGAZINES OF 50 EACH
 - _____ 8mm T&R, T- 146 3K/REEL
 - _____ 8mm T&R, T- 147 3K/REEL
 - _____ 8mm T&R, T- 24610K/REEL
 - _____ 8mm T&R, T- 24710K/REEL

REMARKS: PROCESS: B-25 Thermal Resistance R_{θJA} 625 °C/Watt
FREE AIR, T_A = 25°C

ROHM ELECTRONICS
3034 Owen Drive, Antioch, TN 37013
TEL:(615)641-2020 FAX:(615)641-2022

APPROVAL <i>[Signature]</i> 10/12/87	CHECK ✓	DESIGN
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CUSTOMER	NO.	MMST-A64	
	PAGE	2 of 2	
SUBJECT	TRANSISTOR, PNP ,SILICON	SOT-23	
		DATE	January 14, 1987

ELECTRICAL CHARACTERISTICS: (Ta = 25°C Unless Otherwise Specified)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V_{CES}	$I_C = 100 \mu A$	30			V
I_{CBO}	$V_{CB} = 30 V$		1.0	100	nA
I_{EBO}	$V_{EB} = 10 V$		1.0	100	nA
h_{FE}	$I_C = 10 mA, V_{CE} = 5 V$	10,000			
h_{FE}	$I_C = 100 mA, V_{CE} = 5 V$	20,000			
$V_{CE(SAT)}$	$I_C = 100 mA, I_B = 100 \mu A$			1.5	V
$V_{BE(ON)}$	$I_C = 100 mA, V_{CE} = 5 V$			2.0	V
f_T	$I_C = 100 mA, V_{CE} = 5 V, f = 100MHz$	125			MHz
C_{ob}	$V_{CB} = 10 V, I_E = 0, f = 100KHz$		4.0		pF
h_{fe}	$I_C = 10 mA, V_{CE} = 5 V, f = 1.0KHz$		35,000		
NF	$I_C = 1.0 mA, V_{CE} = 5 V, R_s = 100K\Omega$ $f = 1.0 KHz$		2.0		dB

R-Ohm Corporation P.O. Box 19515 Irvine, California 92713	APPROVAL	CHECK	DESIGN
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