-20V Pch +Pch Middle Power MOSFET

V _{DSS}	-20V
R _{DS(on)} (Max.)	38mΩ
I _D	±5.0A
P _D	1.5W

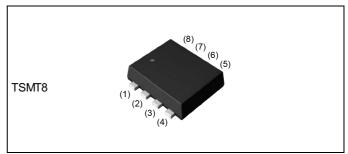
Features

- 1) Low on resistance.
- 2) Small surface mount package(TSMT8)
- 3) -1.8V Drive.
- 4) Pb-free lead plating; RoHS compliant

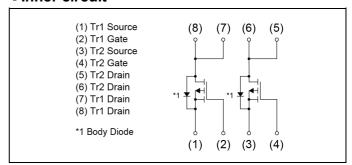
Application

Switching

Outline



•Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
	Quantity (pcs)	3000
	Taping code	TCR
	Marking	JA1

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified) < Tr1 and Tr2>

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	-20	V
Continuous drain current	I _D *1	±5.0	Α
Pulsed drain current	I _{DP} *2	±18	Α
Gate - Source voltage	V _{GSS}	±10	V
Avalanche current, single pulse	I _{AS} *3	-5.0	Α
Avalanche energy, single pulse	E _{AS} *3	8.9	mJ
	P _D *4	1.5	14/
Power dissipation (total)	P _D *5	1.1	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Downwater	Cymabal	Values			1.1
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registeres innetion, embient (total)	R _{thJA} *4	-	-	83.3	°C/W
Thermal resistance, junction - ambient (total)	R _{thJA} *4	-	-	100	C/VV

● Electrical characteristics (T_a = 25°C) < Tr1 and Tr2>

D 4		0 111	Values			
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = -1mA$		-	-	V
Breakdown voltage	ΔV _{(BR)DSS}	I _D = -1mA	-	-10.3	-	mV/°C
temperature coefficient	ΔT_{j}	referenced to 25°C		10.0		,
Zero gate voltage drain current	I _{DSS}	V _{DS} = -20V, V _{GS} = 0V		-	-1	μA
Gate - Source leakage current	I _{GSS}	V _{GS} = ±10V, V _{DS} = 0V		-	±100	nA
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -1mA$	-0.5	-	-1.2	V
Gate threshold voltage	$\Delta V_{GS(th)}$	I _D = -1mA		4.7	-	mV/°C
temperature coefficient	ΔT _j	referenced to 25°C	-	1.7		
		V _{GS} = -4.5V, I _D = -5.0A	-	28	38	
Static drain - source on - state resistance	R _{DS(on)} *6	V _{GS} = -2.5V, I _D = -5.0A	-	35	48	mΩ
		V _{GS} = -1.8V, I _D = -1.2A	-	49	77	
Gate resistance	R_{G}	f = 1MHz, open drain	-	11	-	Ω
Forward Transfer Admittance	Y _{fs} *6	$V_{DS} = -5V, I_{D} = -5A$	5.5	-	-	S

^{*1} Vgs≧2.5V

^{*2} Pw \leq 10 μ s , Duty cycle \leq 1%

^{*3} L \simeq 0.5mH, V_{DD} = -10V, R_G = 25 Ω , STARTING T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*4} Mounted on a ceramic board (30×30×0.8mm)

^{*5} Mounted on a FR4 (25×25×0.8mm)

^{*6} Pulsed

● Electrical characteristics (T_a = 25°C) < Tr1 and Tr2>

Daramatar	Cumbal	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C _{iss}	V _{GS} = 0V	-	720	-	
Output capacitance	C _{oss}	V _{DS} = -10V	-	140	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	125	-	
Turn - on delay time	t _{d(on)} *6	$V_{DD} \simeq -10V, V_{GS} = -4.5V$	-	11	-	
Rise time	t _r *6	I _D = -2.5A	-	32	-	
Turn - off delay time	t _{d(off)} *6	$R_L = 4\Omega$	-	70	-	ns
Fall time	t _f *6	$R_G = 10\Omega$	-	65	-	

ullet Gate charge characteristics (T_a = 25°C) <Tr1 and Tr2>

Parameter	Symbol Conditions		Values			- Unit
raianetei	Symbol	Conditions	Min.	Тур.	Max.	Offit
Total gate charge	Q_g^{*6}		-	10.2	-	
Gate - Source charge	Q _{gs} *6	$V_{DD} \simeq -10V, I_{D} = -5.0A$ $V_{GS} = -4.5V$	-	1.3	-	nC
Gate - Drain charge	Q _{gd} *6	1 63 110 1	-	3.4	-	

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

<Tr1 and Tr2>

Darameter	Cumbal	Conditions	Values			l leit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Continuous forward current	I _S	T - 25°C	-	-	-1.0	^	
Pulse forward current	I _{SP} *2	T _a = 25°C	-	-	-18	А	
Forward voltage	V _{SD} *6	V _{GS} = 0V, I _S = -1A	-	-	-1.2	V	

Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

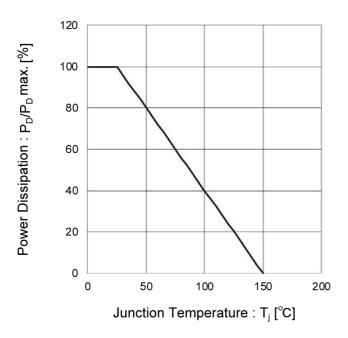
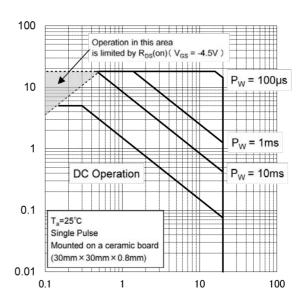


Fig.2 Maximum Safe Operating Area



Drain Current: -l_D [A]

Drain - Source Voltage: -VDS [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

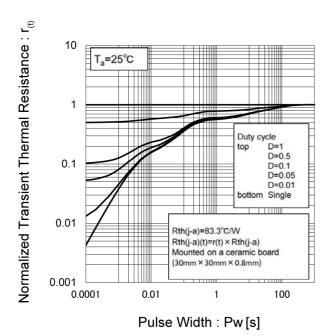
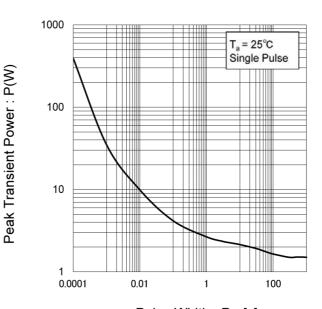


Fig.4 Single Pulse Maximum Power dissipation

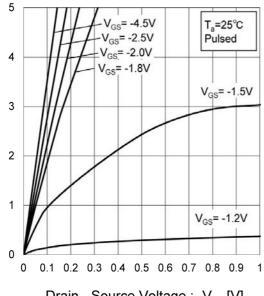


Pulse Width: Pw[s]

Drain Current: -l_D [A]

Electrical characteristic curves

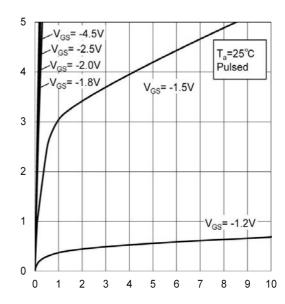
Fig.5 Typical Output Characteristics(I)



Drain Current : -I_D [A]

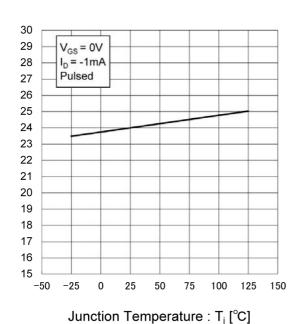
Drain - Source Voltage : -V_{DS} [V]

Fig.6 Typical Output Characteristics(II)



Drain - Source Voltage : -V_{DS} [V]

Fig.7 Breakdown Voltage vs. **Junction Temperature**



Drain-Source Breakdown Voltage: -V_{(BR)DSS} [V]

• Electrical characteristic curves

Fig.8 Typical Transfer Characteristics

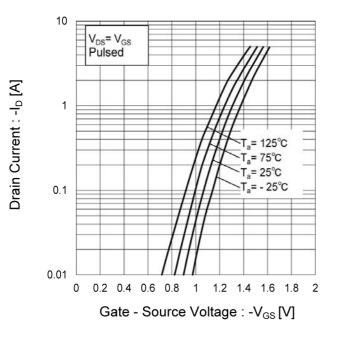


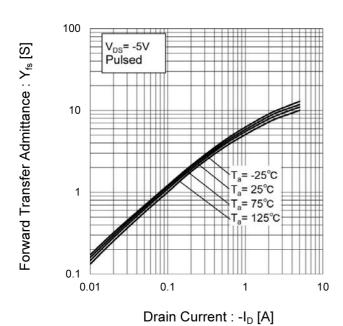
Fig.9 Gate Threshold Voltage vs.
Junction Temperature

1.5 1.4 $I_D = -1 \text{mA}$ 1.3 Pulsed 1.2 1.1 1 0.9 0.8 0.7 0.6 0.5 0.4 0.3 0.2 0.1 -50 -25 50 75 100 125 25

Gate Threshold Voltage: -V_{GS(th)} [V]

Junction Temperature : T_j [°C]

Fig.10 Forward Transfer Admittance vs.
Drain Current



6/11

• Electrical characteristic curves

Fig.11 Drain Current Derating Curve

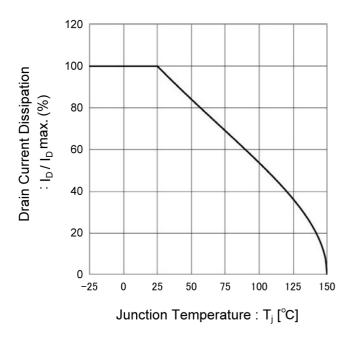


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

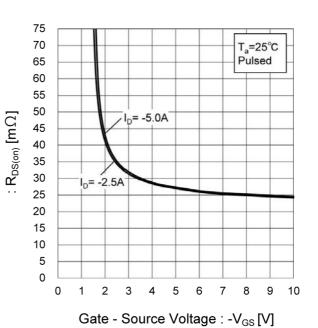
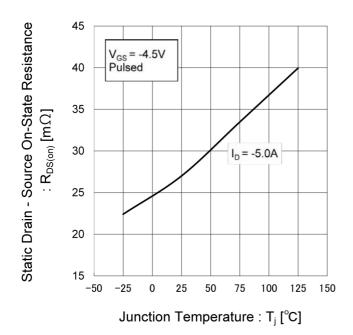


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



Static Drain - Source On-State Resistance

• Electrical characteristic curves

Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

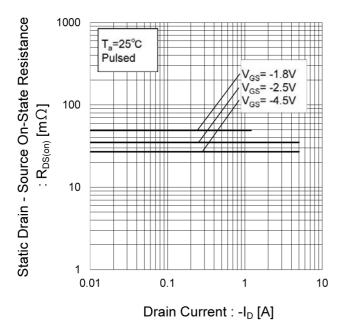


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

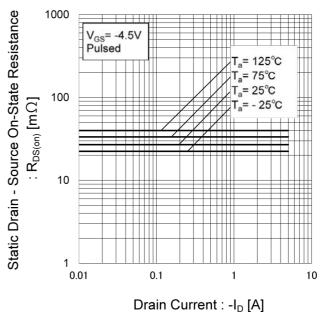


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

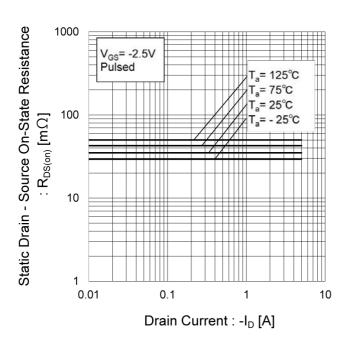
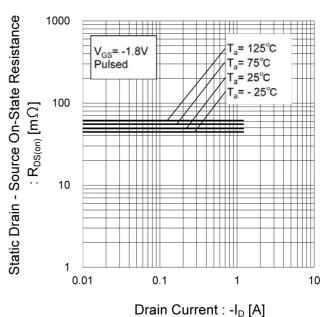


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)



Electrical characteristic curves

Fig.18 Typical Capacitance vs. Drain - Source Voltage

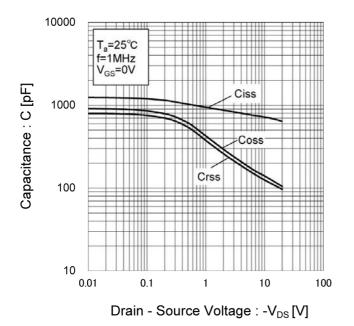


Fig.19 Switching Characteristics

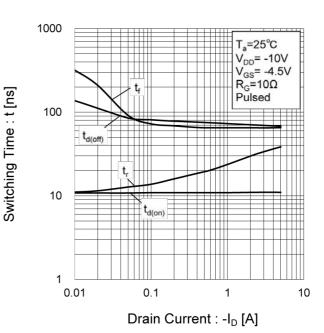
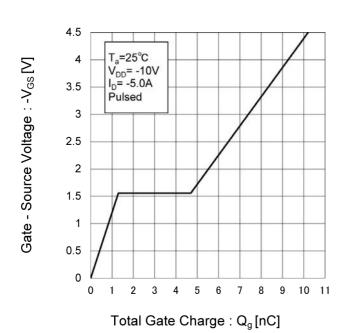
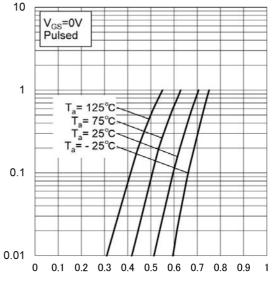


Fig.20 Dynamic Input Characteristics



10 V_{GS}=0V Pulsed

Fig.21 Source Current vs.



Source Drain Voltage

Source Current : -I_s [A]

• Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

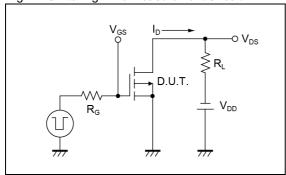


Fig.2-1 Gate Charge Measurement Circuit

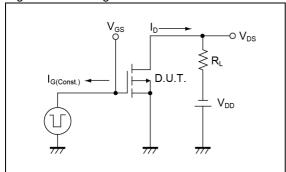


Fig.3-1 Avalanche Measurement Circuit

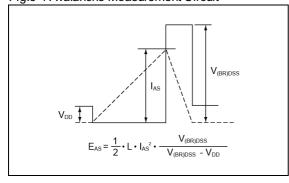


Fig.1-2 Switching Waveforms

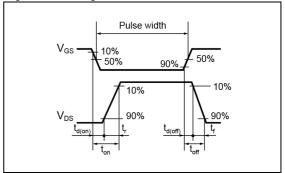


Fig.2-2 Gate Charge Waveform

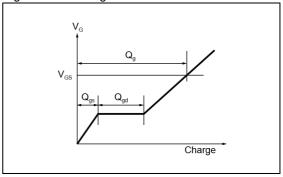
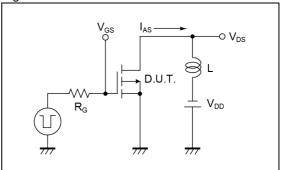


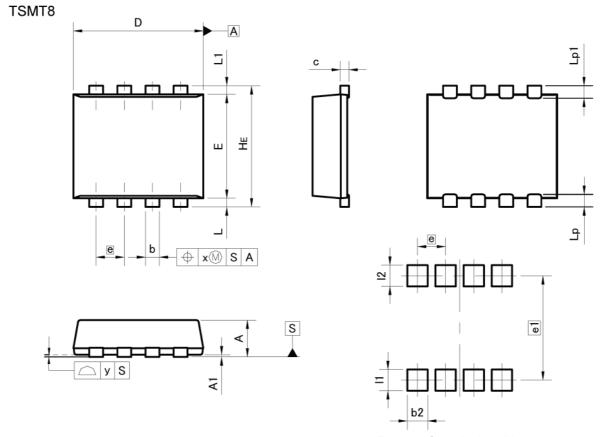
Fig.3-2 Avalanche Waveform



Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.75	0.85	0.030	0.033
A1	0.00	0.05	0.000	0.002
b	0.27	0.37	0.011	0.015
С	0.12	0.22	0.005	0.009
D	2.90	3.10	0.114	0.122
E	2.30	2.50	0.091	0.098
е	0.	65	0.026	
HE	2.70	2.90	0.106	0.114
L	0.10	0.30	0.004	0.012
L1	0.10	0.30	0.004	0.012
Lp	0.19	0.39	0.007	0.015
Lp1	0.19	0.39	0.007	0.015
х	10 10	0.10	70.	0.004
У	25	0.10	_	0.004

DIM	MILIM	ETERS	INC	HES
DIM L	MIN	MAX	MIN	MAX
b2	2044	0.47	-	0.019
e1	2.	2.41		95
11	1 	0.49		0.019
12	0 	0.49	-	0.019

Dimension in mm/inches



Notice

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(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CL ACCTI
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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Notice-PGA-E Rev.004

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