2.5V Drive Nch+SBD MOS FET QS5U16

Structure

Silicon N-channel MOSFET Schottky Barrier DIODE

● Features

- 1) The QS5U16 combines Nch MOSFET with a Schottky barrier diode in a single TSMT5 package.
- 2) Low on-state resistance with fast switching.
- 3) Low voltage drive (2.5V).
- 4) The Independently connected Schottky barrier diode has low forward voltage.

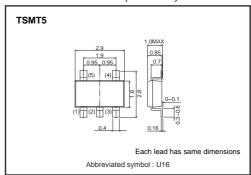
Applications

Load switch, DC / DC conversion

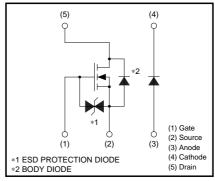
Packaging specifications

	Package	Taping
Type	Code	TR
	Basic ordering unit (pieces)	3000
QS5U16		0

●External dimensions (Unit : mm)



●Equivalent circuit



● Absolute maximum ratings (Ta=25°C)

<MOSFET>

1111001 = 17				
Parameter	Symbol	Limits	Unit	
Drain-source voltage	V _{DSS}	30	V	
Gate-source voltage		V _{GSS}	12	V
Drain current	Continuous	ID	±2.0	А
Drain current	Pulsed	I _{DP} *1	±8.0	Α
Source current	Continuous	Is	0.8	А
(Body diode)	Pulsed	I _{SP} *1	3.2	A
Channel temperature	Tch	150	°C	
Power dissipation	P _D *3	0.9	W/ELEMENT	
<di></di>				
Repetitive peak reverse vol	V _{RM}	30	V	
Reverse voltage		VR	20	V
Forward current	l _F	0.5	А	
Forward current surge peal	I _{FSM} *2	2.0	A	
Junction temperature	Tj	150	°C	
Power dissipation	P _D *3	0.7	W/ELEMENT	
<mosfet and="" di=""></mosfet>				
Total power dissipation	P _D *3	1.25	W / TOTAL	
Range of Storage temperat	Tstg	-55 to +150	°C	

^{*1} Pw≤10μs, Duty cycle≤1% *2 60Hz•1cyc. *3 Mounted on a ceramic board

●Electrical characteristics (Ta=25°C)

<MOSFET>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	-	10	μΑ	V _{GS} =12V / V _{DS} =0V
Drain-source breakdown voltage	V _(BR) DSS	30	_	_	V	I _D =1mA, / V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	-	-	1	μΑ	V _{DS} =30V / V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	0.5	_	1.5	V	V _{DS} =10V / I _D =1mA
Otatia dusia assuma as atata		-	71	100	mΩ	ID=2.0A, VGS=4.5V
Static drain-source on-state resistance	R _{DS (on)} *	-	76	107	mΩ	I _D =2.0A, V _{GS} =4V
resistance		-	110	154	mΩ	ID=2.0A, VGS=2.5V
Forward transfer admittance	Y _{fs} *	1.5	-	-	S	V _{DS} =10V, I _D =2.0A
Input capacitance	Ciss	-	175	-	pF	V _{DS} =10V
Output capacitance	Coss	-	50	_	pF	V _{GS} =0V
Reverse transfer capacitance	Crss	-	25	-	pF	f=1MHz
Turn-on delay time	td (on) *	-	8	_	ns	ID=1.0A
Rise time	tr *	ı	10	-	ns	VDD≒15V VGS=4.5V
Turn-off delay time	td (off) *	_	21	_	ns	R _L =15Ω
Fall time	t _f *	-	8	_	ns	R _G =10Ω
Total gate charge	Qg *	-	2.8	3.9	nC	V _{DD} ≒15V
Gate-source charge	Q _{gs} *	ı	0.6	-	nC	V _{GS} =4.5V
Gate-drain charge	Q _{gd} *	-	0.8	_	nC	I _D =2.0A

*Pulsed

<Body diode (source-drain)>

Forward voltage							
	Forward voltage	Vsp *	_	_	1.2	V	

* Pulsed

<Di>

Forward voltage	VF	_	_	0.36	V	I=0.1A
Forward voltage		_	_	0.47	V	I _F =0.5A
Reverse current	l _R	_	_	100	μA	V _R =20V



•Electrical characteristic curves

<MOSFET>

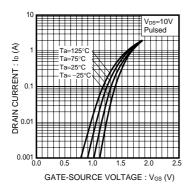


Fig.1 Typical Transfer Characteristics

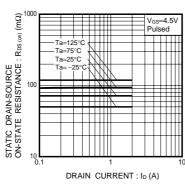


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current

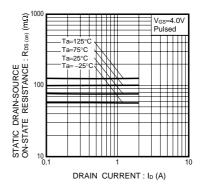


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

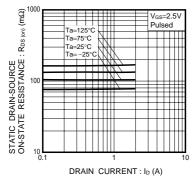


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

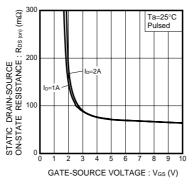


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

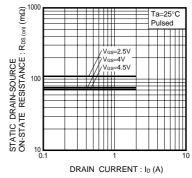


Fig.6 Static Drain-Source On-State Resistance vs. Drain Current

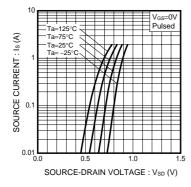


Fig.7 Reverse Drain Current vs. Source-Drain Current

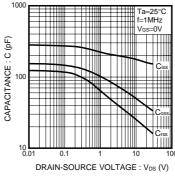


Fig.8 Typical Capacitance vs. Drain-Source Voltage

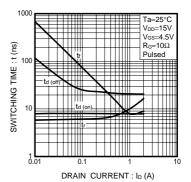


Fig.9 Switching Characteristics

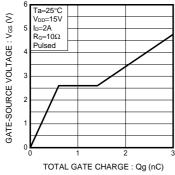


Fig.10 Dynamic Input Characteristics

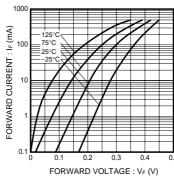


Fig.11 Forward Current vs. Forward Voltage

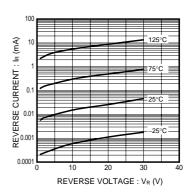


Fig.12 Reverse Current vs. Reverse Voltage

Measurement circuits

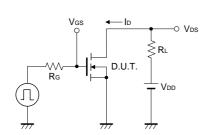


Fig.13 Switching Time Measurement Circuit

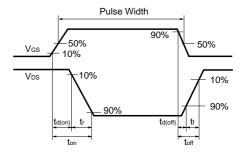


Fig.14 Switching Waveforms

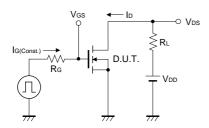


Fig.15 Gate Charge Measurement Circuit

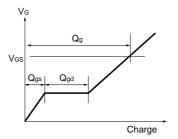


Fig.16 Gate Charge Waveform

Rev.A

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